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Price: ECU 28

Acts whose titles are printed in light type are those relating to day-to-day management of agricultural matters, and are generally valid for a limited period.

The titles of all other Acts are printed in bold type and preceded by an asterisk.

I

(Acts whose publication is obligatory)

COUNCIL REGULATION (EEC) No 1572/93**of 14 June 1993****temporarily suspending the autonomous Common Customs Tariff duties on a number of industrial products (in the microelectronics and related sectors)**

THE COUNCIL OF THE EUROPEAN COMMUNITIES,

Having regard to the Treaty establishing the European Economic Community, and in particular Article 28 thereof,

Having regard to the proposal from the Commission,

Whereas production of the products referred to in this Regulation is at present inadequate or non-existent within the Community and producers are thus unable to meet the needs of user-industries in the Community;

Whereas it is in the Community's interest in certain cases to suspend the autonomous Common Customs Tariff duties only partially, particularly because of the existence of Community production, and in other cases to suspend them completely;

Whereas suspension of these autonomous duties shall be decided by the Community;

Whereas, taking account of the difficulties involved in accurately assessing the development of the economic situation in the sectors concerned in the near future, these suspension measures should be taken only temporarily, by fixing their period of validity by reference to the interests of Community production,

HAS ADOPTED THIS REGULATION:

Article 1

The autonomous Common Customs Tariff duties for the products listed in the Annexes shall be suspended at the level indicated in respect of each of them.

These suspensions shall apply from:

- 1 July to 31 December 1993 for the products referred to in Table I,
- 1 July 1993 to 30 June 1994 for the products referred to in Table II.

Article 2

This Regulation shall enter into force on 1 July 1993.

This Regulation shall be binding in its entirety and directly applicable in all Member States.

Done at Luxembourg, 14 June 1993.

For the Council

The President

J. TRØJBORG

ANNEX

TABLE I

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8471 93 51	*40	Drive-unit for rewritable optical phase change discs with an external diameter of 13,34 cm (5,25 inch), for use in the manufacture of peripheral storage units, with a total storage capacity not exceeding 1 gigabyte (a)	0
ex 8471 93 51	*50	Drive-unit for magneto-optical discs with an external diameter of 13,34 cm (5,25 inch), for use in the manufacture of peripheral storage units, with a total storage capacity not exceeding 2 gigabytes (a)	0
ex 8471 93 60	*10	Twin reel drive-unit of the 8 mm cartridge type, for use in the manufacture of magnetic tape storage units (a)	0
ex 8471 99 90	*10	Optical reader for reading alphanumeric dot-matrix printing characters and converting them into electrical signals, comprising a read head containing an optical detector, an amplifier, a focusing lens and two lamps, linked by one or two flat cables to a central module the dimensions of which do not exceed 200 x 220 mm, comprising a printed circuit board on which are mounted a microprocessor, an image recognition circuit and an analogue-to-digital converter	0
ex 8473 30 10	*20	Processor of ECL technology, consisting of not more than 336 monolithic integrated circuits, each comprising not more than 15 000 programmable logic arrays, mounted on one or both sides of a multiple printed circuit, contained in a housing attached to a cooling plate or enclosed between two cooling plates, the overall exterior dimensions of which do not exceed 148 x 560 x 594 mm and bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 001B-3035-H002 52-203619 52-203621 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 10	*50	Assembly for disc storage units of Winchester technology, comprising a 2- or 4-channel read/write monolithic integrated circuit for magnetic head signals mounted with discrete components on a flexible printed circuit	0
ex 8473 30 10	*60	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, with a 32-bit external data-bus and a 32-bit external address-bus, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 48 x 48 mm, mounted on a printed circuit and with decoupling capacitors, with not more than 208 connections and bearing: — an identification marking consisting of or including the following combination of figures: 80386 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8473 30 10	*70	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising a bus controller and a memory controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 48 × 48 mm, and with decoupling capacitors, with not more than 293 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 390 Z 50 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 10	*80	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, comprising 2 random-access memories (RAMs) with a total storage capacity of 64 Kbits and a read only memory, non-programmable (ROM) with a storage capacity of 128 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 69 × 74 mm, and with decoupling capacitors, with not more than 325 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 320 C 40 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 90	*15	Metal ball coated with plastic or rubber material, with a diameter not exceeding 26 mm and a weight of 38 g or more, for use in the manufacture of a pointing device (so-called 'mouse') (a)	0
ex 8473 30 90	*20	Ferrite magnetic heads of Winchester technology, for hard disc file-peripherals as well as carrying arms equipped with such magnetic heads, capable of recording to a density of 10 tracks or more per mm	0
ex 8473 30 90	*25	Pointing device (so-called 'trackball'), the exterior dimensions of which do not exceed 17 × 27 × 47 mm, with a weight not exceeding 31 g, consisting of printed circuit on which are mounted an optical encoder in the form of a monolithic integrated circuit and a housing comprising a ball with a diameter not exceeding 19 mm and a retainer ring, for use in the manufacture of products falling within subheading 8471 20 20 (a)	0
ex 8473 30 90	*30	Read/write assembly for hard disc storage units, comprising ferrite magnetic heads of Winchester technology, mounted on a carrier arm, connected via a flexible printed circuit to a metal bracket upon which are mounted: — a flexible printed circuit upon which is mounted an amplifier for read/write signals, in the form of an integrated circuit, — a printed circuit socket	0
ex 8473 30 90	*45	Rotor arm without electronic components, for read/write assemblies of hard disk storage units	0
ex 8473 30 90	*55	Data storage assembly (Head/Disc/Assembly) for disc storage units, with a data transfer rate per second of 4,2 megabytes, comprising read/write heads and 9 rigid magnetic discs with an external diameter not exceeding 24,2 cm (9,5 inch) with a total storage capacity, formatted, of 2 838 megabytes, the whole incorporated in a single hermetically sealed housing	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8501 10 99	*53	DC motor, brushless, with a typical running torque of 0,0003 Nm, with a coupling flange of a diameter of 6,8 mm ($\pm 0,1$ mm) and a chuck of a diameter of 12 mm (± 1 mm), with an external rotor, a three-phase winding, a rated speed of 4 800 (± 1 %) rpm and a supply voltage of 4 V (± 25 %)	0
ex 8501 10 99	*54	DC motor, with brushes, with a typical running torque of 0,0015 Nm ($\pm 0,0005$ Nm), with a shaft of a diameter of 2 mm ($\pm 0,004$ mm), with an internal rotor, a rated speed of 2 050 (± 11 %) rpm and a supply voltage of 2,5 V (± 60 % ou -60 % / $+140$ %)	0
ex 8501 10 99	*55	DC motor, with brushes, with a typical running torque of 0,0058 Nm ($\pm 0,001$ Nm), with a chuck of a diameter of 2 mm ($\pm 0,004$ mm), with an internal rotor, a rated speed of 2 270 (± 10 %) rpm and a supply voltage of 4,5 V (-67 % / $+100$ %)	0
ex 8504 40 98	*10	Direct current to direct current converter, with diodes not in cascade connection, with an input voltage range not exceeding 72 V, contained in a housing with not more than 16 connections	0
ex 8518 30 90	*10	Headphone and earphone for hearing aids, contained in a housing the exterior dimensions of which, excluding connecting points, do not exceed $5 \times 6 \times 8$ mm	0
ex 8522 90 91	*92	Electronic assembly for a laser read-head of a compact disc player, comprising: — a flexible printed circuit, — a photo-detector, in the form of a monolithic integrated circuit, contained in a housing, — 2 connectors, — not more than 1 transistor, — not more than 3 variable and 4 fixed resistors, — not more than 5 capacitors, the whole mounted on a support	0
ex 8522 90 99	*92	Thinfilm recording/reproducing device fixed on a non-magnetic substrate, having 9 parallel channels for digital signals and 2 channels for analogue signals, for use in the manufacture of magnetic heads for digital sound recording and digital/analogue sound reproducing apparatus of the cassette-type (a)	0
ex 8528 20 20	*10	Video monitor comprising: — a flat screen monochrome cathode-ray tube with a diagonal measurement of the screen of 100 mm or more but not exceeding 110 mm and equipped with a deflector yoke, and — a printed circuit on which are mounted a deflection unit, a video-amplifier and a transformer, the whole mounted on a chassis, for the manufacture of video entry-phones (a)	0
ex 8529 90 98	*93	Voltage-controlled oscillator, having an oscillation-frequency of 915 MHz or more at 4,5 V and not exceeding 890 MHz at 1 V, contained in a housing the exterior dimensions of which do not exceed $5 \times 10 \times 15$ mm, with not more than 4 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: VW995S05 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8529 90 98	*94	Assembly consisting of a lens unit, having an adjustable focal length of 5,7 mm or more but not exceeding 65 mm and comprising a zoom encoder, a stepping motor unit, a zoom motor unit, an iris motor unit and a photo interrupter	0
ex 8529 90 98	*95	Video recording and reproducing assembly, comprising a tape deck mechanism of the cassette type, comprising a DC motor, for use in the manufacture of products falling within heading 8525 (a)	0
ex 8529 90 98	*96	Assembly comprising a lens unit, having a focal length of 3,6 mm, an interline charge-coupled image sensor having 291 000 photosensitive cells, and integrated circuits, the whole mounted on 2 printed circuits linked via a cable	0
ex 8529 90 98	*97	Assembly consisting of a monochrome cathode-ray tube with a diagonal measurement of the screen of 165 mm or more but not exceeding 230 mm and a concave focus lens mounted on a liquid-filled cooling armature, for use in the manufacture of television projection equipment (a)	0
ex 8531 20 90	*20	Liquid crystal display (LCD) with an active matrix and 640 × 400 pixels (monochrome) or 640 × 480 pixels (colour), consisting of a layer of liquid crystals between two glass sheets or plates, mounted on a printed circuit board comprising electronic components providing drive and/or control functions	0
ex 8531 20 90	*30	Passive liquid crystal display (LCD), consisting of a layer of liquid crystals between two glass sheets or plates with 256 000 dots or more, mounted on a printed circuit board comprising electronic components providing drive and/or control functions	0
ex 8533 40 10	*91	Potentiometer consisting of carbon resistors with a total resistance of 20 Kohm or more but not exceeding 250 Kohm, fitted with a DC motor having a running torque of 0,01 Nm or more but not exceeding 0,045 Nm and an operating current of 150 mA at 4,5 V	0
ex 8534 00 19	*94	Printed circuit, consisting of 31 conductor elements fixed on a flexible plastic film, for use in the manufacture of magnetic heads for digital sound recording and digital/analogue sound reproducing apparatus of the cassette-type (a)	0
ex 8534 00 19	*95	Printed circuit, consisting of conductor elements fixed on a flexible plastic film, with a trace width of 0,095 mm or more but not exceeding 0,155 mm and a trace pitch of 0,095 mm or more but not exceeding 0,305 mm, for use in the manufacture of electronic calculating machines (a)	0
ex 8534 00 90	*93	Printed circuit on one or both sides of a ceramic substrate, consisting of conductor elements, contacts and resistors, incorporating connections isolated in vitrified layers, the dimensions of which do not exceed 45 × 45 mm, with not more than 550 connections	0
ex 8536 50 19	*92	Membrane switch on a multilayer flexible plastic film, consisting of a layer with silver conductor patterns, a layer of graphite conductor patterns, a layer with a dielectric pattern, a layer with a pattern of silver interconnections (jumpers) and a layer with a solder pattern, for use in the manufacture of electronic calculating machines (a)	0
ex 8536 90 80	*91	Elastomeric connectors, consisting of conductor elements coated with gold and fixed on a substrate of rubber	0
ex 8540 91 00	*92	Slit mask, consisting of vertical slits with a distance between slits of 0,78 mm (± 0,08 mm) and a diagonal dimension of either 61,5 cm (± 0,5 cm) or 71 cm (± 0,5 cm) or 79,5 cm (± 0,5 cm)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8541 60 00	*93	Piezo-electric crystal oscillating at a frequency of 4 MHz or more but not exceeding 60 MHz, contained in a housing and with not more than 4 connections	0
ex 8542 11 01	*40	Wafer, not yet cut into chips, consisting only of microprocessors with an arithmetic-logic unit (ALU) with a processing capacity of 16 bits and an 8-bit data-bus, comprising a random-access memory (RAM) with a storage capacity not exceeding 2 Kbits but not comprising a read only memory, non-programmable (ROM), for the manufacture of goods of subheading 8542 11 64 contained in a housing the exterior dimensions of which do not exceed 18 x 59 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 78C10 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*60	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers with a processing capacity of 4 bits, for the manufacture of goods of subheading 8542 11 71 contained in a housing the exterior dimensions of which do not exceed 24 x 59 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 7507 75006 75216 75306 75316 75P216 7508 75108 75217 75308 75512 75P308 75004 75116 75304 75312 75516 75P316 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*70	Wafer, not yet cut into chips, consisting only of display controllers and character generators (DCCG), for liquid-crystal dot-matrix display system, for the manufacture of goods of subheading 8542 11 82 contained in a housing the exterior dimensions of which do not exceed 24 x 26 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 7228	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 16	*01	Pseudo-static random-access memory of C-MOS technology (C-MOS PS-RAM), with a storage capacity of 4 Mbits and an access time not exceeding 100 ns, comprising a timing pulse generator and a refresh control circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 22 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: HM 658512 - or — other identification markings relating to devices complying with the abovementioned description These devices are for the manufacture of portable computers, capable of operating without an external source of power (a)	0
ex 8542 11 16	*02	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 256 K × 16 bits and an access time not exceeding 80 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 27 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MT 4C16256 PD 424260 PD 424170 - or — other identification markings relating to devices complying with the abovementioned description	10
ex 8542 11 16	*03	Dual port dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with data registers and a serial read output control, with a storage capacity of 256 K × 16 bits and an access time not exceeding 80 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 27 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MT 42C256 TMS 55160 TMS 55165 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 16	*04	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 240 K × 12 bits, with separate in- and outputs and serial shift registers (so-called field memories), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 19 mm, with not more than 36 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TMS 4C2970 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)								
ex 8542 11 18	*01	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 256 K × 18 bits and an access time not exceeding 80 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 27 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: PD 424280 - or — other identification markings relating to devices complying with the abovementioned description	10								
ex 8542 11 18	*02	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 512 K × 9 bits and an access time not exceeding 100 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 19 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TMS 44900 - or — other identification markings relating to devices complying with the abovementioned description	10								
ex 8542 11 21	*12	Static random-access memory (S-RAM) with a storage capacity not exceeding 4 Kbit, superimposed bit-for-bit on an electrically erasable, programmable, read only memory (E²PROM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table> <tr> <td>X 2001</td><td>X 2004</td><td>X 2210</td><td>X 2443</td></tr> <tr> <td>X 2002</td><td>X 2201 A</td><td>X 2212</td><td>X 2444</td></tr> </table> - or — other identification markings relating to devices complying with the abovementioned description	X 2001	X 2004	X 2210	X 2443	X 2002	X 2201 A	X 2212	X 2444	0
X 2001	X 2004	X 2210	X 2443								
X 2002	X 2201 A	X 2212	X 2444								
ex 8542 11 21	*13	Static random-access memory (S-RAM) with a storage capacity of 16 Kbits, superimposed bit-for-bit on an electrically erasable, programmable, read only memory (E²PROM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 43 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: X 20C16 - or — other identification markings relating to devices complying with the abovementioned description	0								
ex 8542 11 21	*37	Non-volatile static random access memory of C-MOS technology (C-MOS non-volatile S-RAM), with a capacity of 16 Kbits and an internal energy source, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 40 mm, with not more than 28 connections and bearing:									

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 21 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: DS 1220 Y MK 48 Z 02 - or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 11 21	*42	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 32 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 63 mm, with not more than 48 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: IDT 7134 TC 5532 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*21	Non-volatile static random-access memory of C-MOS technology (C-MOS non-volatile S-RAM), with a storage capacity of 256 Kbits and an internal energy source, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 40 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BQ 4011 DS 1230 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*24	Static random-access memory of BiMOS technology (BiMOS S-RAM), with a storage capacity of 256 Kbits and an access time not exceeding 20 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 21 × 38 mm, with not more than 52 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7B153 CY7B155 HM 6709 A MCM 6709 TC 55B464 CY7B154 CY7B156 MCM 6708 TC 55B328 TC 55B465 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 25	*01	Static random-access memory of BiMOS technology (BiMOS S-RAM), with a storage capacity of 288 Kbits and an access time not exceeding 40 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 20 × 41 mm, with not more than 52 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7B173 CY7B174 PD 46730 TC 55B329 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)																				
ex 8542 11 25	*08	Static random-access cache memory of BiMOS technology (BiMOS S-Cache-RAM), with a total storage capacity of 320 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26 × 26 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combination of figures and letters: PD 46710 PD 46741 or — other identification markings relating to devices complying with the abovementioned description	0																				
ex 8542 11 25	*10	Static random-access memory (S-RAM), with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 47 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table> <tr> <td>CXK581020</td><td>CY7C108</td><td>HM 624257</td><td>TC 551001</td></tr> <tr> <td>CY7C101</td><td>CY7C109</td><td>M5M 51004</td><td>TC 55B4256</td></tr> <tr> <td>CY7C102</td><td>EDI 88128</td><td>M5M 51008</td><td>TC 55B4257</td></tr> <tr> <td>CY7C106</td><td>HM 621100A</td><td>MCM 6228</td><td>TC 55B8128</td></tr> <tr> <td>CY7C107</td><td>HM 624256</td><td>MT 5C1008</td><td></td></tr> </table> or — other identification markings relating to devices complying with the abovementioned description	CXK581020	CY7C108	HM 624257	TC 551001	CY7C101	CY7C109	M5M 51004	TC 55B4256	CY7C102	EDI 88128	M5M 51008	TC 55B4257	CY7C106	HM 621100A	MCM 6228	TC 55B8128	CY7C107	HM 624256	MT 5C1008		0
CXK581020	CY7C108	HM 624257	TC 551001																				
CY7C101	CY7C109	M5M 51004	TC 55B4256																				
CY7C102	EDI 88128	M5M 51008	TC 55B4257																				
CY7C106	HM 621100A	MCM 6228	TC 55B8128																				
CY7C107	HM 624256	MT 5C1008																					
ex 8542 11 25	*12	Non-volatile static random-access memory of C-MOS technology (C-MOS non-volatile S-RAM), with a storage capacity of 1 Mbit and an internal energy source, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 44 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: BQ 4013 or — other identification markings relating to devices complying with the abovementioned description	0																				
ex 8542 11 27	*03	Non-volatile static random-access memory of C-MOS technology (C-MOS non-volatile S-RAM), with a storage capacity exceeding 1 Mbit, with an internal energy source, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 54 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BQ 4014 BQ 4015 BQ 4024 BQ 4025 or — other identification markings relating to devices complying with the abovementioned description	0																				
ex 8542 11 31	*02	Read only memory, non-programmable of C-MOS technology (C-MOS ROM), with a storage capacity exceeding 1 Mbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 20 × 56 mm, with not more than 64 connections and bearing:																					

CN code	TARIC	Description	Rate of autonomous duty (%)																
ex 8542 11 31 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>HN 62334</td><td>LH 534P00</td><td>MB 834 200</td><td>PD 23C16000</td></tr><tr><td>HN 62434</td><td>MB 831620</td><td>MB 838000</td><td>TC 5316200</td></tr><tr><td>LH 534500</td><td>MB 834000</td><td>MB 838200</td><td>TC 534 200</td></tr><tr><td>LH 534K00</td><td>MB 834100</td><td>PD 23C8000</td><td>TC 538200</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	HN 62334	LH 534P00	MB 834 200	PD 23C16000	HN 62434	MB 831620	MB 838000	TC 5316200	LH 534500	MB 834000	MB 838200	TC 534 200	LH 534K00	MB 834100	PD 23C8000	TC 538200	0
HN 62334	LH 534P00	MB 834 200	PD 23C16000																
HN 62434	MB 831620	MB 838000	TC 5316200																
LH 534500	MB 834000	MB 838200	TC 534 200																
LH 534K00	MB 834100	PD 23C8000	TC 538200																
ex 8542 11 42	*02	UV erasable, programmable, read only memory (EPROM) of N-MOS (including H-MOS) technology (N/H-MOS EPROM), with a storage capacity not exceeding 256 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14×39 mm, with a quartz window on the upper surface and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>M 2716</td><td>M 2732 A</td><td>M 2764</td><td>M 27128M 27256</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	M 2716	M 2732 A	M 2764	M 27128M 27256	0												
M 2716	M 2732 A	M 2764	M 27128M 27256																
ex 8542 11 42	*03	Flash electrically erasable, programmable, read only memory (Flash-E²PROM) with a storage capacity of 16 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9×14 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TMS 29 F 816 or — other identification markings relating to devices complying with the abovementioned description	0																
ex 8542 11 42 ex 8542 11 59	*21 *61	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 256 Kbits and an access time of less than 80 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×39 mm, with not more than 32 connections, with or without a quartz window on the upper surface and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>CY 7C 271</td><td>CY 7C 274</td><td>CY 7C 277</td><td>CY 7C 279</td><td>XC 17256</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	CY 7C 271	CY 7C 274	CY 7C 277	CY 7C 279	XC 17256	0											
CY 7C 271	CY 7C 274	CY 7C 277	CY 7C 279	XC 17256															
ex 8542 11 44	*02	UV erasable, programmable, read only memory (EPROM) of N-MOS (including H-MOS) technology (N/H-MOS EPROM), with a storage capacity of 512 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14×39 mm, with a quartz window on the upper surface and bearing: — an identification marking consisting of or including the following combination of figures and letters: M 27512 or — other identification markings relating to devices complying with the abovementioned description	0																

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 44 ex 8542 11 59	*03 *01	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM) with a storage capacity of 1 Mbit, comprising address and data latches, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 38 mm, with not more than 32 connections, with or without a quartz window on the upper surface and bearing : — an identification marking consisting of or including one of the following combinations of figures and letters: TMS 87C110 TMS 87PC110 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 44 ex 8542 11 59	*04 *04	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 512 Kbits, comprising address and data latches, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 38 mm, with not more than 32 connections, with or without a quartz window on the upper surface and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMS 87C510 TMS 87PC510 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 44	*06	Flash electrically erasable, programmable, read only memory (Flash-E²PROM), with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 48 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 28 F 010 28 F 210 48 F 010 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 46	*01	Flash electrically erasable, programmable, read only memory (Flash-E²PROM) with a storage capacity of 2 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 42 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 28 F 020 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 48 ex 8542 11 59	*02 *67	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 16 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 56 mm, with not more than 42 connections, with or without a quartz window on the upper surface and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 48 ex 8542 11 59 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: TC 5716200 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 50	*01	Electrically erasable, programmable, read only memory (E²PROM), with a storage capacity of 256 Kbits or more, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 52 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 28256 28 C 512 AT 28C1024 28 C 256 48 C 256 E/M 28C010 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 50	*06	Electrically erasable, programmable, read only memory (E²PROM), with a storage capacity of 8 K × 8 bits and a byte or page write cycle time not exceeding 2 ms, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AT 28 C 64 AT 28 PC 64 AT 28 HC 64 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*02	Double row buffer memory, with shift registers and random access memories (RAMs), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CRT 9212 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*03	Write buffer memory of C-MOS technology, with an organisation of 4 × 16 bits comprising eight bits of address and eight bits of data, and four-bit parity, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 79 R 2020 A 79 R 3020 R 2020/16 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 59	*05	Programmable, non-erasable, read only memory (PROM) of C-MOS technology (C-MOS PROM), with a storage capacity of 18 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10×11 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: XC 1718 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*27	FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signal or bidirectional operation, with a storage capacity of 18 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13×43 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C439 CY7C443 CY7C453 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 62	*02	Microprocessor of C-MOS technology, with a processing capacity of 8 bits, capable of controlling a compact disc player, providing digital servo-command control, comprising a random-access memory (RAM) with a storage capacity of 32 Kbits, a phase-locked loop circuit, an oversampling filter, a servo-command generator, an error correction circuit, an analogue-to-digital converter, a demodulator and a central processing unit (CPU) interface circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15×21 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters : CXD 2515Q or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 62	*03	Floating-point arithmetic co-processor, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 46×53 mm, with not more than 208 connections and bearing:	
ex 8542 11 64	*03		
ex 8542 11 67	*03		
ex 8542 11 68	*01	— an identification marking consisting of or including one of the following combinations of figures or figures and letters: 3171 80387 MC 68882 TX 32081 W 74 ACT 8847 8087 NCR 32020 WTL 3167 79 R 2010 80C287 NS 32081 79 R 3010 CY 7C 602 NS 32381 80287 MC 68881 R 2010/16 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)																
ex 8542 11 62.	*04	Microprocessor of C-MOS technology, with a processing capacity of 8 bits, comprising a 16-bit digital signal processor, an 8-bit arithmetic-logic unit (ALU), a random-access memory (RAM) with a storage capacity of 4 Kbits and having the function of programme memory, 2 random-access memories (RAMs) with a total storage capacity of 2 Kbits and 256 registers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30×30 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: Z 86C95 or — other identification markings relating to devices complying with the abovementioned description	0																
ex 8542 11 67	*02	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising three 16-bit data-buses, a 16×16 bits multiplier with 32 bits results, 2 or 3 random-access memories (RAMs) with a total storage capacity of 5 or 8 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 40×64 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters : ST 18931 ST 18932 ST 18941 ST 18R942 or — other identification markings relating to devices complying with the abovementioned description	0																
ex 8542 11 67	*04	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising an integer unit with 136 registers and a floating point unit with 32 registers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 46×46 mm, with not more than 207 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: W 8701 or — other identification markings relating to devices complying with the abovementioned description	0																
ex 8542 11 67	*35	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, with a 32-bit external data-bus and a 32-bit external address-bus, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 48×48 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: <table> <tr> <td>486</td><td>79 R 3000</td><td>MC 68020</td><td>NS 32C 032</td></tr> <tr> <td>80386</td><td>AM 29000</td><td>MC 68030</td><td>R 2000/16</td></tr> <tr> <td>80486</td><td>CY7C601</td><td>MC 68040</td><td></td></tr> <tr> <td>79 R 2000 A</td><td>L 64801</td><td>NS 32532</td><td></td></tr> </table> or — other identification markings relating to devices complying with the abovementioned description	486	79 R 3000	MC 68020	NS 32C 032	80386	AM 29000	MC 68030	R 2000/16	80486	CY7C601	MC 68040		79 R 2000 A	L 64801	NS 32532		0
486	79 R 3000	MC 68020	NS 32C 032																
80386	AM 29000	MC 68030	R 2000/16																
80486	CY7C601	MC 68040																	
79 R 2000 A	L 64801	NS 32532																	

CN code	TARIC	Description	Rate of autonomous duty (%)																																																						
ex 8542 11 67	*37	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising a bus controller and a memory controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 48 × 48 mm, with not more than 293 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 390 Z 50 80386 SL or — other identification markings relating to devices complying with the abovementioned description	0																																																						
ex 8542 11 68	*31	Microprocessor of C-MOS technology, with a processing capacity of 64 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 62 × 62 mm, with not more than 431 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 21064 80860 or — other identification markings relating to devices complying with the abovementioned description	0																																																						
ex 8542 11 71 ex 8542 11 91	*01 *03	Microcontroller or microcomputer with a processing capacity of 4 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 22 × 60 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>CD 3200 – 3299</td><td>MN 1584531</td><td>TMP 47 C 1670</td></tr><tr><td>HD 38800</td><td>PD 17203A</td><td>TMP 47 C 200</td></tr><tr><td>HD 38820</td><td>PD 17204</td><td>TMP 47 C 220</td></tr><tr><td>HD 404189</td><td>SMC 6214</td><td>TMP 47 C 221</td></tr><tr><td>HD 404019</td><td>SMC 6215</td><td>TMP 47 C 452</td></tr><tr><td>HD 404608</td><td>SMC 6234</td><td>TMP 47 C 456</td></tr><tr><td>HD 404919</td><td>SMC 6266</td><td>TMP 47 C 670</td></tr><tr><td>HD 4074019</td><td>SMC 62 L 34</td><td>TMP 47 C 855</td></tr><tr><td>HD 4074608</td><td>T 6978</td><td>TMP 47 C 858</td></tr><tr><td>HD 44750</td><td>T 7767 BS</td><td>TMP 47 P 1670</td></tr><tr><td>HD 44796</td><td>TCM 8301</td><td>TMP 47 P 855</td></tr><tr><td>HD 44 800</td><td>TCM 8302</td><td>TMP 47 P 860 E</td></tr><tr><td>HD 44801</td><td>TMC 0270 – 0279</td><td>TP 0310 – 03299</td></tr><tr><td>HD 44820</td><td>TMC 0500 – 0599</td><td>TP 0450 – 04599</td></tr><tr><td>HD 44840</td><td>TMC 0980 – 0989</td><td>TP 0480 – 04899</td></tr><tr><td>HD 44860</td><td>TMC 1500 – 1599</td><td>TP 0500 – 05999</td></tr><tr><td>HD 614042</td><td>TMC 17</td><td>TSS 200</td></tr><tr><td>HD 614080</td><td>TMC 1980 – 1999</td><td>TSS 400</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	CD 3200 – 3299	MN 1584531	TMP 47 C 1670	HD 38800	PD 17203A	TMP 47 C 200	HD 38820	PD 17204	TMP 47 C 220	HD 404189	SMC 6214	TMP 47 C 221	HD 404019	SMC 6215	TMP 47 C 452	HD 404608	SMC 6234	TMP 47 C 456	HD 404919	SMC 6266	TMP 47 C 670	HD 4074019	SMC 62 L 34	TMP 47 C 855	HD 4074608	T 6978	TMP 47 C 858	HD 44750	T 7767 BS	TMP 47 P 1670	HD 44796	TCM 8301	TMP 47 P 855	HD 44 800	TCM 8302	TMP 47 P 860 E	HD 44801	TMC 0270 – 0279	TP 0310 – 03299	HD 44820	TMC 0500 – 0599	TP 0450 – 04599	HD 44840	TMC 0980 – 0989	TP 0480 – 04899	HD 44860	TMC 1500 – 1599	TP 0500 – 05999	HD 614042	TMC 17	TSS 200	HD 614080	TMC 1980 – 1999	TSS 400	0
CD 3200 – 3299	MN 1584531	TMP 47 C 1670																																																							
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HD 614042	TMC 17	TSS 200																																																							
HD 614080	TMC 1980 – 1999	TSS 400																																																							
ex 8542 11 71	*08	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 160 Kbits, a random-access memory (RAM) with a storage capacity of 4 992 bits, a random-access memory (RAM) with a storage capacity of 640 bits, an 8-channel analogue-to-digital converter, a serial interface circuit with a FIFO (first in,first out) read/write memory, 4 timers, a display control and driver circuit, and a clockgenerator, in the form of a monolithic intergrated circuit, contained in a housing the exterior dimensions of which do not exceed 15×21 mm, with not more than 80 connections and bearing:																																																							

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: CXP 82320 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*09	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing control and interface functions for a keyboard, comprising of a read only memory, non-programmable (ROM) with a storage capacity of 64 Kbits, a random-access memory (RAM) with a storage capacity of 2 Kbits and an analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 23 × 23 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 80C51 SL - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*10	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing remote control functions, comprising a read only memory, non-programmable (ROM) with a storage capacity of 8 Kbits, a random-access memory (RAM) with a storage capacity of 256 bits, an oscillator and a modulator for generating pulse trains, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 16 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: PCA 84C122 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*11	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, for text data decoding and display, comprising a read only memory, non-programmable (ROM) with a storage capacity of 8 Kbits, a read only memory, non-programmable (ROM) with 120 character fonts and a random-access memory (RAM) with a storage capacity not exceeding 2 304 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 59 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CF 72307 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*28	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a random-access memory (RAM) with a storage capacity of 2 Kbits, a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 64 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 52 × 63 mm, with not more than 68 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: 77 C 82 80 C 152 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*31	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a random-access memory (RAM) with a storage capacity of 3 Kbits, either a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or an UV erasable, programmable, read only memory (EPROM), with a storage capacity of 128 Kbits, an analogue-to-digital converter and a digital-to-analogue converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 58 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: M 37450E8 M 37450M8 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*35	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 32 Kbits, a random-access memory (RAM) with a storage capacity of 1 Kbit, an 8-channel analogue-to-digital converter, two 16-bit timers each with two 16-bit registers and a serial synchronous communications interface consisting of an 8-bit serial shift register with serial data input, serial data output and serial shift clock, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: COP 884CF COP 888CF - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*36	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 32 Kbits, a random-access memory (RAM) with a storage capacity of 1,5 Kbits, a full duplex universal asynchronous receiver/transmitter (UART), 2 analogue comparators, three 16-bit timers each with two 16-bit registers, and a serial synchronous communications interface consisting of an 8-bit serial shift register with serial data input, serial data output and serial shift clock, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: COP 888CG - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71	*37	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 64 Kbits, a random-access memory (RAM) with a storage capacity of 2 Kbits, a full duplex universal asynchronous receiver/transmitter (UART), 2 analogue comparators, three 16-bit timers each with two 16-bit registers, and a serial synchronous communications interface consisting of an 8-bit serial shift register with serial data input, serial data output and serial shift clock, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: COP 888EG or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*38	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 32 Kbits, a random-access memory (RAM) with a storage capacity of 1 Kbit, a 16-bit timer with a 16-bit register, and a serial synchronous communication interface consisting of an 8-bit serial shift register with serial data input, serial data output and serial shift clock, and in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: COP 880C COP 881C or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*40	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing communication and control functions in local operating networks (LON), comprising three 8-bit central processing units (CPUs), a static random-access memory (S-RAM) with a storage capacity of 16 Kbits and an electrically erasable, programmable, read only memory (E²PROM) with a storage capacity of 4 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 15 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 143150 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*41	Microcontroller or microcomputer of C-MOS-technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 8 or 16 Kbits, a random-access memory (RAM) with a storage capacity of 512 bits or 1 Kbit and a synchronous serial communications interface circuit consisting of an 8-bit serial shift register with a serial data input, a serial data output and a serial shift clock, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 38 mm, with not more than 28 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: COP 820 COP 840 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 73	*03	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, having the function of charge control of nickel-cadmium batteries, comprising a read only memory, non-programmable (ROM) with a storage capacity of 42 000 bits, a read only memory, non-programmable (ROM) with a storage capacity of 1 Kbit, a random-access memory (RAM) with a storage capacity of 1 Kbit and a 10-bit analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 20 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: ICS 1700 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 73	*05	Microcontroller or microcomputer with a processing capacity of 16 bits, comprising a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity not exceeding 256 Kbits, a random-access memory (RAM) with a storage capacity not exceeding 12 Kbits, an analogue-to-digital converter with sample/hold, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 40 × 40 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 8396 8397 8796 83C196 83C198 87C196 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 73	*08	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 128 or 256 Kbits, a random-access memory (RAM) with a storage capacity of 4 or 16 Kbits and an 8-bit analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 58 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: M 37702 E2 M 37702 E4 M 37702 M2 M 37702 M4 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 74	*02	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, comprising three 16-bit data buses, a 16×16 bits multiplier with 32 bits results, 2 read only memories, non-programmable (ROMs) with a total storage capacity of 104 or 136 Kbits and random-access memories (RAMs) with a total storage capacity of 5, 8 or 10 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 40×64 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ST 18930 ST 18940 ST 18942 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 74	*03	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 24 bits, capable of 2-channel digital audio signal processing, providing reverberation and equaliser functions, comprising a read only memory, non-programmable (ROM) with a storage capacity of 192 bits, a random-access memory (RAM) with a storage capacity of 344 bits, an instruction register, an accumulator register, 2 multiplication registers, 2 sequence registers, two 16-bit input registers, two 16-bit output registers and 2 filters, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15×15 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters : CXD 2701Q - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 74	*11	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, comprising 2 random-access memories (RAMs) with a total storage capacity of 64 Kbits and a read only memory, non-programmable (ROM) with a storage capacity of 128 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 69×74 mm, with not more than 325 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 320 C 30 320 C 40 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 77	*02	Gate array of C-MOS technology, comprising at least 234 916 gates and at least 416 in-/outputs, in the form of a monolithic integrated circuit, either contained in a housing the exterior dimensions of which do not exceed 62×62 mm or fixed on a plastic support, with not more than 512 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LCA 100235 TGC 1245 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)																																																																											
ex 8542 11 77	*03	Gate array of C-MOS technology, with 169 360 gates and 304 in-/outputs, comprising buffers capable of driving 48 mA outputs, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15×21 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including the following combination of figures: 0390779 or — other identification markings relating to devices complying with the abovementioned description	7																																																																											
ex 8542 11 80	*06	Programmable logic device, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 85×124 mm, with or without a quartz window on the upper surface, with not more than 300 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>16 L8-W</td><td>C 16 R 6</td><td>CY7C345</td><td>EPM 7096</td><td>XC 3020</td></tr><tr><td>16 P 8</td><td>C 16 R 8</td><td>CY7C361</td><td>EPM 7128</td><td>XC 3030</td></tr><tr><td>16 R4-W</td><td>Ç 20 G 10</td><td>EP 600</td><td>EPM 7160</td><td>XC 3042</td></tr><tr><td>16 R6-W</td><td>C 22 V 10</td><td>EP 610</td><td>EPM 7192</td><td>XC 3064</td></tr><tr><td>16 R8-W</td><td>CY7B326</td><td>EP 630</td><td>EPM 7256</td><td>XC 3090</td></tr><tr><td>16 RP 4</td><td>CY7B336</td><td>EP 910</td><td>EPM 7320</td><td>XC 4002</td></tr><tr><td>22 V10-W</td><td>CY7B337</td><td>EP 1800</td><td>EPM 7384</td><td>XC 4003</td></tr><tr><td>A 1010</td><td>CY7B338</td><td>EP 1810</td><td>EPM 7512</td><td>XC 4004</td></tr><tr><td>A 1020</td><td>CY7B339</td><td>EP 1830</td><td>EPM 7768</td><td>XC 4005</td></tr><tr><td>A 1225</td><td>CY7C330</td><td>EPM 5032</td><td>EPM 71024</td><td>XC 4006</td></tr><tr><td>A 1240</td><td>CY7C331</td><td>EPM 5064</td><td>TPC 1225</td><td>XC 4008</td></tr><tr><td>A 1280</td><td>CY7C332</td><td>EPM 5128</td><td>TPC 1240</td><td>XC 4010</td></tr><tr><td>ATT 3090</td><td>CY7C342</td><td>EPM 5130</td><td>TPC 1280</td><td>XC 4013</td></tr><tr><td>C 16 L 8</td><td>CY7C343</td><td>EPM 5192</td><td>XC 2018</td><td>XC 4016</td></tr><tr><td>C 16 R 4</td><td>CY7C344</td><td>EPM 7032</td><td>XC 2064</td><td>XC 4020</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	16 L8-W	C 16 R 6	CY7C345	EPM 7096	XC 3020	16 P 8	C 16 R 8	CY7C361	EPM 7128	XC 3030	16 R4-W	Ç 20 G 10	EP 600	EPM 7160	XC 3042	16 R6-W	C 22 V 10	EP 610	EPM 7192	XC 3064	16 R8-W	CY7B326	EP 630	EPM 7256	XC 3090	16 RP 4	CY7B336	EP 910	EPM 7320	XC 4002	22 V10-W	CY7B337	EP 1800	EPM 7384	XC 4003	A 1010	CY7B338	EP 1810	EPM 7512	XC 4004	A 1020	CY7B339	EP 1830	EPM 7768	XC 4005	A 1225	CY7C330	EPM 5032	EPM 71024	XC 4006	A 1240	CY7C331	EPM 5064	TPC 1225	XC 4008	A 1280	CY7C332	EPM 5128	TPC 1240	XC 4010	ATT 3090	CY7C342	EPM 5130	TPC 1280	XC 4013	C 16 L 8	CY7C343	EPM 5192	XC 2018	XC 4016	C 16 R 4	CY7C344	EPM 7032	XC 2064	XC 4020	0
16 L8-W	C 16 R 6	CY7C345	EPM 7096	XC 3020																																																																										
16 P 8	C 16 R 8	CY7C361	EPM 7128	XC 3030																																																																										
16 R4-W	Ç 20 G 10	EP 600	EPM 7160	XC 3042																																																																										
16 R6-W	C 22 V 10	EP 610	EPM 7192	XC 3064																																																																										
16 R8-W	CY7B326	EP 630	EPM 7256	XC 3090																																																																										
16 RP 4	CY7B336	EP 910	EPM 7320	XC 4002																																																																										
22 V10-W	CY7B337	EP 1800	EPM 7384	XC 4003																																																																										
A 1010	CY7B338	EP 1810	EPM 7512	XC 4004																																																																										
A 1020	CY7B339	EP 1830	EPM 7768	XC 4005																																																																										
A 1225	CY7C330	EPM 5032	EPM 71024	XC 4006																																																																										
A 1240	CY7C331	EPM 5064	TPC 1225	XC 4008																																																																										
A 1280	CY7C332	EPM 5128	TPC 1240	XC 4010																																																																										
ATT 3090	CY7C342	EPM 5130	TPC 1280	XC 4013																																																																										
C 16 L 8	CY7C343	EPM 5192	XC 2018	XC 4016																																																																										
C 16 R 4	CY7C344	EPM 7032	XC 2064	XC 4020																																																																										
ex 8542 11 82 ex 8542 11 95	*01 *31	Analogue-digital monolithic integrated circuit, capable of controlling brushless motors and keeping their speed constant, contained in a housing the exterior dimensions of which do not exceed 9×25 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>32 M 590</td><td>UC 1633</td><td>UC 3633</td></tr><tr><td>MGA 3015 A</td><td>UC 1634</td><td>UC 3634</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	32 M 590	UC 1633	UC 3633	MGA 3015 A	UC 1634	UC 3634	0																																																																					
32 M 590	UC 1633	UC 3633																																																																												
MGA 3015 A	UC 1634	UC 3634																																																																												
ex 8542 11 82	*04	Controller of a local area network (LAN), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 63×63 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: <table><tr><td>82586</td><td>AM 7990</td><td>DP 83932</td><td>WD 83 C 510</td></tr><tr><td>82588</td><td>COM 9026</td><td>MB 86950</td><td>WD 83 C 603</td></tr><tr><td>82590</td><td>DP 8390</td><td>WD 80 C 24</td><td>WD 83 C 690</td></tr><tr><td>82592</td><td>DP 83261</td><td>WD 83 C 503</td><td></td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	82586	AM 7990	DP 83932	WD 83 C 510	82588	COM 9026	MB 86950	WD 83 C 603	82590	DP 8390	WD 80 C 24	WD 83 C 690	82592	DP 83261	WD 83 C 503		0																																																											
82586	AM 7990	DP 83932	WD 83 C 510																																																																											
82588	COM 9026	MB 86950	WD 83 C 603																																																																											
82590	DP 8390	WD 80 C 24	WD 83 C 690																																																																											
82592	DP 83261	WD 83 C 503																																																																												

CN code	TARIC	Description	Rate of autonomous duty (%)																																				
ex 8542 11 82	*06	Bus controller of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 41 × 41 mm, with not more than 240 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: <table><tr><td>82308</td><td>82 C 211</td><td>GC 181</td><td>R 4230</td></tr><tr><td>82309</td><td>82 C 288</td><td>HT 216</td><td>TACT 83443</td></tr><tr><td>82355</td><td>82 C 301</td><td>L1A 4601</td><td>VIC 068</td></tr><tr><td>82358</td><td>82 C 362</td><td>MSM 6307</td><td>VL 82 C 331</td></tr><tr><td>82 C 88</td><td>CA 91C014</td><td>R 4220</td><td>VY 86 C 410</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	82308	82 C 211	GC 181	R 4230	82309	82 C 288	HT 216	TACT 83443	82355	82 C 301	L1A 4601	VIC 068	82358	82 C 362	MSM 6307	VL 82 C 331	82 C 88	CA 91C014	R 4220	VY 86 C 410	0																
82308	82 C 211	GC 181	R 4230																																				
82309	82 C 288	HT 216	TACT 83443																																				
82355	82 C 301	L1A 4601	VIC 068																																				
82358	82 C 362	MSM 6307	VL 82 C 331																																				
82 C 88	CA 91C014	R 4220	VY 86 C 410																																				
ex 8542 11 82	*07	Control circuit of C-MOS technology, for system buses and peripheral buses and for the generation of clock signals (System Controller), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 33 × 33 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>82C101</td><td>82C320</td><td>82C461</td><td>82C493</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	82C101	82C320	82C461	82C493	0																																
82C101	82C320	82C461	82C493																																				
ex 8542 11 82	*08	Control circuit of C-MOS technology, capable of managing battery-charge/discharge, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 33 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>BQ 2001</td><td>BQ 2002</td><td>BQ 2003</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	BQ 2001	BQ 2002	BQ 2003	0																																	
BQ 2001	BQ 2002	BQ 2003																																					
ex 8542 11 82	*09	Disc storage unit controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 62 mm, with not more than 144 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: <table><tr><td>1454-001</td><td>CL SH265</td><td>OTI 033</td></tr><tr><td>8980</td><td>CL SH350</td><td>PD 7261</td></tr><tr><td>61156-001</td><td>CL SH360</td><td>PD 7262</td></tr><tr><td>61157-001</td><td>CL SH361</td><td>WD 1010</td></tr><tr><td>ADS 10C00</td><td>FE 2100</td><td>WD 2010</td></tr><tr><td>AIC 010</td><td>G 70360-33</td><td>WD 5010</td></tr><tr><td>AIC 100</td><td>HDC 9224</td><td>WD 5011</td></tr><tr><td>AIC 610 L</td><td>HDC 9234</td><td>WD 16C92</td></tr><tr><td>AIC 6190</td><td>L 1 A 0519</td><td>WD 42C22</td></tr><tr><td>AIC 65 C 10 B</td><td>MB 89311</td><td>WD 76C20</td></tr><tr><td>CL SH250</td><td>OMTI 505</td><td>Z 86 C 99</td></tr><tr><td>CL SH260</td><td>OTI 018</td><td></td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	1454-001	CL SH265	OTI 033	8980	CL SH350	PD 7261	61156-001	CL SH360	PD 7262	61157-001	CL SH361	WD 1010	ADS 10C00	FE 2100	WD 2010	AIC 010	G 70360-33	WD 5010	AIC 100	HDC 9224	WD 5011	AIC 610 L	HDC 9234	WD 16C92	AIC 6190	L 1 A 0519	WD 42C22	AIC 65 C 10 B	MB 89311	WD 76C20	CL SH250	OMTI 505	Z 86 C 99	CL SH260	OTI 018		0
1454-001	CL SH265	OTI 033																																					
8980	CL SH350	PD 7261																																					
61156-001	CL SH360	PD 7262																																					
61157-001	CL SH361	WD 1010																																					
ADS 10C00	FE 2100	WD 2010																																					
AIC 010	G 70360-33	WD 5010																																					
AIC 100	HDC 9224	WD 5011																																					
AIC 610 L	HDC 9234	WD 16C92																																					
AIC 6190	L 1 A 0519	WD 42C22																																					
AIC 65 C 10 B	MB 89311	WD 76C20																																					
CL SH250	OMTI 505	Z 86 C 99																																					
CL SH260	OTI 018																																						

CN code	TARIC	Description	Rate of autonomous duty (%)																																																																
ex 8542 11 82	*10	Video controller, with at least one of the following functions: — cathode-ray tube controlling, — liquid crystal display (LCD) controlling, — display controlling or character generation for liquid crystal displays (LCDs), — graphics controlling, — colour selection controlling, in the form of a monolithic integrated circuit, either contained in a housing the exterior dimensions of which do not exceed 36 × 62 mm or fixed on a plastic support, with not more than 231 connections: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>82 C 425</td><td>CL-GD5410</td><td>LC 7582</td><td>TMS 3492</td></tr><tr><td>82 C 431</td><td>CL-GD6410</td><td>LH 5821</td><td>TMS 57202</td></tr><tr><td>82 C 433</td><td>COP 472</td><td>M 50452</td><td>TMS 57206</td></tr><tr><td>82 C 434</td><td>CRT 9007</td><td>MB 89321</td><td>TMS 57207</td></tr><tr><td>82 C 435</td><td>CRT 97 C 11</td><td>MB 89322</td><td>TMS 57210</td></tr><tr><td>82 C 441</td><td>ET 4000</td><td>MSM 5259</td><td>TMS 57212</td></tr><tr><td>82 C 451</td><td>HD 44100</td><td>MSM 5298</td><td>TMS 57213</td></tr><tr><td>82 C 452</td><td>HD 44780</td><td>MSM 5299</td><td>TVGA 8900C</td></tr><tr><td>82 C 453</td><td>HD 61830</td><td>MSM 5839</td><td>V 6355-DJ</td></tr><tr><td>82 C 801</td><td>HD 61104T</td><td>NCR 77C22</td><td>V 6363</td></tr><tr><td>84 C 451</td><td>HD 61105T</td><td>OTI 067</td><td>VY 86 C 310</td></tr><tr><td>86 C 805</td><td>HD 66100</td><td>PEGA</td><td>WD 90 C 00</td></tr><tr><td>86 C 911</td><td>HD 66106T</td><td>PVGA</td><td>WD 90 C 10</td></tr><tr><td>86 C 928</td><td>HD 66107T</td><td>SED 1600</td><td>WD 90 C 30</td></tr><tr><td>AM 8052</td><td>HT 208</td><td>SED 1610</td><td>WD 90 C 31</td></tr><tr><td>AVGA1</td><td>HT 209</td><td>TMS 3491</td><td></td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	82 C 425	CL-GD5410	LC 7582	TMS 3492	82 C 431	CL-GD6410	LH 5821	TMS 57202	82 C 433	COP 472	M 50452	TMS 57206	82 C 434	CRT 9007	MB 89321	TMS 57207	82 C 435	CRT 97 C 11	MB 89322	TMS 57210	82 C 441	ET 4000	MSM 5259	TMS 57212	82 C 451	HD 44100	MSM 5298	TMS 57213	82 C 452	HD 44780	MSM 5299	TVGA 8900C	82 C 453	HD 61830	MSM 5839	V 6355-DJ	82 C 801	HD 61104T	NCR 77C22	V 6363	84 C 451	HD 61105T	OTI 067	VY 86 C 310	86 C 805	HD 66100	PEGA	WD 90 C 00	86 C 911	HD 66106T	PVGA	WD 90 C 10	86 C 928	HD 66107T	SED 1600	WD 90 C 30	AM 8052	HT 208	SED 1610	WD 90 C 31	AVGA1	HT 209	TMS 3491		0
82 C 425	CL-GD5410	LC 7582	TMS 3492																																																																
82 C 431	CL-GD6410	LH 5821	TMS 57202																																																																
82 C 433	COP 472	M 50452	TMS 57206																																																																
82 C 434	CRT 9007	MB 89321	TMS 57207																																																																
82 C 435	CRT 97 C 11	MB 89322	TMS 57210																																																																
82 C 441	ET 4000	MSM 5259	TMS 57212																																																																
82 C 451	HD 44100	MSM 5298	TMS 57213																																																																
82 C 452	HD 44780	MSM 5299	TVGA 8900C																																																																
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86 C 911	HD 66106T	PVGA	WD 90 C 10																																																																
86 C 928	HD 66107T	SED 1600	WD 90 C 30																																																																
AM 8052	HT 208	SED 1610	WD 90 C 31																																																																
AVGA1	HT 209	TMS 3491																																																																	
ex 8542 11 82	*11	Direct memory access (DMA) controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 38 × 83 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: <table><tr><td>82307</td><td>HD 68450</td><td>WE 32104</td></tr><tr><td>82357</td><td>L1A 4599</td><td>Z 8516</td></tr><tr><td>82380</td><td>MC 68440</td><td>Z 9516</td></tr><tr><td>82 C 223</td><td>MC 68450</td><td></td></tr><tr><td>GC 183</td><td>VC 2730-000 1 C</td><td></td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	82307	HD 68450	WE 32104	82357	L1A 4599	Z 8516	82380	MC 68440	Z 9516	82 C 223	MC 68450		GC 183	VC 2730-000 1 C		0																																																	
82307	HD 68450	WE 32104																																																																	
82357	L1A 4599	Z 8516																																																																	
82380	MC 68440	Z 9516																																																																	
82 C 223	MC 68450																																																																		
GC 183	VC 2730-000 1 C																																																																		
ex 8542 11 82	*12	Control and/or management circuit for memories (including buffers), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 53 × 82 mm, with not more than 369 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: <table><tr><td>0404 1872</td><td>82 C 222</td><td>NS 32382</td></tr><tr><td>1RJ3-0001</td><td>82 C 283</td><td>T 9490</td></tr><tr><td>1TU9-0301</td><td>82 C 302</td><td>THCT 4502</td></tr><tr><td>390 Z 55</td><td>82 C 325</td><td>TX 32082 W</td></tr><tr><td>68451</td><td>82 C 392</td><td>VL 4502</td></tr><tr><td>82359</td><td>A 38202</td><td>WD 1015</td></tr><tr><td>82385</td><td>CY7C604</td><td>WD 11 C 00-22</td></tr></table>	0404 1872	82 C 222	NS 32382	1RJ3-0001	82 C 283	T 9490	1TU9-0301	82 C 302	THCT 4502	390 Z 55	82 C 325	TX 32082 W	68451	82 C 392	VL 4502	82359	A 38202	WD 1015	82385	CY7C604	WD 11 C 00-22																																												
0404 1872	82 C 222	NS 32382																																																																	
1RJ3-0001	82 C 283	T 9490																																																																	
1TU9-0301	82 C 302	THCT 4502																																																																	
390 Z 55	82 C 325	TX 32082 W																																																																	
68451	82 C 392	VL 4502																																																																	
82359	A 38202	WD 1015																																																																	
82385	CY7C604	WD 11 C 00-22																																																																	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82 (cont'd)		82395 CY7C605 WD 12 C 00-22 82485 GC 113 WD 83 C 580 82495 XP HT 113 WD 83 C 583 82 C 08 MC 68851 82 C 102 NS 32082 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*13	Serial and/or parallel communication controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 62 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82 C 605 CL-CD180 PD 72001 SCN 68652 Z 85 C 30 82 C 606 MC 2652 SCN 2652 WD 76C30 Z 85 C 35 82 C 607 MC 68652 SCN 68562 Z 80 C 30 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*14	Control circuit of BiMOS technology, for DC motors, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 11 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 9990CS or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*22	Digital line interface control circuit of C-MOS technology, capable of controlling the data flow between a system interface circuit, a subscriber line interface circuit and a microprocessor interface circuit, comprising 2 multiplexers, 4 transmit/receive buffers with serial data ports, a data protocol control circuit, interrupt registers, 256 7-bit registers and a random-access memory (RAM) with a storage capacity of 128 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 53 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TP 3120 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*26	Control circuit of C-MOS technology, providing memory control, comprising an equilliser, a memory interface circuit, a multiplexer, a decoder, a bus control circuit and direct memory access (DMA) control circuits in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 1TX6-0301 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82	*28	Control circuit of C-MOS technology, operating at 12 MHz, comprising a programmable interval timer, a clockgenerator, two direct memory access controllers and a memory mapper, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including the following combination of figures: 82231 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*29	Circuit with a multimaster bus, of C-MOS technology, for controlling a local channel in 16 bits automatic data-processing machines, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 82303 82304 82306 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*30	Control circuit of C-MOS technology, for controlling the sequence of 4-bit addresses for execution of instructions in a microprogramme memory, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 38 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY2909 CY2911 CY7C909 CY7C911 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*32	Remote control circuit of C-MOS technology, capable of generating 2 048 different commands and controlling 32 systems, comprising a keyboard encoder, a keyboard decoder, a parallel to serial converter, a divider, a reset generator and an oscillator, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 36 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: SAA 3010 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*33	Eight-channel control circuit of C-MOS technology, for maintaining a constant electromagnetic traction force with incorporated diodes and a storage capacity of 8 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 28 mm, with not more than 22 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: UCN 5801 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*34	Monolithic integrated circuit with at least 16 analogue switching elements, of C-MOS technology, for controlling signals of 20 Hz or more but not exceeding 20000Hz, capable of dealing with signals up to 3 V with a distortion of not more than 0,05 % over the whole frequency range at a voltage of 1 V, contained in a housing the exterior dimensions of which do not exceed 16 × 40 mm, with not more than 42 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TC 9164 N TC 9177 P TC 9184 P - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*35	Control circuit of C-MOS technology, for the management of asynchronous cycles of a 32-bit central processing unit (CPU), of a direct memory access (DMA) circuit and of a multimaster bus, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters : 82 C 321 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*36	Control circuit of C-MOS technology, comprising 2 direct memory access (DMA) controllers, 2 interrupt controllers, a timer or a timer/counter and at least one memory mapper, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 33 × 33 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82C206 GC 101 SX VL 82 C 486 82C316 HT 101 SX - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*37	Control and management circuit containing 2 direct memory access (DMA) controllers, 2 interrupt controllers, 2 serial port controllers, one parallel port controller and a power management circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 39 × 39 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 82360SL - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82	*40	Control circuit of C-MOS technology, for controlling and interfacing signals between a central processing unit (CPU), memory and input/output interfaces, comprising circuits for refreshing dynamic random-access memories (DRAMs), for decoding of addresses, for generating clock signals and monitoring data transfer interrupt signals, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 344 S 0602 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*41	Control circuit of C-MOS technology, for multiplexing the address bus of a central processing unit (CPU), consisting of 41 bus control circuits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 82 C 103 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*52	Control circuit of C-MOS technology, capable of processing read-signals and of controlling the motor of a compact-disc player, comprising a central processing unit (CPU) interface, an error detection/correction circuit, a read-signal demodulator, a phase locked loop (PLL) circuit and a constant-linear-velocity (CLV) controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 21 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXD 1125 CXD 1130 CXD 1135 CXD 1167 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*53	Control circuit of C-MOS technology, for microcontroller or microcomputer systems with a processing capacity of 16 or 32 bits, providing programmable-memory-configuration control, programmable wait-states and programmable command-delays for memory- and bus-timing, comprising a real-time clock, a static random-access memory (S-RAM) with a storage capacity of 1 Kbit, an interrupt-controller, a direct memory access (DMA) controller, a programmable DMA-clock, and timers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TACT 82S411 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82	*54	Control circuit of C-MOS technology, capable of controlling signals of a microprocessor with a processing capacity of 32 bits and access to a dynamic random-access memory (D-RAM) and a cache memory, 8-bit, 16-bit or 32-bit system-buses and providing bus-arbitration, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31×31 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 82C311 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*62	Input-output circuit of N-MOS (including H-MOS) technology for data control, equipped with a timing control with a static random-access memory (S-RAM) with a storage capacity of 128×8 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16×54 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 6532 CO 10750 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*63	Control circuit, of N-MOS (including H-MOS) technology, for data/address flows from the central processing unit (CPU), inputs/outputs and the main memory, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 36×36 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CIM 1456 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*65	Control circuit of C-MOS technology for DC electric motors, comprising a circuit to monitor power supply, a circuit to store and decode addresses and to multiplex data, an 8-bit digital to analogue converter and 5 amplifiers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: GC 27 GC 45 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*68	Error detection and correction circuit of N-MOS (including H-MOS) technology, capable of detecting and correcting single bit errors and detecting all double bit errors, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30×30 mm, with not more than 68 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82 (cont'd)		— an identification marking consisting of or including the following combination of figures: 8206 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*69	Interpolation pulse generator, of C-MOS technology, for controlling geometric functions, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: KM 3701 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*71	Control circuit of C-MOS technology, for a microprogramme, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×54 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY 2910 CY 7C 910 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*74	Control circuit of C-MOS technology, for the control of constant power supply of 60 V / 500 mA, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13×13 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: UCN 5816 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 82	*75	Control circuit, of C-MOS technology, for monitoring the voltage of random-access memories (RAMs) in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×54 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BQ 2201 BQ 2202 BQ 2204 BQ 2502 BQ 2503 DS 1210 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)																																
ex 8542 11 82	*81	Controller for servo-devices of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 54 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: KM 3702 or — other identification markings relating to devices complying with the abovementioned description	0																																
ex 8542 11 82	*82	Timing control unit (TCU) with two-phase cycle for central processing unit (CPU) and memory management unit (MMU), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 33 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: NS 32201 NS 32 C 201 or — other identification markings relating to devices complying with the abovementioned description	0																																
ex 8542 11 82	*85	Control circuit of N-MOS (including H-MOS) technology, for DC motors, comprising a digital 16-bit filter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 37 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: LM 629 or — other identification markings relating to devices complying with the abovementioned description	0																																
ex 8542 11 84	*08	Bus interface circuit, whether or not with bus control functions, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 34 × 63 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: <table> <tr> <td>53 C 700</td><td>CL PD6710</td><td>GC 132</td><td>PBI</td></tr> <tr> <td>53 C 710</td><td>CL PD6720</td><td>GC 133</td><td>TMS 38030</td></tr> <tr> <td>53 C 720</td><td>ESP 216</td><td>HS 3282</td><td>WD 33 C 92</td></tr> <tr> <td>82335</td><td>ESP 226</td><td>L 64853A</td><td>WD 33 C 93</td></tr> <tr> <td>82351</td><td>FAS 216</td><td>NCR 5380</td><td>Z 86017</td></tr> <tr> <td>82353</td><td>FAS 226</td><td>NCR 5381</td><td></td></tr> <tr> <td>82365SL</td><td>FAS 236</td><td>NCR 53 C 80</td><td></td></tr> <tr> <td>AIC 6250</td><td>FE 3030</td><td>NCR 53 C 90</td><td></td></tr> </table> or — other identification markings relating to devices complying with the abovementioned description	53 C 700	CL PD6710	GC 132	PBI	53 C 710	CL PD6720	GC 133	TMS 38030	53 C 720	ESP 216	HS 3282	WD 33 C 92	82335	ESP 226	L 64853A	WD 33 C 93	82351	FAS 216	NCR 5380	Z 86017	82353	FAS 226	NCR 5381		82365SL	FAS 236	NCR 53 C 80		AIC 6250	FE 3030	NCR 53 C 90		0
53 C 700	CL PD6710	GC 132	PBI																																
53 C 710	CL PD6720	GC 133	TMS 38030																																
53 C 720	ESP 216	HS 3282	WD 33 C 92																																
82335	ESP 226	L 64853A	WD 33 C 93																																
82351	FAS 216	NCR 5380	Z 86017																																
82353	FAS 226	NCR 5381																																	
82365SL	FAS 236	NCR 53 C 80																																	
AIC 6250	FE 3030	NCR 53 C 90																																	

CN code	TARIC	Description	Rate of autonomous duty (%)								
ex 8542 11 84	*09	Interface circuit or interface circuit with control functions, for a fibre-optic or twisted-pair-cable local area network (LAN), capable of interfacing between a media access controller and a local area network (LAN) or a central processing unit (CPU), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 38 × 38 mm, with not more than 175 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>DP 8025</td><td>DP 83255</td><td>DP 83902</td><td>T 7213</td></tr><tr><td>DP 83251</td><td>DP 83265</td><td>DP 83905</td><td></td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	DP 8025	DP 83255	DP 83902	T 7213	DP 83251	DP 83265	DP 83905		0
DP 8025	DP 83255	DP 83902	T 7213								
DP 83251	DP 83265	DP 83905									
ex 8542 11 84	*10	Cathode-ray tube control and interface circuit, capable of interfacing between a graphic microprocessor and dynamic random-access memories (D-RAMs), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>TMS 34096</td><td>TMS 34098</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	TMS 34096	TMS 34098	0						
TMS 34096	TMS 34098										
ex 8542 11 84	*11	Control and interface circuit of C-MOS technology, comprising a memory controller, a peripheral controller, a central processing unit (CPU) interface circuit, a numeric processor unit (NPU) interface circuit, a clock generation circuit, a timer and a parity-check circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 33 × 33 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>HT 15</td><td>HT 18</td><td>HT 21</td><td>HT 22</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	HT 15	HT 18	HT 21	HT 22	0				
HT 15	HT 18	HT 21	HT 22								
ex 8542 11 84	*12	Interface circuit of C-MOS technology, for at least one encoder, capable of identifying and measuring direction and displacement via signals of external sensors, comprising at least 3 counters, at least one latch of 16 or 24 bits, at least one multiplexer, at least one 8-bit parallel data buffer, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 25 × 37 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>THCT 2000</td><td>THCT 12016</td><td>THCT 12024</td><td>THCT 12316</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	THCT 2000	THCT 12016	THCT 12024	THCT 12316	0				
THCT 2000	THCT 12016	THCT 12024	THCT 12316								

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 84	*13	Data interface and control circuit of C-MOS technology, providing transmission and reception of 1-bit, 4-bit or 8-bit data packets at a rate of 51,84 Mbits per second, comprising a receive control circuit, a transmit control circuit, a receive data formatter and a transmit data formatter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 144 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TXC 05101 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*22	Enhanced programmable communications interface (EPCI), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 2661 68661 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84 ex 8542 11 95	*23 *56	Serial interface, capable of implementing the data stream encoding, decoding and associated control functions for a local area network (LAN), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 33 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 8002 82501 AM 7991 COM 91 C 32 8023 82 C 501 COM 9032 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*24	Interface circuit for a text data decoder of C-MOS technology, capable of data-slicing, clock regeneration and synchronisation separation, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 25 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CF 72303 CF 72306 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 84	*25	Interface circuit of C-MOS technology, for signals between a peripheral hard-disc memory unit and a central processing unit (CPU), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 53 × 62 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: - AIC 560 L OMTI 5090 (OMTI 20509) - DP 8466 WD 11 C 00-17 - OMTI 5080 (OMTI 20508) WD 14 C 00-17 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*26	Flow meter interface of BiMOS technology, comprising 16 amplifiers, 3 digital-to-analogue converters, an analogue-to-digital converter, filters, a sample and hold circuit, an oscillator, a phase locked loop (PLL) circuit and a serial interface circuit for a microprocessor, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 25 × 25 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: - AD75027 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*27	Interface circuit of C-MOS technology, for a keyboard with a capacitive matrix, capable of matrix scanning and detection, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 52 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including the following combination of figures: 22-00958-000 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*29	Interface circuit of C-MOS technology, capable of interfacing between a graphic microprocessor, dynamic random-access memories (D-RAMs) and a colour palette register, generating local memory address and control signals for the graphic microprocessor, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: - TMS 34092 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 84	*30	Bus controller and interface of C-MOS technology, comprising one or two direct memory access (DMA) controllers, one or two interrupt controllers and a timer/counter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 33 × 33 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82C100 82C300 82C836 WD 76 C 10 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*32	Control and interface circuit for controlling a bi-directional 32-bit data/address flow between several buses, capable of operating in data mode with parity check, in data mode without parity check and in address mode, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 33 × 33 mm, with not more than 120 connections and bearing: — an identification marking consisting of or including the following combination of figures: 82352 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*33	Interface and control circuit of C-MOS technology, for controlling data communication between a system bus interface and peripheral units, comprising a system interface gate, a microprocessor gate and a direct memory access (DMA) gate, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 21 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 1TV1-0301 1TV3-0301 1TV4-0301 1TV2-0301 1TV3-0302 1TV4-0302 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*34	Control and interface circuit of C-MOS technology, capable of receiving, processing and transmitting subscriber data in a digital network, comprising a line interface unit, a multiplexer, a data link controller, a microprocessor interface and an oscillator, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 54 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 79C30A AM 79C32A QMV 453 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*35	Interface and control circuit of C-MOS technology, for interfacing and controlling the data sequence between an automatic data-processing machine and a rigid disc storage unit, comprising a dual-port buffer memory manager, a sequencer control circuit, a microprocessor interface and a bus interface having 24 mA drivers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 100 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 84 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: AIC-6060 32C260 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*36	Control circuit of C-MOS technology, for a floppy disc-unit and data transfer rate, for precompensation of write signals, for data separation, for clock-signal generation and for interfacing a central processing unit (CPU), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26 × 62 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 82077 DP 8473 WD 37C65 WD 57C65 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*40	Control and interface circuit for central processing unit (CPU) of C-MOS technology, comprising a control unit for the refreshment of memories, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: FE 3010 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*43	Control and interface circuit between a microprocessor with a processing capacity of 32 bits and a floating point coprocessor, of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 54 × 54 mm, with not more than 299 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CY 7 C 608 L 64802 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*44	Control and interface circuit of C-MOS technology, operating at 12 MHz, comprising a clock generator, a bus controller for a microprocessor, a timer, two programmable interrupt controllers and an interface for a numeric coprocessor, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including the following combination of figures: 82230 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 84	*45	Control and interface circuit of C-MOS technology, comprising 2 interrupt controllers, three 16-bit programmable timer-counters, a real time clock and a numeric microprocessor interface, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 28×28 mm, with not more than 120 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TACT 82303 PB - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*46	Control and interface circuit of C-MOS technology, comprising 48 mA drivers, registers, an 18-bit direct memory access (DMA) interface, an 8-bit microprocessor bus and a parity generator and checker, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31×31 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: NCR 53C94 NCR 53C95 NCR 53C96 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*47	Interface and control circuit of C-MOS technology, comprising 2 universal asynchronous receiver/transmitters (UARTs), a parallel-data port, a hard-disc interface and a floppy-disc controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29×29 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82C711 TACT 88511 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*48	Interface and control circuit of C-MOS technology, programmable for interfacing signals between video-graphic-array (VGA) controllers and cathode-ray tube (CRT) displays, liquid crystal displays (LCDs), light-emitting-diode (LEDs) displays or plasma-displays, capable of simultaneously controlling a CRT-display and a LCD display, comprising a digital-to-analogue video-converter with random-access memory (RAMDAC), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15×21 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CL-GD6340 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 84	*49	Repeater interface and control circuit of C-MOS technology, comprising 7 or 12 transmission/reception interface ports, an attachment-unit interface (AUI) port, a phase locked loop (PLL) decoder, a 32-bit buffer and a bus-interface for communication between repeater interface controllers (RICs), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 45 × 45 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DP 83950 DP 83955 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*50	Interface circuit of C-MOS technology, for interfacing data between a 32-bit bus and input/output-registers and/or interface circuits, providing direct memory access (DMA) control, comprising a FIFO (first in, first out) read/write memory with a storage capacity of 256 bits, a data-latch, an address-latch and 32-bit registers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 37 × 37 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 1XB9-0401 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*51	Bus interface and control circuit of C-MOS technology, comprising 2 programmable interrupt-controllers, 2 timers, 3 counters, a real-time clock, a random-access memory (RAM) with a storage capacity of 912 bits, a memory-interface circuit, a floppy-disc controller interface circuit, a hard-disc controller interface circuit, a numeric-coprocessor interface and a keyboard-interface, capable of address-enable generation, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TACT 84544 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 84	*52	Interface and control circuit of C-MOS technology, for read/write data between a digital-audio-tape storage unit and a microprocessor, comprising a buffer-manager circuit for a dynamic random-access memory (D-RAM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 1XK2-0301 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*01	8-bit data-desynchroniser/synchroniser of C-MOS technology, capable of transmitting and receiving 2 asynchronous signals at a rate of 2 Mbits per second, comprising a microprocessor control circuit and a 4-buses interface circuit operating at a rate of 19,44 megabytes per second and a phase-locked loop (PLL) circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30×30 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TXC 04002 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*02	Universal synchronous receiver/transmitter of C-MOS technology (C-MOS USRT), capable of full duplex digital voice and/or data transfer with a total speed of 80 Kbits/s or more over a distance of 2 km or a total speed of 160 Kbits/s or less over a distance of 1 km, comprising a modulator and data buffers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15×33 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 145421 MC 145425 TP 3401 TP 3403 MC 145422 MC 145426 TP 3402 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*03	Transmitter/receiver of C-MOS technology, capable of terminating line rates of 8 448 or 34 368 Kbits per second, comprising an automatic gain control circuit, an equalizer circuit, an error detector, an encoder, a decoder and a clock recovery circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TXC 02050 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*04	Arithmetic-logic unit of N-MOS (including H-MOS) technology (N/H-MOS ALU), comprising one 32-bit register, one 24-bit register, one 4-bit register, 12 1-bit registers, two 16×24-bit service memories, one logic network performing arithmetic and logic operations, decoding logic, and error detection and management logic, one 8-bit counter and a timing network, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 23×82 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: ALU 0486 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*05	Data segmentation or reassembly circuit of C-MOS technology, providing fragmentation of 16 382 packets of 8- or 16-bit words into cells or providing reassembly of these cells in 16 382 packets of 8- or 16-bit words, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TXC 05501 TXC 05601 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*06	8-bit identity comparator of C-MOS technology, with a propagation delay of not more than 7,2 ns, capable of bit for bit comparison of 2 words of not more than 8 bits each, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 27 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 74FCT521 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*07	Colour palette circuit of C-MOS technology, for 256 colour nuances or more, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 38 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SC 11482 SC 11484 SC 11487 SC 15025 SC 11483 SC 11485 SC 11489 SC 15026 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*08	Transmitter/receiver of C-MOS technology, operating with a single power supply of +5 V and a supply current not exceeding 2 mA, comprising 2 transmitters and 2 receivers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 20 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MAX 220 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*09	8-bit (octal) register of C-MOS technology, with a propagation delay not exceeding 11 ns, comprising 8 D-type-flip-flops, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 27 mm, with not more than 20 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: 74FCT374 74FCT534 74FCT574 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*10	8-, 16- or 18-bit bidirectional transmitter/receiver of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 33 mm, with not more than 56 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 29 C 833 A 74 AC 16646 74 ACT 16470 74 FCT 645 29 C 853 A 74 AC 16657 4 ACT 16863 74 AC 16472 74 ACT 16245 74 FCT 245 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*11	Transmitter/receiver of C-MOS technology, operating with a single power supply of +5 V, comprising 4 transmitters and 5 receivers of which one stays active during shutdown, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 38 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MAX 223 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*12	Transmitter/receiver of C-MOS technology, operating with a single power supply of 5 V, comprising at least 6 transmitters and at least 8 receivers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MAX 244 MAX 245 MAX 246 MAX 247 MAX 248 MAX 249 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*13	Encoder/decoder with filter of C-MOS technology, for frequencies of 600 Hz or more but not exceeding 4 000 Hz, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 31 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: QMV 112 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*14	16- or 20-bit flip-flop of the D-type of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 20 mm, with not more than 56 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74 AC 16821 74 ACT 16374 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*15	Dual-tone multi-frequency (DTMF) receiver of C-MOS technology, capable of decoding DTMF signals to 4-bit binary data, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 30 mm, with not more than 22 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: M-957 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*16	Transmitter/receiver of C-MOS technology, for signals between an encoder/decoder using Manchester code (MED) or an interface unit and a twisted pair cable or a coaxial cable in a local area network (LAN), comprising an oscillator and a collision detector circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 20 × 40 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 83C94 83C92 Am 79C98 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*17	Clock recovery circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 12 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: DP 83231 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*18	Digital-to-analogue and analogue-to-digital converter of BiMOS technology, comprising sample and hold circuits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 25 × 25 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures: 21-26500 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*19	Programmable overhead terminator of C-MOS technology, providing 8-bit data transmission/reception and line pointer generation using an external clock, comprising a random-access memory (RAM), a serial in-/output port, a microprocessor interface circuit, a multiplexer and a demultiplexer, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TXC 03003 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*20	Quadruple encoder/decoder with pulse-code-modulation filters of C-MOS technology, comprising amplifiers for sidetone balance, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 18 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: QMV 365 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*23	8-bit analogue-to-digital parallel converter of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combinations of figures and letters: IDT 75C48 IDT 75C58 MP 7683 MP 7684 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*26	Analogue-to-digital signal converter, containing amplifiers, digital-to-analogue and analogue-to-digital converters with a supply voltage of 12 V (± 10 %) and a digital serial interface with asynchronous receiver/transmitter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 18 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 75002 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*27	Audio digital-to-analogue converter of C-MOS technology, with a dynamic range of 93 dB or more, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 38 mm, with not more than 44 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: CS 4328 CXD 2564 TMS 57010 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*30	Decoder of C-MOS technology, for demodulating and demultiplexing of stereo signals, comprising an interface circuit of a digital-to-analogue converter having a output clock signal of 8,192 or 16,384 MHz, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 63 mm, with not more than 48 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CF 70088 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*31	Synchronising circuit combined with a scan and signal distributor of C-MOS technology, comprising a control unit, a contact bounce elimination circuit, a 17-bit shift register and a data output formatting unit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 54 × 54 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: QMV 222 - or — other identification marking relating to devices complying with the abovementioned description	0
ex 8542 11 86	*34	Dual or octal universal asynchronous receiver/transmitter (Dual or octal UART), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 32 × 53 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 1TQ1-0202 2681 PC 87310 SCC 2698 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*35	Latch, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 33 mm, with not more than 56 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74FCT162373 74FCT533 74FCT843 74FCT16373 74FCT573 74FCT844 74FCT373 74FCT841 74FCT845 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86 ex 8542 11 98	*36 *18	12-, 16-, 18- or 20-bit analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 55 mm, with not more than 60 connections and bearing: — an identification marking consisting of or including one the following combinations of figures and letters: AD 574 A CS 5317 CS 5338 CS 5506 CS 5016 CS 5326 CS 5339 CS 5507 CS 5101 CS 5327 CS 5346 CS 5508 CS 5102 CS 5328 CS 5349 CS 5516 CS 5116 CS 5329 CS 5501 CS 5520 CS 5126 CS 5336 CS 5503 DSP 56ADC16 CS 5316 CS 5337 CS 5505 TSC 7109 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*37	Analogue-to-digital converter capable of driving a liquid crystal (LCD) or light emitting diode (LED) display with not more than 4 digits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 54 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ICL 7106 ICL 7116 ICL 7126 ICL 7137 MAX 136 ICL 7107 ICL 7117 ICL 7136 MAX 133 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*38	Clock/calendar circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 33 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 58274 MCCS 146818 MM 58174 A MC 146818 MM 58167 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*39	Clock generator, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 38 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 82 C 402 DP 8532 ICS 2494 SC 11411 AV 9129 DP 83241 ICS 90C64 SC 11412 Bt 438 ICD 2023 PCLK 1 TCK 9002 Bt 439 ICD 2028 PCLK 2 WD 90 C 61 DP 8531 ICS 1394 SC 11410 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*40	Data-buffer or data/address-buffer circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 128 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: FB 2020 GC 102 HT 102 VL 82 C 332 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*41	Buffer and line driver of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 27 mm, with not more than 56 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74 AC 16240 74 ACT 16244 74 FCT 244 74 AC 16825 74 FCT 240 74 FCT 540 74 AC 16827 74 FCT 241 74 FCT 541 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*42	Arithmetic-logic unit (ALU) of C-MOS technology, with a capacity of 4 or 16 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 83 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY2901 CY7C9115 CY7C9117 CY7C9101 CY7C9116 CY7C901 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*43	Circuit of C-MOS technology, with audio output and with or without hour-count, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 33 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 7910 SVM 5530 or — other identification markings relating to devices complying with the abovementioned description These devices are for use in the manufacture of goods falling within Chapter 91 (a)	0
ex 8542 11 86	*44	Data or image compression/decompression circuit of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 46 × 46 mm, with not more than 188 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 1XH4-0301 74 ACT 6340 CL 550 1XK6-0301 CL 450 CL 950 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*45	7-, 8- or 12-bit digital-to-analogue converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 40 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 664 DAC 0830 DAC 0831 DAC 0832 PBM 3960 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*48	Single or triple digital-to-analogue video converter with at least one random-access memory (RAMDAC) of C-MOS technology, having one or more colour palette registers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 39 × 39 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 357S0010 Bt451 Bt460 Bt473 357S0011 Bt458 Bt461 Bt475 357S0012 Bt459 Bt462 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*49	Frequency synthesiser of BiMOS technology, capable of synchronising and dividing of frequencies, comprising 1 or 2 phase-locked loop circuits and 1 or 2 prescalers with an operating frequency of 10 MHz or more but not exceeding 2,5 GHz, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 7 × 25 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MB 1501 MB 1509 MB 1518 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*50	Circuit for the recording and reproduction of speech of C-MOS technology, working at a speed of 9,6 Kbits/sec, comprising a memory interface circuit, an encoding/decoding interface circuit, a central processing unit (CPU) interface and a random-access memory (RAM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 21 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TC 88401 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*54	Modulator/demodulator of C-MOS technology (C-MOS-Modem), only for half duplex transfer of image telegraphy (facsimile) at a rate of 300, 2 400, 4 800, 7 200 or 9 600 bits per second, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 21 mm, with not more than 80 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: TC 35128 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*55	Generator of C-MOS technology, for a user-definable cursor, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 33 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: Bt431 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*56	6-bit transmitter/receiver of BiMOS technology, capable of filtering out glitches from handshake signals, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: DS 3884 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*57	Programmable asynchronous communication element circuit of C-MOS technology, for the asynchronous transmission and reception of data, comprising a FIFO (first in, first out) read/write memory with a storage capacity of 128 bits and at least one serial input/output channel and a bi-directional parallel channel, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26 × 26 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16C551 16C552 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*58	Address comparator of C-MOS technology, comprising one or more static random-access memories (S-RAMs), a parity generator, a parity checker and one or more comparators, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 20 × 65 mm, with not more than 48 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SN 74 ACT 2151 SN 74 ACT 2154 SN 74 ACT 2158 SN 74 ACT 2152 SN 74 ACT 2156 SN 74 ACT 2159 SN 74 ACT 2153 SN 74 ACT 2157 SN 74 ACT 2160 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*61	Computing unit without an internal programme sequencer for the multiplication or processing of fixed and floating point numbers, of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 42 × 42 mm, with not more than 144 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ADSP 3210 ADSP 3220 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86 ex 8542 11 98	*63 *09	Smoke detector operating in a temperature range of -20 °C or more but not exceeding 60 °C, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 23 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 14467 MC 14471 CS 235 MC 14468 MC 145010 V 24216 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*64	Scan and signal distributor of C-MOS technology, comprising a data and clock extraction unit, a control unit, a contact bounce elimination circuit, a current source control unit, a 17-bit shift register and a data output formatting unit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 54 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: QMV 16 - or — other identification marking relating to devices complying with the abovementioned description	0
ex 8542 11 86	*65	Encoder/decoder of C-MOS technology, for the conversion of data into NRZ (Non-Return-to-Zero) format or RLL (Run-Length-Limited) format, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 12 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 61158 CL-SH110 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*66	9-bit data transmitter/receiver of BiMOS technology, comprising a transmitter with an edge-triggered latch and a receiver with a transparent latch, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: DS 3886 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*67	Linear phase digital filter of C-MOS technology, capable of filtering phase periods into a 24 bits serial format, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13×13 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CS 5322 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*68	Modulator/demodulator of C-MOS technology (C-MOS-Modem), for full duplex data-transfer at a rate of 75, 300, 600, 1 200, 2 400, 4 800, 7 200, 9 600, 12 000 or 14 400 bits per second and for half duplex transfer of image telegraphy (facsimile) at a rate of 2 400, 4 800, 7 200, 9 600, 12 000 or 14 400 bits per second, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26×53 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: RC 144DPL SC 11046 SC 11055 SC 11094 SC 11044 SC 11054 SC 11092 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*69	Echo and reverberation module, comprising a multiplier/accumulator, two random-access memories (RAMs) with a total storage capacity of 4 Kbits and a read only memory, non-programmable (ROM) with a storage capacity of 12 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 25×25 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: VC5344 VC5909 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*70	8×8-bit multiplier accumulator (MAC) of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16×62 mm, with not more than 48 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: ADSP 1008-A - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*71	Adaptive differentiated pulse-code-modulation circuit of C-MOS technology, for encoding/decoding speech and data, having 8 independent channels with a bandwidth of 8 kHz and capable of full and half duplex data-transfer at a rate of 32 000 bits per second, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 38 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SC 11360 SC 11362 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*72	Encoder/decoder with pulse-code-modulation of N-MOS (including H-MOS) technology (N/H-MOS PCM), comprising a sample and hold circuit, a digital-to-analogue converter, a comparator, a successive approximation register and a logic function to interface to a full duplex PCM link, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 29 mm, with not more than 22 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 2911 A-1 or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 11 86	*73	Pulse-code-modulation transmit/receive filter of N-MOS (including H-MOS) technology (N/H-MOS PCM), for a PCM line or trunk termination, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 21 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: D 2912 A or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 11 86	*76	Encoder/decoder of N-MOS (including H-MOS) technology, for the conversion of data into serial or parallel signals, comprising an arithmetic logic unit (ALU) and a read only memory, non-programmable (ROM) with a storage capacity of 128 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 60 mm, with not more than 48 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMS 38020 TMS 38021 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*80	Comparator circuit of C-MOS technology, capable of comparing cache-address tags, comprising a static random-access memory (S-RAM) with a storage capacity of 16 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 33 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MCM 62350 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*81	Digital filter of C-MOS technology, capable of quadruple or octuple over-sampling for 2 channels, comprising a read only memory, non-programmable (ROM) and a random-access memory (RAM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 36 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TMS 57021 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*82	Transmitter/receiver of C-MOS technology, with a propagation delay not exceeding 11 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 33 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 29FCT52 82503 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*83	Transmitter/receiver of BiMOS technology, for bi-directional 16-bit data transmission, comprising a data/address multiplexer/demultiplexer, data storage latches and an 8-bit control circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26 × 26 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SN 74 BCT 2423 SN 74 BCT 2424 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*84	Transmitter/receiver of BiMOS technology, for multiplexed data/address transmission, comprising D-type flip-flops, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26×26 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SN 74 BCT 2420 SN 74 BCT 2425 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*85	Transmitter/receiver of C-MOS technology, operating with a supply current not exceeding 3 mA, comprising 2 driver circuits, 3 receiver circuits and a thermal protection circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9×12 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: LTC 902 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*86	Digitally controlled potentiometer of C-MOS technology, consisting of an array of 31 or 99 resistive elements, comprising a 5 or 7 bits counter, a 5 or 7 bits non-volatile memory and a decoder, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 7×11 mm, with not more than 14 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: X 9102 X 9104 X 9312 X 9503 X 9C103 X 9C503 X 9103 X 9311 X 9313 X 9C102 X 9C104 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*87	Circuit for the recording and reproduction of speech of C-MOS technology, working at a programmable speed of 8 Kbits/sec or more, with an amplifier and a 10 bit digital-to-analogue converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18×22 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: T 6668 TC 8830 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*88	Hall effect sensor of BiMOS technology, capable of communicating over a 2-wire bus, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 5×5 mm, with not more than 3 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: UGN 3055U UGS 3055U - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86 ex 8542 11 98	*89 *15	Disc storage unit data separator (DDS), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 37 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DP 8460-2 DP 8465 VM 5352 WD 10 C 21 DP 8460-3 HDC 9226 VM 5353 DP 8460-4 VM 5351 WD 10 C 20 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*90	Read channel circuit, providing read/write and servo demodulator functions, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 11 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 32P4730 91C020 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*91	Subscriber line audio-processing circuit (SLAC) comprising 2 digital signal processors, an analogue-to-digital converter and a digital-to-analogue converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 7901 AM 7905 or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 11 86	*92	Decoder and sink driver, providing a output current of 500 mA at an output voltage of 35 V, with 4 inputs and 16 outputs, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 19 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 34142 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*93	Signal synthesiser of N-MOS (including H-MOS) technology with a frequency generator, a memory of 15 instrumental tones, a digital-to-analogue converter and a quartz oscillator, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 25 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: YM 2413 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*94	Digital signal synthesiser of C-MOS technology, comprising 32 or 48 frequency generators, a clock generator and an address generator, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: VC 2375 VC 5395 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*95	Phoneme speech synthesiser of C-MOS technology, with a supply current of less than 10 mA, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 78 A 263 CD 54123 N2L CM 54145 N2L CD 54121 N2L CD 54147 N2L CM 54146 N2L CD 54122 N2L CM 54104 CM 54166 N2L - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*96	8-bit data-synchroniser of C-MOS technology, capable of transmitting and receiving serial or parallel data at a rate of 155,52 Mbits or 19,44 megabytes per second, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TXC 02301 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*97	Programmable digital signal synthesiser of C-MOS technology, for the generation of digital sound signals comprising random-access memories (RAMs) with a total storage capacity of 16 Kbits, with a sampling rate of 22,257 kHz and 44,1 kHz and two output channels for mono or stereo signals, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 18 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 344 S 0053 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 86	*98	Translator of BiMOS technology, to convert to emitter-coupled logic (ECL) levels or to transistor-transistor logic (TTL) levels, capable of bus driving of full duplex data transmission, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 84 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: CY10E383 CY101E383 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 90	*01	Static random-access memory of GaAs technology (GaAs S-RAM), with a storage capacity not exceeding 4 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 12G014 12G044 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 90	*02	Random-access memory of ECL technology (ECL-RAM) with a storage capacity not exceeding 64 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 37 mm or fixed on a plastic support, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 10422 10474 10484 100474 100490 10470 10480 10490 100484 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*02	Programmable logic device, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 16L4 20R6 CY10E302 SN 54 LS 334 16L6 20R8 CY100E301 SN 54 LS 335 16L8 22V10 CY100E302 SN 54 LS 336 16R4 82 S 100 FP 54 AS 839 SN 74 LS 333 16R6 82 S 101 FP 54 AS 840 SN 74 LS 334 16R8 93458 FP 74 AS 839 SN 74 LS 335 20L8 93459 FP 74 AS 840 SN 74 LS 336 20R4 CY10E301 SN 54 LS 333 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*14	Translator of bipolar technology, to convert to emitter-coupled logic (ECL) levels or to transistor-transistor logic (TTL) levels, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 31 mm, with not more than 124 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 95 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 57-240245 F100124 F100125 MB 768 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*15	Control and/or management circuit for memories (including buffers) of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26×67 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 2965 DP 8409 DP 8429 AM 2966 DP 8419 MB 1422 DP 8408 DP 8428 SN 74 S 409 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*16	Bus interface circuit of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the external dimensions of which do not exceed 26×34 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82 A 203 82 A 304 AM 29821 AM 29825 AM 29845 82 A 204 82 A 305 AM 29822 AM 29826 82 A 205 82 A 436 AM 29823 AM 29843 82 A 303 82 A 442 AM 29824 AM 29844 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*19	9-bit analogue-to-digital converter of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30×30 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TDC 1049 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*22	Digitise and data-separation circuit of bipolar technology, comprising a phase-locked loop circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15×15 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: SN 28962 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 30	*60	Winchester disc drive amplifier, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 7×7 mm, with not more than 10 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: A 2480 FC - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*60	Voltage regulator with an input voltage range of 4,75 V or more but not exceeding 60 V and a quiescent current not exceeding 10 mA, comprising a 1 A switch circuit and an oscillator with a fixed frequency of 52 kHz, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 11×29 mm or the diameter of which do not exceed 11 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LM 1575 LM 2575 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*01	Control circuit of C-MOS technology, capable of driving 4 or 8 independent output loads each with an output current of 3 A and an output leakage current not exceeding 80 μA, comprising an overvoltage detector, a short-circuit detector and an open load detector, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 23×38 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 33293 PC 33298 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*02	Control circuit of BiMOS technology, capable of driving an output load with an output current of 0,5 A and an output leakage current not exceeding 100 μA, comprising an overvoltage detector, a current limiting circuit and a thermal shutdown circuit, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 11×11 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 3392 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*03	Monolithic integrated analogue circuit of bipolar technology, for driving direct-current motors, working with an output voltage not exceeding 18 V at an output current not exceeding 1,6 A, contained in a housing the exterior dimensions of which do not exceed 9×26 mm, with not more than 10 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 60 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: BA 6109 BA 6209 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*05	Control circuit of C-MOS technology, for monitoring the voltage of microprocessors, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 12 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DS 1231 DS 1232 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*07	Control circuit of bipolar technology, capable of driving N-MOS transistors, with a standby current not exceeding 3 µA, comprising an overvoltage shutdown circuit and a charge pump, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 7 × 11 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 33091 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 70	*10	Interface circuit of dielectric isolation technology, for telephone sets with a line voltage not exceeding 265 V, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 13 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: LH 1497 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*01	Filter of C-MOS technology, with a programmable cut-off frequency of 4,5 MHz or more but not exceeding 25,2 MHz and a programmable frequency amplification not exceeding 9 dB, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 5 × 12 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 896 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80	*02	Audio recording/reproducing circuit of C-MOS technology, capable of direct analogue storage of audio data with a duration of 12, 16 or 20 seconds, comprising an electrically erasable, programmable, read only memory (E²PROM) with a storage capacity of 1 Mbit, 3 amplifiers, an automatic gain control circuit and 2 filters, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 37 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ISD 1012A ISD 1016A ISD 1020A or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*03	Analogue signal microprocessor of bipolar technology, providing automatic gain control, read-signal processing and generation of head-positioning signals for magnetic heads in disc storage units, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 15 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: SN 28961 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*04	Monolithic integrated analogue circuit, for the reduction of audio noise by 14 dB, contained in a housing the exterior dimensions of which do not exceed 7 × 26 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: HA 12043 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*07	AM and FM receiver of bipolar technology, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 37 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXA 1030 P CXA 1240 P or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 19 80	*08	Delay line of C-MOS technology, for drop-out compensation of video signals, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 7 × 10 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MSM 6965 RS or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80	*10	Monolithic integrated analogue circuit of bipolar technology, for the overload protection of telephone exchanges, contained in a housing the exterior dimensions of which do not exceed 10×11 mm, with not more than 3 connections and bearing: — an identification marking consisting of or including the following combination of figures: 1515 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*11	Programmable filter of bipolar technology, with a programmable cut-off frequency of 5 MHz or more but not exceeding 15 MHz and a programmable peak frequency and bandwidth, comprising a seven-pole filter and a differentiator, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8×20 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 32F8011 32F8012 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*13	Frequency converter for the conversion of frequencies of 10,95 GHz or more but not exceeding 12,5 GHz to frequencies of 950 MHz or more but not exceeding 1750 MHz, in the form of a monolithic integrated analogue circuit, contained in a cylindrical housing with a diameter of not more than 10 mm, with not more than 6 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 20070C AKD12000 AKD12010 AKD12011 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*19	Image sensor consisting of a row of not more than 3 648 photosensitive areas and a matrix linked to shift registers, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 12×43 mm, with not more than 22 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LZ 2019 PD 3573 TCD 103 TCD 105 TCD 133 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*20	Interline charge-coupled image sensor, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 21×32 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ICX 018 ICX 021 ICX 022 ICX 024 ICX 038 ICX 039 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80	*27	5-channel voltage comparator for monitoring lamp-circuits, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 10×28 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 22001 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*28	Image sensor consisting of 16 rows of not more than 992 photosensitive areas and a matrix linked to a shift register, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 12×28 mm, with not more than 22 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: RA 16×62 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*29	Local telephone network of C-MOS technology, capable of tone generation and of switching, amplifying and decoding audio signals from not more than 2 external telephone lines and from not more than 12 internal telephone lines, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 26×26 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: SC 11390 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*30	16-bit stereo encoder/decoder with C-MOS technology, having sample rates of 4 kHz or more but not exceeding 48 kHz, comprising a multiplexer, a digital-to-analogue converter, an analogue-to-digital converter, a mute circuit, a voltage reference circuit, a microphone-input, a loudspeaker-output and a headphone-output, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 18×18 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 1849 CS 4215 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*36	Voice-switched speakerphone circuit of bipolar technology, capable of half duplex transmission and reception of speech, comprising attenuators, level-detectors, a dial-tone detector, an algorithm-control circuit and amplifiers, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8×19 mm, with not more than 28 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: MC 34118 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*43	Bidirectional switch circuit of C-MOS technology, capable of switching analogue signals of -5 V and of +5 V, comprising 4 field-effect transistors (FETs), in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 11 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: SD 5401 CY - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 30	*50	12-bit analogue-to-digital converter of C-MOS technology, comprising a sample and hold amplifier having a dynamic performance of 0,5 MHz per second or more, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 21 × 34 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ADS 111 ADS 112 ADS 117 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 80	*10	Memory module, consisting of 4 electrically erasable, programmable, read only memories (E²PROMs) with a total storage capacity of 2 Mbits or more, mounted on a multilayered ceramic substrate, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 42 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: XM 28C020 XM 28C040 XM 28C4096 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 80	*30	Voltage regulator with an input voltage not exceeding 900 V and a fixed output voltage of 41,8 V (± 0,5 V), in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 29 mm, with not more than 5 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: STR 54041 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 80 00	*02	Unregulated DC converter, with a rated output power not exceeding 1 W at 25°C, in the form of a microassembly, contained in a housing the exterior dimensions of which do not exceed 11 × 20 mm, with not more than 5 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CPR 5848 HPR 116 HPR 405 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 80	*92	Oscillator with a frequency of 1,5 MHz or more but not exceeding 70 MHz, comprising a monolithic integrated circuit not contained in a housing (microchip) and a quartz resonator, both mounted on a support, the whole contained in a housing the exterior dimensions of which do not exceed 9 × 15 mm, with not more than 4 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SG-615 TOC-711 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8548 00 00	*91	Clock/calendar circuit, comprising a lithium battery, a quartz oscillator and a monolithic integrated circuit of C-MOS technology comprising a programmable generator for periodic interruptions and square waves and a static random-access memory (S-RAM), the whole contained in a housing the exterior dimensions of which do not exceed 18 × 34 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DS 1287 DS 1387 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8548 00 00	*93	Memory module, comprising a printed circuit on which are mounted decoupling capacitors and 4 static random-access memories (S-RAMs) with a total storage capacity of 4 Mbits, contained in a housing the exterior dimensions of which do not exceed 15 × 50 mm, with not more than 69 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: HB 6612832IB-7 or — other identification markings relating to devices complying with the abovementioned description	0
ex 9001 20 00	*10	Material consisting of a polarising film, supported on one or both sides by transparent material	0
ex 9001 20 00	*20	Polarising plastic film, consisting of a transparent protective film and a polarising membrane	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 9001 20 00	*30	Polarising lenticular plastic plate with a lenticular array pitch of 0,78 mm, a thickness not exceeding 0,99 mm and the exterior dimensions of which do not exceed 740 × 974 mm	0
ex 9001 90 90	*10	Fresnel lens of plastic, unmounted, for use in the manufacture of products falling within subheadings 8528 10 and 9008 30 (a)	0
ex 9002 11 00	*50	Lens unit, having a focal length of 75 mm or more but not exceeding 94 mm, consisting of glass or plastic lenses, with a diameter of 60 mm or more but not exceeding 180 mm	0
ex 9021 30 90	*21	Vascular prostheses; neither woven nor knitted, of which the largest opening has an internal diameter not exceeding 6 mm	3,1
ex 9110 12 00	*91	Assembly consisting of a printed circuit on which are mounted one quartz oscillator, at least one watch circuit and, whether or not integrated, at least one capacitor, of a thickness not exceeding 5 mm, for use in the manufacture of products falling within Chapter 91 (a)	0
ex 9110 90 00	*92	Assembly consisting of a printed circuit on which is mounted a watch circuit or a watch circuit and a quartz oscillator, of a thickness not exceeding 5 mm, for use in the manufacture of products falling within Chapter 91 (a)	0
ex 9114 90 00	*91		
ex 9110 90 00	*93	Assembly consisting of a printed circuit on which is mounted at least one watch circuit, a quartz oscillator and a piezo-electric sound element, with a thickness exceeding 5 mm, for the manufacture of products falling within Chapter 91 (a)	0

(a) Control of the use for this special purpose shall be carried out pursuant to the relevant Community provisions.

TABLE I

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8471 93 59	*10	Floppy-disc storage units	0
ex 8471 93 59	*20	Disc storage unit, capable of parallel data-transfer via 1, 4, 5 or 6 channels at, respectively, a rate per second of 3,014 megabytes, 12,05 megabytes, 15,07 megabytes or 18,08 megabytes, comprising 8 rigid magnetic discs with an external diameter of 20,32 cm (8 inch) with a total storage capacity, unformatted, not exceeding 1 000,2 megabytes and incorporating a storage-module-drive (SMD) interface	0
ex 8471 93 60	*12	Drive-unit for use in the manufacture of digital audio tape storage units (a)	0
ex 8471 93 60	*20	Magnetic tape storage unit for single-reel cartridges	0
ex 8473 30 10	*30	Component forming the arithmetic/logic element of a central processing unit (CPU), comprising not more than 9 printed circuit boards, the dimensions of which do not exceed 290 × 310 mm, on each of which are mounted not more than 121 ECL gate arrays or ECL random access memories (ECL-RAMs) and combinations thereof, contained in a framework the dimensions of which do not exceed 501 × 596 × 611 mm which serves as a housing and interconnector for the printed circuit boards, and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CO1B 2675 E 500 CO1B 2675 H 501 CO1B 2675 H 503 CO1B 2675 H 500 CO1B 2675 H 502 CO1B 2675 H 504 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 90	*35	Mechanical encoder, consisting of a disc, contact-brushes, a bearing, a spacer and a rotational shaft, mounted in a casing with 3 connections, and having a withstanding voltage of 50 V AC for at least 1 minute	0
ex 8473 30 90	*40	Inkjet printhead	0
ex 8473 30 90	*50	Magnetic bubble memories with a storage capacity of not more than 4 Mbits, contained in a housing the exterior dimensions of which do not exceed 43 × 44 mm, with not more than 56 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or letters: 7110 FBM 54 DB BDL 0133 BDN 0151 MBM 2011 7114-1 FBM 64 DA BDL 0134 BDN 515 MBM 2256 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 40 90	*10	Thermal printer heads of thick or thin film technology, consisting of a printed circuit with at least one tantalum capacitor and an electrolytic capacitor contained in a metal support with connector, printer element and heat sink, supplied with the appropriate support and transport roll	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8483 10 90	*10	Integrally forged and roughly shaped generator and turbine shafts of a weight exceeding 215 t	0
ex 8501 10 99	*52	DC motor, brushless, with a typical running torque not exceeding 0,20 Nm, with a coupling flange of a diameter of 76 mm (± 1 mm), with an outside rotor of a diameter not exceeding 55 mm (± 1 mm), a four-phase winding, a rated speed of 3 600 (± 1 %) rpm, a supply voltage of 12 V (± 10 %) and fitted with wires and connectors	0
ex 8501 10 99	*68	DC motor, brushless, with a typical running torque not exceeding 0,0005 Nm, with a chuck of a diameter of 20 mm ($\pm 0,5$ mm), a rated speed of 3 486 ($\pm 0,1$ %) rpm and a supply voltage of 5 V (± 10 %), mounted on a support the dimensions of which do not exceed 71 $\times$ 103 mm, fitted with a connector and conductor elements	0
ex 8501 10 99	*69	DC motor, brushless, with a typical running torque not exceeding 0,002 Nm, with a coupling flange of a diameter of 27,5 mm ($\pm 0,5$ mm), a coupling flange of a diameter of 15 mm ($\pm 0,5$ mm) and a chuck of a diameter of 25 mm ($\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 3 409 ($\pm 0,1$ %) rpm and a supply voltage of 12 V (± 5 %)	0
ex 8501 10 99	*70	DC motor, brushless, with a typical running torque not exceeding 0,001 Nm, with a coupling flange of a diameter of 57 mm ($\pm 0,5$ mm), a coupling flange of a diameter of 43,5 mm ($\pm 0,5$ mm) and a chuck of a diameter of 25 mm ($\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 3 575 ($\pm 0,1$ %) rpm and a supply voltage of 12 V (± 5 %)	0
ex 8501 10 99	*71	DC bipolar motor, brushless, with a typical running torque not exceeding 0,0223 Nm, with a coupling flange of a diameter of 17,8 mm ($\pm 0,5$ mm) and a chuck of a diameter of 25 mm ($\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 4 498 ($\pm 0,1$ %) rpm and a supply voltage of 12 V (± 10 %)	0
ex 8501 10 99	*72	DC bipolar motor, brushless, with a typical running torque not exceeding 0,00825 Nm, with a coupling flange of a diameter of 27,4 mm ($\pm 0,5$ mm) and a chuck of 25 mm ($\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 4 498 ($\pm 0,1$ %) rpm and a supply voltage of 12 V (± 10 %)	0
ex 8501 10 99	*73	DC motor, brushless, with a typical running torque not exceeding $1,6 \times 10^{-5}$ Nm, with a coupling flange of a diameter of 32 mm ($\pm 0,5$ mm), a chuck of a diameter of 25 mm ($\pm 0,5$ mm), a rated speed of 3 663 ($\pm 0,5$ %) rpm and a supply voltage of 12 V ($+10$ %/ -8 %), integrated on an aluminium baseplate the exterior dimensions of which do not exceed 98 $\times$ 141 mm	0
ex 8501 10 99	*74	DC motor, brushless, with a typical running torque not exceeding 0,0071 Nm, with a coupling flange of a diameter of 12,7 mm ($\pm 0,5$ mm) and a spindle of a diameter of 39,7 mm (± 1 mm), with a rated speed of 3 600 ($\pm 0,1$ %) rpm and a supply voltage of 11 V (± 10 %)	0
ex 8501 10 99	*75	DC motor, brushless, with a typical running torque not exceeding 0,002 Nm, with a coupling flange of a diameter of 27,5 mm ($\pm 0,5$ mm), a coupling flange of a diameter of 15 mm ($\pm 0,5$ mm) and a chuck of a diameter of 25 mm ($\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 3 857 ($\pm 0,1$ %) rpm and a supply voltage of 12 V (± 5 %)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8506 19 19	*10	Lithium iodine single cell battery the dimensions of which do not exceed $9 \times 23 \times 45$ mm and a voltage not exceeding 2,8 V	0
ex 8506 19 19	*20	Unit consisting of not more than 2 lithium batteries embedded in a socket for integrated circuits (battery-buffered socket), with not more than 32 connections and incorporating a control circuit	0
ex 8506 19 99	*10	Dry zinc/carbon batteries of a voltage of 5,5 V or more but not exceeding 6,5 V and the dimensions of which do not exceed $5 \times 90 \times 110$ mm, for incorporation in film cassettes for instant pictures (a)	0
ex 8517 90 81	*10	Assembly for telephonic apparatus comprising a microphone, a protecting circuit and a four-way connecting socket, mounted on a printed circuit the dimensions of which do not exceed 22×40 mm	0
ex 8518 90 00	*91	Integrally cold-upsetted steel coreplate, in the form of a disc on one side provided with a cylinder, for use in the manufacture of loudspeakers (a)	0
ex 8522 90 91	*91	Optical unit consisting of a laser diode with one photodiode, emitting light of a nominal wavelength of 780 nm, contained in a housing with a diameter of not more than 10 mm and a height of not more than 9 mm, with not more than 10 connections and bearing: — an identification marking consisting of or including one of the following combination of letters or of figures and letters: LDGU LT 022 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8522 90 99	*91	Single cassette-deck sub-assembly with a total thickness not exceeding 53 mm, for use in the manufacture of sound recording and reproducing apparatus (a)	0
ex 8522 90 99	*93	Cassette-deck sub-assembly for sound recording and reproducing apparatus, for use in the manufacture of telephone answering machines (a)	0
ex 8523 20 10	*40	Rigid magnetic discs, prelubricated, oxide type, with a coercivity of 300 Oe or more	0
ex 8523 20 10	*50	Rigid magnetic disc with a thin-film metallic coating, having a coercivity exceeding 600 Oe and an external diameter not exceeding 231 mm	0
ex 8529 10 70	*10	Ceramic filter package comprising 2 ceramic filters and 1 ceramic resonator for a frequency of 10,7 MHz (± 30 kHz), contained in a housing the exterior dimensions of which do not exceed 10×10 mm	0
ex 8529 10 70	*20	Ceramic filters for frequencies of 4,5 MHz or more but not exceeding 6,6 MHz contained in a housing the dimensions of which do not exceed 9×24 mm	0
ex 8529 90 98	*91	Device consisting only of an interline charge-coupled image sensor with filter, the dimensions of which do not exceed 43×59 mm	0

CN code	TARIC	Description	Rate of autonomous duty (%)								
ex 8531 20 90	*10	Dot matrix display consisting of a line of 8 characters, each character composed of 35 light-emitting diodes (LEDs), comprising electronic components for interface and drive functions, contained in a housing the exterior dimensions of which do not exceed 20×43 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table> <tr> <td>HDSP 2107</td><td>HDSP 2112</td><td>PDSP 2110</td><td>PDSP 2112</td></tr> <tr> <td>HDSP 2111</td><td>HDSP 2113</td><td>PDSP 2111</td><td>PDSP 2113</td></tr> </table> or — other identification markings relating to devices complying with the abovementioned description	HDSP 2107	HDSP 2112	PDSP 2110	PDSP 2112	HDSP 2111	HDSP 2113	PDSP 2111	PDSP 2113	0
HDSP 2107	HDSP 2112	PDSP 2110	PDSP 2112								
HDSP 2111	HDSP 2113	PDSP 2111	PDSP 2113								
ex 8531 20 90	*45	Digital displays, consisting of a printed circuit board of a size not exceeding 35×90 mm with a single line of characters, not less than 3 in number, comprising light-emitting diodes (LEDs) made from gallium-based semiconductor compounds mounted thereon. Each character is composed of up to 8 segments with or without a decimal point and the line of characters has a protective cover of plastic	0								
ex 8531 80 90	*10	Direct current plasma display	0								
ex 8534 00 11 ex 8534 00 19	*91 *91	Single-face printed circuit the dimensions of which do not exceed 30×30 mm, for the manufacture of products falling within Chapter 91 (a)	0								
ex 8534 00 11	*92	Multiple printed circuit, consisting of 24 layers, including 5 layers with buried vias of bismaleimide triazine, the exterior dimensions of which do not exceed 64×65 cm	0								
ex 8534 00 19	*92	Single-face printed circuits, each with not more than 268 conductive leads, on a plastic tape with sprocket holes on both edges and having a width of not more than 35 mm and a thickness of not more than 0,26 mm	0								
ex 8534 00 19	*93	Printed circuit on one or both sides of a ceramic substrate, consisting of conductor elements and contacts, the dimensions of which do not exceed 36×36 mm, with not more than 304 connections	0								
ex 8536 41 10 ex 8536 41 90 ex 8536 49 00	*91 *91 *91	Thermal relays contained in a hermetically sealed glass cartridge not exceeding 35 mm in length excluding wires, with a maximum leakage rate of 10 ⁻⁶ cm ³ He/sec at one bar in the temperature range 0 to 160 °C, to be incorporated into compressors for refrigerating equipment (a)	0								
ex 8536 50 19 ex 8536 50 90	*91 *91	Reed switches in the form of a glass capsule containing a small quantity of mercury and not more than 3 electrical contacts on metal arms	0								
ex 8538 90 90	*91	Part of an electrothermal fuse, consisting of a tin coated copper wire attached to a cylindrical casing, the exterior dimensions of which do not exceed 5×48 mm	0								
ex 8540 11 10	*93	Colour cathode-ray tube with a slot mask, equipped with electron guns placed side by side (in-line technology) and with a diagonal measurement of the screen of 12 cm or more but not exceeding 26 cm	0								
ex 8540 12 10	*91	Flat screen monochrome cathode-ray tube with a diagonal measurement of the screen of 150 mm or more but not exceeding 155 mm and an anode voltage of 25 kV or more but not exceeding 32 kV	0								

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8540 20 90	*91	Photomultiplier consisting of a photocathode tube with 9 dynodes, for light of wavelength of 160 nm or more but not exceeding 930 nm, of a diameter not exceeding 14 mm and a height not exceeding 94 mm	0
ex 8540 30 10	*25	Colour cathode-ray tube with a dot mask, equipped with electron guns in a triangular fashion (delta technology), with a diagonal measurement of the screen of 66 cm or more	0
ex 8540 30 10	*34	Colour cathode-ray tube with a dot mask, equipped with electron guns placed side by side (in-line technology), with a diagonal measurement of the screen of more than 72 cm and a distance of less than 0,5 mm between dots of the same colour	0
ex 8540 30 10	*35	Colour cathode-ray tube with a dot mask, equipped with electron guns placed side by side (in-line technology), having a diagonal measurement of the screen not exceeding 72 cm	0
ex 8540 30 90	*26	Flat screen monochrome cathode-ray tube, with a diagonal measurement of the screen of 142 mm or more but not exceeding 190 mm, a luminescence of 300 lumen or more but not exceeding 2 000 lumen, a resolution of 0,06 mm or more but not exceeding 0,1 mm, phosphor types P1 or P22 or P53 or P55 or P56, an anode voltage of more than 34 kV, a focus voltage of more than 7 kV and a cathode current of 3 mA or more	0
ex 8540 89 11	*91	Displays in the form of a tube consisting of a glass housing mounted on a board the dimensions of which do not exceed 300×350 mm excluding leads. The tube contains one or more rows of characters or lines arranged in rows, each character or line consisting of fluorescent or phosphorescent elements. These elements are mounted on a metallised base which is covered with fluorescent substances or phosphorescent salts which give off light when bombarded with electrons	0
ex 8540 91 00	*91	Deflector yoke for cathode-ray tubes with an operating frequency of 31 250 Hz or more but not exceeding 64 000 Hz, incorporating a quadripolar magnet	0
ex 8540 91 00	*93	Electron gun for the production of monochrome cathode-ray tubes with a diagonal measurement of the screen of 7,6 cm or more but not exceeding 30,5 cm (a)	0
ex 8540 99 00	*91	Anode, cathode or output part, for the manufacture of magnetrons of subheading 8540 41 00 (a)	0
ex 8541 10 91	*10	Silicon power rectifier diodes of planar technology, with a recovery time of less than 100 ns, a maximum recurring reverse voltage of 200 V, and average forward current of 2,5 A or more, contained in a flat housing the exterior dimensions of which exceed 3×9×9 mm but do not exceed 5×11×17 mm	0
ex 8541 10 99	*20	Silicium diode with a variable capacitance, capable of a capacitance change of more than 10 times in relation to its starting value by variation of the operating voltage of 2 V or more but not exceeding 10 V and with a capacitance of 96 pF or more but not exceeding 144 pF at 2 V, contained in a cylindrical housing having a diameter of not more than 3 mm and a height of not more than 8 mm, with not more than 2 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MV 1404 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8541 21 90	*10	High electron mobility transistor (HEMT), for frequencies of 2 GHz or more but not exceeding 20 GHz, with a dissipation rate not exceeding 180 mW, contained in a housing with a diameter not exceeding 3 mm, with not more than 4 connections	0
ex 8541 21 90	*20	Field-effect transistor (FET) for frequencies of 2 GHz or more but not exceeding 16 GHz, with a dissipation rate not exceeding 225 mW, contained in a housing with a diameter not exceeding 3 mm, with not more than 4 connections	0
ex 8541 29 90	*10	Transistor with a power of 150 W or more at a voltage of 160 V or more and with a cut-off frequency of 20 MHz or more, contained in a housing the exterior dimensions of which do not exceed 22 × 37 mm, with not more than 3 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 2 SA 1170 2 SA 1215 2 SA 1494 2 SC 2774 2 SC 2921 2 SC 3858 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 90	*30	Field-effect transistor (FET), having a drain-to-source breakdown-voltage of 50 V, operating with a drain-current of 8,2 A, a drain-to-source resistance not exceeding 0,2 ohm, and with a dissipation rate not exceeding 25 W, contained in a housing the exterior dimensions of which do not exceed 6 × 7 mm, with not more than 3 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: IRFR 010 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 40 10	*60	Light-emitting diode (LED), made from gallium-arsenic-phosphor (GaAsP) semiconductor, operating at a nominal wavelength of 710 nanometers, in the form of a monolithic integrated circuit without a housing (chips), for the manufacture of optocouplers (a)	0
ex 8541 40 10	*70	Light-emitting diode (LED), made from aluminium-gallium-arsenic (AlGaAs) semiconductor, having a square base with an edge length not exceeding 8,2 mm, having a primary lens	0
ex 8541 40 10	*80	Light-emitting diode (LED) of Transparent Substrate (TS) technology, made from aluminium-gallium-arsenic (AlGaAs) semiconductor, having a luminous intensity of 1,4 candela or more at 20 mA	0
ex 8541 40 91	*91	Module consisting of not more than five solar cells of thin-film technology, on a substrate the exterior dimensions of which do not exceed 18 × 70 mm	0
ex 8541 60 00	*91	Piezo-electric crystal oscillating at a frequency of 32 768 Hz, contained in a cylindrical housing of a length not exceeding 8,2 mm and a diameter not exceeding 3,2 mm, for the manufacture of products falling within Chapter 91 (a)	0
ex 8541 60 00	*92	Polarised ceramic piezo-electric crystal oscillating in a frequency range of 500 kHz or more but not exceeding 12 500 kHz, contained in a housing the exterior dimensions of which do not exceed 14 × 15 mm, with not more than 3 connections	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 01	*30	Wafer, not yet cut into chips, consisting only of static random-access memories (S-RAMs) with a storage capacity of 1 Mbit, for the manufacture of goods of subheading 8542 11 25 contained in a housing the exterior dimensions of which do not exceed 14 × 42 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including the following combination of figures: 431000 - or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*50	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers with an arithmetic-logic unit (ALU) with a processing capacity of 16 bits and an 8-bit data-bus, comprising a read only memory, non-programmable (ROM) with a storage capacity not exceeding 128 Kbits and a random-access memory (RAM) with a storage capacity not exceeding 2 Kbits, for the manufacture of goods of subheading 8542 11 73 contained in a housing the exterior dimensions of which do not exceed 18 × 43 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 78C11 78C12 78C14 - or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*10	Control and driver circuit for dot-matrix liquid crystal displays (LCDs) of C-MOS technology, with a character generator, and having a drive voltage of more than 10 V, in the form of a monolithic integrated circuit without a housing (microchip), for the manufacture of liquid crystal display (LCD) modules (a)	0
ex 8542 11 05	*20	Driver circuit for liquid crystal displays (LCDs) of C-MOS technology, with 40 or more output channels, and having a drive voltage of more than 5 V, in the form of a monolithic integrated circuit not contained in a housing (microchip), for the manufacture of liquid crystal displays (LCDs) or assemblies therefore (a)	0
ex 8542 11 12	*01	Dynamic random-access memory of N-MOS (including H-MOS) technology (N/H-MOS D-RAM) with a storage capacity of 64 Kbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 24 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: KM 4164 MN 4264 TMS 4164 TMS 4416 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 12	*11	Dynamic random-access memory of N-MOS (including H-MOS) technology (N/H-MOS D-RAM), with a storage capacity of 256 Kbits and an access time not exceeding 150 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 34 mm, with not more than 24 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)																				
ex 8542 11 12 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>HB 50562</td><td>KM 41257</td><td>MB 81464</td><td>PD 41256</td><td>TMS 4256</td></tr><tr><td>HM 50256</td><td>M5M 4256</td><td>MSM 4256</td><td>PD 41464</td><td>TMS 4464</td></tr><tr><td>HM 50464</td><td>M5M 4464</td><td>MSM 4464</td><td>TMM 41256</td><td></td></tr><tr><td>KM 41256</td><td>MB 81256</td><td>PD 41254</td><td>TMM 41464</td><td></td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	HB 50562	KM 41257	MB 81464	PD 41256	TMS 4256	HM 50256	M5M 4256	MSM 4256	PD 41464	TMS 4464	HM 50464	M5M 4464	MSM 4464	TMM 41256		KM 41256	MB 81256	PD 41254	TMM 41464		8
HB 50562	KM 41257	MB 81464	PD 41256	TMS 4256																			
HM 50256	M5M 4256	MSM 4256	PD 41464	TMS 4464																			
HM 50464	M5M 4464	MSM 4464	TMM 41256																				
KM 41256	MB 81256	PD 41254	TMM 41464																				
ex 8542 11 12	*12	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 256 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>53 C 256</td><td>53 C 466</td><td>MB 81 C 466</td><td>TC 51832</td></tr><tr><td>53 C 258</td><td>HM 65256</td><td>P 51 C 256</td><td></td></tr><tr><td>53 C 464</td><td>MB 81 C 258</td><td>P 51 C 259</td><td></td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	53 C 256	53 C 466	MB 81 C 466	TC 51832	53 C 258	HM 65256	P 51 C 256		53 C 464	MB 81 C 258	P 51 C 259		0								
53 C 256	53 C 466	MB 81 C 466	TC 51832																				
53 C 258	HM 65256	P 51 C 256																					
53 C 464	MB 81 C 258	P 51 C 259																					
ex 8542 11 12	*13	Dual port dynamic random-access memory (D-RAM), with data registers and a serial read output control, with a storage capacity of 256 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 39 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>M5M 4 C 264</td><td>MB 81461</td><td>PD 41264</td><td>TMS 4461</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	M5M 4 C 264	MB 81461	PD 41264	TMS 4461	0																
M5M 4 C 264	MB 81461	PD 41264	TMS 4461																				
ex 8542 11 14	*01	Random-access memory with a storage capacity of 1 Mbit, with separate in- and outputs and serial shift registers (so-called field memories), of C-MOS technology (C-MOS RAM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 54 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TC 521000 or — other identification markings relating to devices complying with the abovementioned description	0																				
ex 8542 11 14	*02	Dual or triple port dynamic random-access memory (D-RAM), with data registers and a serial read output control, with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 20 × 51 mm, with not more than 52 connections and bearing:																					

CN code	TARIC	Description	Rate of autonomous duty (%)												
ex 8542 11 14 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>TC 524256</td><td>TC 528128</td><td>MT 42 C 4256</td><td>MT 43 C 8128</td></tr><tr><td>TC 524257</td><td>M5M 442256</td><td>MT 43 C 4257</td><td>TMS 44 C 251</td></tr><tr><td>TC 528126</td><td>MB 81 C 4251</td><td>MT 43 C 4258</td><td></td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	TC 524256	TC 528128	MT 42 C 4256	MT 43 C 8128	TC 524257	M5M 442256	MT 43 C 4257	TMS 44 C 251	TC 528126	MB 81 C 4251	MT 43 C 4258		0
TC 524256	TC 528128	MT 42 C 4256	MT 43 C 8128												
TC 524257	M5M 442256	MT 43 C 4257	TMS 44 C 251												
TC 528126	MB 81 C 4251	MT 43 C 4258													
ex 8542 11 21	*01	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 16 × 4 bits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 21 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>CY27S03</td><td>CY54S189</td><td>CY7C189</td></tr><tr><td>CY27S07</td><td>CY74S189</td><td>CY7C190</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	CY27S03	CY54S189	CY7C189	CY27S07	CY74S189	CY7C190	0						
CY27S03	CY54S189	CY7C189													
CY27S07	CY74S189	CY7C190													
ex 8542 11 21	*17	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 256 × 4 bits and an access time not exceeding 60 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 33 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>CY7C122</td><td>CY7C123</td><td>CY93422</td><td>CY93L422</td><td>P4C 422</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	CY7C122	CY7C123	CY93422	CY93L422	P4C 422	0							
CY7C122	CY7C123	CY93422	CY93L422	P4C 422											
ex 8542 11 21	*28	Static random-access memory (S-RAM), with a storage capacity of 1 K × 4 bits and an access time not exceeding 25 ns, in the form of a monolithic integrated circuit contained in a housing the exterior dimensions of which do not exceed 8 × 32 mm, with not less than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>AM 9150-20</td><td>CY 7 C 150-15</td></tr><tr><td>AM 9150-25</td><td>CY 7 C 150-25</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	AM 9150-20	CY 7 C 150-15	AM 9150-25	CY 7 C 150-25	0								
AM 9150-20	CY 7 C 150-15														
AM 9150-25	CY 7 C 150-25														
ex 8542 11 21	*32	Static random-access memory of N-MOS (including H-MOS) technology (N/H-MOS S-RAM), with a storage capacity of 8 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 24 connections and bearing:													

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 21 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures or figures and letters: 4008 4801 8108 8114 PD 421 4118 8104 8112 8185 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*43	Dual port static random-access memory of BiMOS technology (BiMOS dual port S-RAM), with a storage capacity of 32 Kbits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 62 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7B134 CY7B1342 CY7B135 CY7B138 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*48	Static random-access cache memory of C-MOS technology (C-MOS S-Cache-RAM), with a storage capacity of 4 K × 16 bits and an access time not exceeding 55 ns, comprising a 12-bits address latch and control circuits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 53 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: IDT71586S - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*49	Dual port static random-access memory of BiMOS technology (BiMOS dual port S-RAM), with a storage capacity of 64 Kbits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CY7B144 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*01	Static random-access cache memory of BiMOS technology (BiMOS S-Cache-RAM), with a storage capacity of 72 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7B180 CY7B181 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 23	*19	Static random-access cache memory of C-MOS technology (C-MOS S-Cache-RAM), with a storage capacity of 144 Kbits and an access time not exceeding 30 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 21 × 21 mm, with not more than 52 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TC 55187 TC 55188 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*20	Dual port static random-access cache memory of C-MOS technology (C-MOS dual port S-Cache-RAM), with a storage capacity of 256 Kbits, comprising a bus-memory control circuit, a bus control circuit of a central processing unit (CPU), an address-latch and 2 multiplexers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 45 × 45 mm, with not more than 84 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 82490XP - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*22	Static asynchronous random-access memory of C-MOS technology (C-MOS asynchronous S-RAM), with a storage capacity of 16 K × 16 bits, excluding static random-access cache memory (S-Cache-RAM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 21 × 21 mm, with not more than 52 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MCM 62995-17 MCM 62995-20 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 25	*11	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 512 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 28 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TC 551632 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 31	*01	Read only memory, non-programmable in C-MOS technology (C-MOS ROM), with a storage capacity of 256 Kbits, in the form of a monolithic integrated circuit contained in a housing the exterior dimensions of which do not exceed 17 × 50 mm, with not more than 54 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 31 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: HN 61256 MB 83256 TMS 60C19 HN 613256 TMS 60C18 TMS 60C20 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 31	*06	Read only memory, non-programmable of C-MOS technology (C-MOS ROM) with a storage capacity of 1 Mbit, comprising a serial data output port, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 43 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TMS 60C80 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 42	*01	UV erasable, programmable, read only memory (EPROM), with a storage capacity of 16 Kbits and an access time not exceeding 65 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with a quartz window on the upper surface and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY 7C 245 W CY 7C 291 W or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 42	*06	UV-erasable, programmable, read only memory (EPROM), with a storage capacity of 32 Kbits and an access time not exceeding 45 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with a quartz window on the upper surface and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 27 CX 321 27 CX 322 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 42	*11	UV erasable, programmable, read only memory (EPROM), with a storage capacity of 64 Kbits and an access time not exceeding 65 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with a quartz window on the upper surface and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY 7C 261 W CY 7C 263 W CY 7C 268 W CY 7C 269 W or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 42 ex 8542 11 59	*16 *58	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 128 Kbits and an access time not exceeding 100 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 32 connections, with or without a quartz window on the upper surface and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY 7C 251 CY 7C 254 XC 17128 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 44 ex 8542 11 59	*01 *64	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM) with a storage capacity of 1 Mbit and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 40 connections, with or without a quartz window on the upper surface and bearing : — an identification marking consisting of or including one of the following combinations of figures and letters: CY7B201 CY7B210 CY7B211 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 44 ex 8542 11 59	*07 *68	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 512 Kbits and an access time not exceeding 75 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 43 mm, with not more than 32 connections, with or without a quartz window on the upper surface and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 27 C 512 CY 7C 285-75 CY 7C 287 CY 7C 289-75 CY 7C 285-65 CY 7C 286 CY 7C 289-65 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 46	*02	Flash electrically erasable, programmable, read only memory (Flash-E²PROM) with a storage capacity of 4 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 21 × 43 mm, with not more than 56 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 28 F 040 TC 584000 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 48 ex 8542 11 59	*01 *66	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 8 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 56 mm, with not more than 42 connections, with or without a quartz window on the upper surface and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 48 ex 8542 11 59 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: TC 578200 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*21	FIFO (first in, first out), read/write memory of C-MOS technology, with a storage capacity of 64×8 bits or 64×9 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12×38 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C408A CY7C409A - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*24	FIFO (first in, first out) read/write memory, with a storage capacity of 7 280 or 9 080 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12×36 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: PD 41101 PD 41102 PD 42101 PD 42102 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*26	FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signal, with a storage capacity of 512×9 bits and an access time not exceeding 40 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12×43 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C441 CY7C451 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*31	FIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 72 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14×38 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C460 CY7C470 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 59	*33	FIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 144 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14×38 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C462 CY7C472 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*35	FIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 288 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14×38 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C464 CY7C474 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*48	Programmable, non-erasable, read only memory (PROM), with a storage capacity of 16 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×39 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 7 C 245 7 C 291 7 C 292 HM 6616 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*52	Programmable, non-erasable, read only memory (PROM) of C-MOS technology with a storage capacity of 36 288 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16×16 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: XC 1736 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 59	*54	Programmable, non-erasable, read only memory (PROM), with a storage capacity of 64 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×44 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C261 CY7C264 CY7C269 MB7144 XC 1764 CY7C263 CY7C268 MB7143 MB71C44 XC 1765 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 62 ex 8542 11 64 ex 8542 11 67	*01 *01 *01	Co-processor of C-MOS technology, for multiple interfacing a parallel system bus to 8, 16 or 32 bits central processing units (CPUs), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 41 × 41 mm, with not more than 149 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 82389 82C389 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 62	*21	Microprocessor of N-MOS (including H-MOS) technology, with a processing capacity of 8 bits, for the encoding/decoding of data, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 53 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 8294 Z 8068 Z 9518 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 62	*22	Microprocessor of C-MOS technology, with a processing capacity of 8 bit, comprising a central processing unit (CPU), a memory management unit, a two-channel DMA controller, two-channel programmable 16-bit counter/timer, a wait-state generator, a two-channel asynchronous serial communication interface (ASCI) and a bus controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26 × 62 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HD 64 A 180 HD 64 B 180 Z 64180 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 62	*23	Microprocessor of C-MOS technology, with a processing capacity of 8 bits, comprising a modulation circuit for recording signals, a demodulation circuit for reproducing signals, an error-correction code circuit (ECC), an analogue-to-digital/digital-to-analogue converter, a static random-access memory (S-RAM) interface, an input/output circuit for interfacing a microcontroller or microcomputer, an input/output circuit for interfacing an audio system and a phase-locked loop (PLL) circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 21 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CXD2601 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 62 ex 8542 11 64	*41 *31	Multiprotocol microprocessor of C-MOS technology, with a processing capacity of 8 or 16 bits, comprising a central processing unit (CPU), a direct memory access controller, an interrupt controller, a dual port random-access memory (Dual Port RAM) with a storage capacity of 9 216 bits, 3 timers, a communication processor and 3 full duplex serial communication controllers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 36 × 36 mm, with not more than 132 connections and bearing : — an identification marking consisting of or including the following combination of figures and letters: MC 68302 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 64	*02	Microprocessor of C-MOS technology, with a processing capacity of 16 bits, comprising a multiplier/accumulator (MAC), an arithmetic-logic shifter, a static random-access memory (S-RAM) with a storage capacity of 48 Kbits and having the function of programme memory, a static random-access memory (S-RAM) with a storage capacity of 16 Kbits and a programmable timer, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 80 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ADSP 2101 ADSP 2102BS-50 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 64	*21	Microprocessor of N-MOS (including H-MOS) technology, with a processing capacity of 16 bits and an 8-bit address/data-bus, comprising a central processing unit (CPU), a timing generator, 2 independent direct memory access (DMA) channels, a programmable interrupt controller, 3 programmable 16-bit timers, programmable memory and external chip selection logic, a programmable wait state generator and a bus controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures: 80188 or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 11 64	*22	Microprocessor of C-MOS technology, with a processing capacity of 16 bits and an 8-bit data-bus, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 36 × 60 mm, with not more than 120 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters : MB 89T713 MB 89T715 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 64	*23	Microprocessor of C-MOS technology, with a processing capacity of 10 bits, for text data decoding, comprising a prefix processor for serial to parallel data conversion, a video display processor, a memory access controller, a display memory and an interface circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 37 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CF 70064 CF 70095 CF 72301 CF 72305A or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 64	*34	Microprocessor in N-MOS (including H-MOS) technology, with a processing capacity of 16 bits, comprising a central processing unit (CPU), a timing generator, two independent direct memory access (DMA) channels, a programmable interrupt controller, 3 programmable 16-bit timers, programmable memory and external chip selection logic, a programmable wait state generator and a bus control unit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures: 80186 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 64	*35	Microprocessor of N-MOS technology (including H-MOS), with a processing capacity of 16 bits, comprising a central processing unit (CPU), a memory management unit (MMU) and a real address and virtual address operating mode system (OSO), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures: 80286 or — other identification markings relating to devices complying with the abovementioned description	10
ex 8542 11 64	*36	Communications processor of N-MOS (including H-MOS) technology, with a processing capacity of 16 bits, comprising a random-access memory (RAM) with a storage capacity of 22 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 60 mm, with not more than 48 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TMS 38010 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 64	*40	Microprocessor of C-MOS technology, with a processing capacity of 16 bits, comprising a central processing unit (CPU) and an 8-bit or 16-bit external data-bus, contained in a housing the exterior dimensions of which do not exceed 36 × 54 mm, with not more than 132 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)												
ex 8542 11 64 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: <table> <tr> <td>80 C 186</td><td>80 C 296</td><td>DSP 56156</td><td>Z 280</td></tr> <tr> <td>80 C 188</td><td>ADSP 2100</td><td>DSP 56200</td><td>Z 70108</td></tr> <tr> <td>80 C 196</td><td>DSP 56116</td><td>XSP 56200</td><td>Z 70116</td></tr> </table> or — other identification markings relating to devices complying with the abovementioned description	80 C 186	80 C 296	DSP 56156	Z 280	80 C 188	ADSP 2100	DSP 56200	Z 70108	80 C 196	DSP 56116	XSP 56200	Z 70116	0
80 C 186	80 C 296	DSP 56156	Z 280												
80 C 188	ADSP 2100	DSP 56200	Z 70108												
80 C 196	DSP 56116	XSP 56200	Z 70116												
ex 8542 11 64	*42	Communication processor of C-MOS technology, with a processing capacity of 16 bits, comprising a coder/decoder for the conversion of data into serial/parallel signals, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 28 × 28 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TMS 380 C 16 or — other identification markings relating to devices complying with the abovementioned description	0												
ex 8542 11 64	*43	Microprocessor of C-MOS technology, with a processing capacity of 16 bits, comprising a 4-channel direct memory access (DMA) controller, a universal asynchronous receiver/transmitter (UART), three 16-bit timer/counters, an 8-channel interrupt-controller, a memory-refresh controller, a clock-generator and a bus-controller with wait-state generator, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 28 × 29 mm, with not more than 120 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: PD 70236 or — other identification markings relating to devices complying with the abovementioned description	0												
ex 8542 11 64	*44	Microprocessor of C-MOS technology, with a processing capacity of 16 bits, comprising 2 timers, a dynamic random-access memory (D-RAM) controller, a flash electrically erasable, programmable, read only memory (Flash E²PROM) controller, an electrically erasable, programmable, read only memory (E²PROM) controller, an universal asynchronous receiver/transmitter (UART), an address decoder circuit and registers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 1TX9-0301 or — other identification markings relating to devices complying with the abovementioned description	0												

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 67	*05	Coprocessor of C-MOS technology, with a processing capacity of 32 bits, for a local area network, comprising a data interface unit, a bus interface unit, a direct memory access (DMA) controller and FIFO (first in, first out) read/write memories with a total storage capacity of 1 536 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 63 × 63 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including the following combination of figures: 82596 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 67	*31	Microprocessor of N-MOS (including H-MOS) technology, with a processing capacity of 32 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 38 × 38 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CPU 04041871 NCR 32 000 NS 32032 NS 32332 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 67	*32	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising a floating point arithmetic unit with four 80-bit registers, thirty six 32-bit registers, a cache memory with a storage capacity of 4 Kbits and an interrupt controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 37 × 37 mm, with not more than 132 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 80960KB - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 67	*33	Microprocessor with a processing capacity of 32 bits, comprising one or more random-access memories (RAMs) with a total storage capacity not exceeding 48 Kbits and a floating decimal point arithmetic unit with a capacity of 32 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 35 × 35 mm, with not more than 164 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: DSP 32 C - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 67	*34	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising a binary arithmetic-logic unit, a floating point arithmetic-logic unit, a decimal arithmetic-logic unit, a control store unit, a virtual memory management unit and registers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 48 × 48 mm, with not more than 223 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 67 (cont'd)		— an identification marking consisting of or including the following combination of figures: 78201441 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 67	*36	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, with a 16-bit external data-bus and a 24-bit external address-bus, with a virtual storage address capacity of 64 terabytes, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 80386 SX - or — other identification marking relating to devices complying with the abovementioned description	0
ex 8542 11 67	*39	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising a 32-bit external data-bus and a 26-bit external address-bus, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 86C020 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 67	*40	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising a static random-access cache memory (S-Cache-RAM) with a storage capacity of 64 Kbits for instruction and data stream and a static random-access cache memory (S-Cache-RAM) with a storage capacity of 16 Kbits for instruction stream only, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 62 × 62 mm, with not more than 431 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: DC 262 A - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*21	Microcontroller or microcomputer of N-MOS (including H-MOS) technology, with a processing capacity of 8 bits, comprising an UV-erasable, programmable, read only memory (EPROM) with a storage capacity of either 14 432 bits, 30 016 bits or 30 208 bits, a random-access memory (RAM) with a storage capacity of either 832 bits or 896 bits, a read only memory, non-programmable (ROM) with a storage capacity of either 920 bits, 1 528 bits or 1 728 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 54 mm, with not more than 44 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: MC 68705P3 MC 68705R3 MC 68705S3 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*22	Microcontroller or microcomputer with a processing capacity of 8 bits, comprising a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a capacity of 32 Kbits and a random-access memory (RAM) with a capacity of 1 Kbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×53 mm, with not more than 44 connections, with or without a quartz window on the upper surface and bearing: — an identification marking consisting of or including one of the following combinations of figures: 7742 8751 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*23	Microcontroller or microcomputer of N-MOS (including H-MOS) technology, with a processing capacity of 8 bits, having peripheral interface functions, comprising a random-access memory (RAM) with a storage capacity not exceeding 2 Kbits, a read only memory, non-programmable (ROM), a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 16 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×53 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 8042 8742 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*24	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, having a register-to-register architecture, comprising a static random-access memory (S-RAM) with a storage capacity of not more than 12 Kbits and a programmable, non-erasable, read only memory (PROM) or an UV-erasable, programmable, read only memory (EPROM) or an electrically erasable, programmable, read only memory (E²PROM), with a storage capacity of not more than 256 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30×30 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 370C010 370C056 370C250 370C350 370C732 370C850 370C032 370C058 370C256 370C352 370C756 374C036 370C050 370C150 370C310 370C356 370C758 370C052 370C156 370C332 370C358 370C810 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71	*25	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a random-access memory (RAM) with a storage capacity of 256 bits, a read only memory, non-programmable (ROM) with a storage capacity of 16 Kbits, an electrically erasable, programmable, read only memory (E²PROM) with a storage capacity of 2 Kbits, a clock generator, a 4-bit timer, a serial communications interface and a 14-bit digital-to-analogue converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 29 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: HD 401220 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*26	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a programmable, non-erasable, read only memory (PROM) or an UV erasable, programmable, read only memory (EPROM) with a storage capacity of 60 Kbits or more but not exceeding 128 Kbits and a random-access memory (RAM) with a storage capacity not exceeding 3 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26 × 54 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 87C51 87C54 MC68HC705C8 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*29	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a random-access memory (RAM) with a storage capacity of 4 Kbits, a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or an UV erasable, programmable, read only memory (EPROM), with a storage capacity of 96 Kbits, an electrically erasable programmable, read only memory (E²PROM) with a storage capacity of 4 Kbits and an 8-channel analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 20 × 20 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 68HC11E9 MC 68HC711E9 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*30	Microcontroller or microcomputer of C-MOS-technology, with a processing capacity of 8 bits, consisting of a read only memory, non-programmable (ROM) with a storage capacity of 192 Kbits, a random-access memory (RAM) with a storage capacity of 4,5 Kbits and an arithmetic coprocessor, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 21 mm, with not more than 80 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: CXP80524 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*32	Microcontroller or microcomputer with a processing capacity of 8 bits, having universal peripheral interface functions, comprising a random-access memory (RAM) with a storage capacity of 1 Kbit, an UV erasable, programmable, read only memory (EPROM) with a storage capacity of 2 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×53 mm, with not more than 44 connections, with a quartz window on the upper surface, and bearing: — an identification marking consisting of or including the following combination of figures and letters: D 8742 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 71	*34	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a random-access memory (RAM) with a storage capacity of 2 or 8 Kbits, an electrically erasable, programmable, read only memory (E²PROM) with a storage capacity of 4 Kbits and an 8-channel analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 25×25 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 68HC11F SC 415112FU SC 805666FN SC 415111FU SC 415016FU - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 73	*01	Microcontroller or microcomputer of N-MOS technology (including H-MOS), with a processing capacity of 16 bits, comprising at least one read only memory, non-programmable (ROM) with a storage capacity of 510×13 bits or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 512×13 bits, a random-access memory (RAM) with a storage capacity of 2 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16×37 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: PD 7720 PD 77 P 20 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 73	*02	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits and a 16-bit address-bus and an 8-bit data-bus, comprising a random-access memory (RAM) with a storage capacity of 4 Kbits or more, a read only memory, non-programmable (ROM) or a programmable non-erasable read only memory (PROM) or a UV-erasable, programmable, read only memory (EPROM) with a storage capacity of 128 Kbits or more, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 36 × 60 mm, with not more than 120 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MB 89715 MB 89P715 MB 89W715 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 73	*04	Microcontroller or microcomputer of C-MOS technology, with an arithmetic-logic unit (ALU) with a processing capacity of 16 bits and an 8-bit data-bus, comprising a read only memory, non-programmable (ROM) with a storage capacity of 128 Kbits, a random-access memory (RAM) with a storage capacity of 2 Kbits, an 8-channel analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26 × 42 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: PD 78C14 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 73	*07	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising a multiplier/accumulator (MAC), an arithmetic-logic shifter, a microprocessor interface port, a read only memory (ROM) with a storage capacity of 48 Kbits, a static random-access memory (S-RAM) with a storage capacity of 16 Kbits, an analogue-to-digital converter, a digital-to-analogue converter and a programmable timer, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 40 × 40 mm, with not more than 144 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 21msp52BS-52 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 73	*09	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising sixteen 8-bit or eight 16-bit registers, a read only memory, non-programmable (ROM) or a programmable, read only memory (PROM), with a storage capacity of 128 Kbits, a random-access memory (RAM) with a storage capacity of 4 Kbits, 3 timers, a serial communications interface, an 8-channel analogue-to-digital converter and 9 input/output ports, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 84 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)												
ex 8542 11 73 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: HD 6473308CP - or — other identification markings relating to devices complying with the abovementioned description	0												
ex 8542 11 74	*01	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 24 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 60 Kbits or more, random-access memories (RAMs) with a total storage capacity of 12 Kbits or more and a 24-bit floating point data arithmetic unit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 40×40 mm, with not more than 135 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters : MB 86220 - or — other identification markings relating to devices complying with the abovementioned description	0												
ex 8542 11 74	*08	Microcontroller or microcomputer, with an arithmetic-logic unit (ALU) with a processing capacity of 32 bits and a 16-bit data-bus, comprising random-access memories (RAMs) with a total storage capacity not exceeding 160 Kbits, a read only memory, non-programmable (ROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity not exceeding 256 Kbits, in the form of a monolithic integrated circuit contained in a housing the exterior dimensions of which do not exceed 29×54 mm, with not more than 144 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: <table> <tr> <td>320 10</td><td>320 C 15</td><td>320 C 50</td><td>320 E 15</td></tr> <tr> <td>320 11</td><td>320 C 17</td><td>320 C 51</td><td>320 E 17</td></tr> <tr> <td>320 C 10</td><td>320 C 25</td><td>320 C 53</td><td></td></tr> </table> - or — other identification markings relating to devices complying with the abovementioned description	320 10	320 C 15	320 C 50	320 E 15	320 11	320 C 17	320 C 51	320 E 17	320 C 10	320 C 25	320 C 53		0
320 10	320 C 15	320 C 50	320 E 15												
320 11	320 C 17	320 C 51	320 E 17												
320 C 10	320 C 25	320 C 53													
ex 8542 11 74	*09	Microcontroller or microcomputer of N-MOS (including H-MOS) technology, with a processing capacity of 32 bits, comprising 24 registers of 32 bits and a random-access memory (RAM) with a storage capacity of 2 Kbits, in the form of a monolithic integrated circuit contained in a housing the exterior dimensions of which do not exceed 24×24 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: HGC 6127 - or — other identification markings relating to devices complying with the abovementioned description	0												

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 74	*10	Microcontroller or microcomputer with a processing capacity of 32 bits, comprising one or more random-access memories (RAMs) with a total storage capacity not exceeding 48 Kbits, a read only memory, non-programmable (ROM) with a storage capacity not exceeding 128 Kbits and a floating decimal point arithmetic unit with a capacity of 32 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 45 × 53 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DSP 32 MB 86232 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 74	*12	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, consisting of a system integration module (SIM) with configuration register, a random-access memory (RAM) with a storage capacity of 16 Kbits, a time processor unit (TPU) with 16 independent programmable channels, a serial peripheral interface and a serial communications interface, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 144 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 68332 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 74	*13	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, having the function of audio-data processing, comprising an arithmetic-logic unit (ALU) of 32 bits, a multiplier/accumulator (MAC) of 52 bits, 2 dynamic random-access memories (D-RAMs) with a total storage capacity of 12 Kbits and 2 static random-access memories (S-RAMs) with a total storage capacity of 14 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 23 × 82 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMC 57000 TMC 57001 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 75	*01	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 56 bits, comprising 4 bidirectional buses of 24 bits, 3 read only memories, non-programmable (ROMs) with a total storage capacity of 12,75 Kbits or 102 Kbits and 2 or 3 random-access memories (RAMs) with a total storage capacity of 12 Kbits or 24 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 48 × 48 mm, with not more than 132 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 75 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: DSP 56000 DSP 56001 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 75	*22	Floating-point dual port microcontroller or microcomputer of C-MOS technology, with a processing capacity of 96 bits, comprising a random-access memory (RAM) with a storage capacity of 32 Kbits and 2 random-access memories (RAMs) each with a storage capacity of 16 Kbits and a read only memory, non-programmable (ROM) with a storage capacity of 2 Kbits and 2 read only memories, non-programmable (ROMs) each with a storage capacity of 16 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 48 × 48 mm, with not more than 223 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: DSP 9 6002 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 77	*01	Gate array of C-MOS technology, with metal gates, with an operating voltage of 12 V, with not less than 637 two-input functions, having within the array a digital code produced by an electron beam, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 29 mm, with not more than 22 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: FB 215 or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 11 79	*01	Digital signal synthesiser based on standard cells of C-MOS technology, comprising 32 independent programmable channels, a clock generator, an input/output decoder, a microprocessor with a processing capacity of 8 bits, 2 timers, an interrupt controller, 2 digital-to-analogue converters and an analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 128 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: VY 06243 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 79	*02	Interface and control circuit based on standard cells of C-MOS technology, capable of interfacing between a 32-bit data-bus and a 16-port asynchronous multiplexer, comprising data-registers, control- and status-registers, and a FIFO (first in, first out) read/write memory controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 1TWA-0302 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 79	*03	Audio digital filter based on standard cells of C-MOS technology, with 16 channels, each of them real-time programmable with 20 parameters or more, comprising a multiplier/accumulator (MAC), a timer and 2 random-access memories (RAMs) for the storage of parameters and of temporary processing data, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 25 × 25 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: VC 5396 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 90	*21	Static random-access memory of bipolar technology (bipolar S-RAM), with a storage capacity of 64 × 9 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 40 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82 S 09 MBM 93419 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 90	*28	Static random-access memory of TTL technology (TTL S-RAM), with a storage capacity of 1 Kbit and an access time not exceeding 45 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 30 mm, with not more than 22 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 93422 93425 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)																																													
ex 8542 11 90	*72	Programmable, non-erasable, read only memory (PROM) of Schottky TTL technology, with a storage capacity of 2 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: <table><tr><td>27 S 12</td><td>5305</td><td>5624</td><td>7053</td><td>82 S 131</td></tr><tr><td>27 S 13</td><td>5306</td><td>6305</td><td>7058</td><td>93436</td></tr><tr><td>28 L 22</td><td>5308</td><td>6306</td><td>74 S 570</td><td>93446</td></tr><tr><td>28 LA 22</td><td>5309</td><td>6308</td><td>74 S 571</td><td>MB 7115</td></tr><tr><td>28 L2XMFC</td><td>53 S 240</td><td>6309</td><td>76 LS 03</td><td>MB 7116</td></tr><tr><td>29613</td><td>53 S 241</td><td>63 S 240</td><td>7620</td><td>MB 7117</td></tr><tr><td>29770</td><td>54 S 570</td><td>63 S 241</td><td>7621</td><td>MB 7118</td></tr><tr><td>29771</td><td>54 S 571</td><td>6335</td><td>82 S 114</td><td></td></tr><tr><td>38510</td><td>5604</td><td>6336</td><td>82 S 130</td><td></td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	27 S 12	5305	5624	7053	82 S 131	27 S 13	5306	6305	7058	93436	28 L 22	5308	6306	74 S 570	93446	28 LA 22	5309	6308	74 S 571	MB 7115	28 L2XMFC	53 S 240	6309	76 LS 03	MB 7116	29613	53 S 241	63 S 240	7620	MB 7117	29770	54 S 570	63 S 241	7621	MB 7118	29771	54 S 571	6335	82 S 114		38510	5604	6336	82 S 130		0
27 S 12	5305	5624	7053	82 S 131																																												
27 S 13	5306	6305	7058	93436																																												
28 L 22	5308	6306	74 S 570	93446																																												
28 LA 22	5309	6308	74 S 571	MB 7115																																												
28 L2XMFC	53 S 240	6309	76 LS 03	MB 7116																																												
29613	53 S 241	63 S 240	7620	MB 7117																																												
29770	54 S 570	63 S 241	7621	MB 7118																																												
29771	54 S 571	6335	82 S 114																																													
38510	5604	6336	82 S 130																																													
ex 8542 11 90	*73	Programmable, non-erasable, read only memory of bipolar technology (bipolar PROM), with a storage capacity of 16 Kbits and a standby current of 50 mA or more but not exceeding 80 mA, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 33 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>27 PS 191</td><td>27 PS 291</td><td>27 S 191</td><td>27 S 291</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	27 PS 191	27 PS 291	27 S 191	27 S 291	0																																									
27 PS 191	27 PS 291	27 S 191	27 S 291																																													
ex 8542 11 90	*74	Programmable, non-erasable, read only memory (PROM), with a storage capacity of 32 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>63 S 3281</td><td>AM 27S43</td><td>MB 7141</td><td>MB 7142</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	63 S 3281	AM 27S43	MB 7141	MB 7142	0																																									
63 S 3281	AM 27S43	MB 7141	MB 7142																																													
ex 8542 11 90	*76	Programmable, non-erasable, read only memory (PROM) of GaAs technology with a storage capacity of 4 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: <table><tr><td>14GD048</td><td>14GM048</td></tr></table> or — other identification markings relating to devices complying with the abovementioned description	14GD048	14GM048	0																																											
14GD048	14GM048																																															

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 91	*02	16-bit microprocessor of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 25 × 82 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 29116 SBP 9989 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*05	Sextuple or triple flip-flop of the D-type of bipolar technology, with emitter-coupled logic (ECL) input/output, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 31 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combination of figures and letters: F100131 F100151 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*06	Quintuple OR/NOR gate of bipolar technology, with emitter-coupled logic (ECL) input/output, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 31 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: F100102 F100107 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*07	Sextuple hexadecimal latch of the D-type of bipolar technology, with emitter-coupled logic (ECL) input/output, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 31 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: F100150 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*08	Logic circuit of bipolar technology, with not more than 6 logic functions, a supply voltage of 11 V or more but not exceeding 18 V, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 23 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: FZH 101 A FZH 191 FZJ 121 FZH 111 A FZH 201 FZK 101 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 95	*09	12- or 16-input multiplexer of bipolar technology, with emitter-coupled logic (ECL) input/output, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13×31 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: F100163 F100164 F100171 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*10	Quintuple differential line receiver of bipolar technology, with emitter-coupled logic (ECL) input/output, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13×31 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: F100114 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*11	4-stage counter/4-bit shift register of bipolar technology, with emitter-coupled logic (ECL) input/output, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13×31 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: F100136 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*12	8-bit shift register of bipolar technology, with emitter-coupled logic (ECL) input/output, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13×31 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: F100141 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*18	Control circuit for disc storage units based on gate arrays of bipolar technology, comprising 1 800 gates, with not more than 80 emitter-coupled logic (ECL) and/or transistor-transistor logic (TTL) inputs/outputs, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30×30 mm, with not more than 68 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: AmMPA 1850 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 95	*34	Multiple bus interface circuit (multiple bus buffer) of low-power Schottky (LPS) technology, for interfacing the error correction and detection unit, a system data-bus and a dynamic random-access memory (D-RAM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 20 × 33 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: AM 2961 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*38	2-, 4-, 6-, 10- or 14-channel controller of bipolar technology, for controlling read/write signals from magnetic heads in hard-disc units, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 19 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters : 32 R 2020 R 32 R 510 A 32 R 522 32 R 2021 R 32 R 5121 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*42	Driver circuit for write signals for magnetic tape storage units, of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 29 mm, with not more than 22 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: VT 211 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*43	Error correction and detection unit (ECDU) of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 46 × 68 mm, with not more than 208 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 2960 74 AS 632 74 F 630 74 LS 631 54 AS 632 74 AS 634 74 F 631 DP 8400 54 AS 634 74 AS 6364 74 LS 630 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 95	*44	Control circuit of TTL technology, for the firing of magnetic print hammers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 26 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 801379-002 810751-001 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*45	Status and shift control unit of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 57 mm, with not more than 42 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: AM 2904 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*58	Interface and control circuit of bipolar technology, for interfacing signals between data processing machines and coaxial cable in a local area network (LAN), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 38 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 7996 DP 8392 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*59	Interface circuit for the synchronization of data flow from a hard-disc drive, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 35 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: DP 8462 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*60	Analogue-digital monolithic integrated circuit of bipolar technology for interface signals between the peripheral hard-disc, memory unit and the central processing unit (CPU), contained in a housing the exterior dimensions of which do not exceed 15 × 50 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 581 C - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 95	*61	Subscriber line interface circuit (SLIC) of dielectric isolation technology, with an internal programmed constant line current, comprising a resistor network and an operational amplifier, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HC 5502 HC 5504 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 95	*62	Driver circuit of GaAs technology, for controlling laser diodes or other light-emitting diodes (LEDs), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G075 16G076 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*02	Audio digital-to-analogue converter of bipolar technology, with a dynamic range of 96 dB or more, comprising an internal voltage reference, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 37 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: PCM 63P - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*03	Programmable amplifier of bipolar technology, for signals on a digital communications bus, in the form of monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 21 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: HS 3182 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*04	Monolithic integrated circuit (read/write data processor circuit) for the amplification and conversion of read signals and conversion of write signals for hard-disc drives, contained in a housing the exterior dimensions of which do not exceed 19 × 38 mm, with not more than 28 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 98 (cont'd)		— an identification marking consisting of or including one of the following combinations of letters and figures: 32 P 3000 32 P 540 32 P 541 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*07	Demodulator for phase-shifted signals of bipolar technology, with a clock signal generator and a circuit for parallel to serial conversion, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 28 mm, with not more than 30 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TA 8662 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*08	Demodulator/tone-decoder of bipolar technology for frequency decoding, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 21 mm, with not more than 14 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: XR 2211 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*10	Block slave address generator of bipolar technology, for the address generation during block transfers on a bus, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 11 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: SN 74 ALS 2442 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*11	2-, 4-, 6- or 8-channel read/write signal generator for hard-disc drives, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 32 R 117 32 R 501 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 98	*12	Function generator of bipolar technology for the generation of variable wave-forms, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10×21 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: XR 2206 XR 8038 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*13	Receiver/transmitter of Schottky technology, for Manchester-coded data, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18×28 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMS 38051 TMS 38053 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*14	Circuit of bipolar technology, for the regeneration of pulse-code-modulated (PCM) signals, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 10×21 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: XR C 240 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*16	Data-synchroniser for tape-reading units of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15×39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: VT 210 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*17	Data synchroniser and encoder/decoder of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13×21 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or letters: 32 D 532 32 D 535 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 98	*20	Clock and data recovery circuit of GaAs technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 16G040 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 98	*21	Comparator circuit of GaAs technology, for phase and frequency differences of frequencies not exceeding 1 GHz, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 16G044 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 20	*10	Amplifier, in the form of a monolithic integrated analogue circuit, the exterior dimensions of which do not exceed 3×3 mm, for use in the manufacture of products falling within subheading 9021 40 00 (a)	0
ex 8542 19 20	*20	FM receiver/amplifier of bipolar technology, in the form of an unmounted analogue monolithic integrated circuit, the exterior dimensions of which do not exceed 4×6 mm, for use in the manufacture of products falling within subheading 9021 40 00 (a)	0
ex 8542 19 30	*02	Microwave amplifier of bipolar technology, with a nominal gain of 18 dB at 0,5 GHz or 22,5 dB at 1 GHz and 32,5 dB at 0,1 GHz or 26 at 1,5 GHz, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 2×4 mm or the diameter of which does not exceed 3 mm, with not more than 4 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: A-06 A-08 N10 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*03	Quadruple amplifier of C-MOS technology, with an input current not exceeding 20 pA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8×20 mm, with not more than 14 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 30 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: LMC 660 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*04	Programmable amplifier for driving into an output voltage of $-9,5$ V or more but not exceeding $+14,5$ V or into an output current not exceeding $0,5$ A, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 9×23 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: EL 2021C - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*05	Amplifier of bipolar technology, with a supply current not exceeding $1,5$ mA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 9×9 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: LM 1964 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*06	Amplifier of bipolar technology, with a typical operating frequency of $1,3$ GHz, $2,3$ GHz or 3 GHz and a single supply voltage of 5 V, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 3×3 mm, with not more than 6 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: C1D C1E C1F C1G C1H C1J - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*07	Amplifier with an off-set voltage not exceeding $525 \mu\text{V}$ at 25°C, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 5×7 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including the following combination of figures: 1006 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 30	*08	Amplifier of GaAs technology, having a nominal gain of 18 or more but not exceeding 30 dB and a frequency range of not more than 1,9 GHz, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G071 16G072 16G074 MGF 7131 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*10	Amplifier, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 2×4×4 mm, with not more than 10 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: C 05 V 35 or — other identification markings relating to devices complying with the abovementioned description These devices are for use in the manufacture of products falling within subheading 9021 40 00 (a)	0
ex 8542 19 30	*15	Amplifier with an input current not exceeding 80 nA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8×11 mm or the diameter of which does not exceed 10 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: OPA 37 PA 111 OPA 121 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*25	Variable amplifiers for the range of frequencies of 10 Hz or more but not exceeding 30 kHz, with a gain of 85 dB or more, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8×19 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: M 5218 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*30	Dual or quadruple amplifier operating with a supply current per amplifier not exceeding 30 µA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8×11 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LT 1178 LT 1179 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 30	*40	Thermocouple amplifier for instrumentation control at temperatures from 0 to 50 °C, incorporating an alarm system, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 20 mm, with not more than 14 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 594 AD 595 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*80	Amplifier with a programmable gain factor, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 45 mm or the diameter of which do not exceed 11 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: PGA 102 PGA 202 PGA 203 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*10	Voltage regulators with a quiescent current of 75 µA and a drop out voltage of 380 mV at 100 mA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 7 × 11 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LP 2950 LP 2951 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*20	Voltage regulator with an input voltage range of 3 V or more but not exceeding 60 V and a quiescent current of 6 mA, comprising an internal 1,25 A, 2,5 A or 5 A switch circuit, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 39 mm or the diameter of which do not exceed 11 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LT 1070 LT 1071 LT 1170 LT 1171 LT 1172 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*30	Adjustable shunt voltage regulator, comprising an internal voltage reference and divider resistors with a collector (sink) current of 1 mA or more but not exceeding 100 mA and an initial voltage reference tolerance of 0,4 %, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm, with not more than 8 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 50 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: LT 1431 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*40	Variable voltage regulator with a supply current not exceeding 120 μA at an output current not exceeding 100 μA and a dropout voltage not exceeding 0,85 V at an output current of 125 mA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 $\times$ 20 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LT 1020 LT 1120 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*50	Voltage regulator with an output voltage of 2,1 V ($\pm$2,5 %) or 3 V ($\pm$2,5 %) at a nominal output current of 40 mA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 6 $\times$ 6 mm, with not more than 3 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 0 C (RH5 RA 30 AA) 1 B (RH5 RA 21 AA) - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*06	Pre-magnetisation control circuit for audio magnetic tapes of bipolar technology, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 $\times$ 26 mm, with not more than 18 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: PC 1297 CA - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*08	Current-control circuit of bipolar technology, capable of driving a continuous output current of 2 A, having an output-error detection function, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 11 $\times$ 16 mm, with not more than 5 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: PBD 3548 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 60	*09	Control circuit of BiMOS technology, capable of driving loads from the positive side of a power supply, providing a voltage-transient protection of -100 V or more but not exceeding $+100\text{ V}$, having an output leak-current not exceeding $150\text{ }\mu\text{A}$, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed $8 \times 11\text{ mm}$, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 3399DW - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*10	Circuit for driving current in a winding of linear motors or motors with rotating arms, of bipolar technology, working with an output voltage not exceeding 45 V at an output current not exceeding $1,8\text{ A}$, in form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed $17 \times 23\text{ mm}$, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: PBL 3717 PBL 3770 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*11	Circuit for driving linear motors or motors with rotating arms, of bipolar technology, working with an output voltage not exceeding 45 V at an output current not exceeding 1 A , comprising a clock generator, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed $17 \times 24\text{ mm}$, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 34993 PBL 3771 PBL 3772 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 60	*13	Circuit for driving linear motors or motors with rotating arms, of bipolar technology, working with an output voltage of 45 V at an output current of $1,75\text{ A}$, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed $17 \times 17\text{ mm}$, with not more than 44 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: UDN 2917 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 70	*20	Interface and control circuit of C-MOS technology, for the generation of graphic symbols on a cathode-ray tube, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed $9 \times 29\text{ mm}$, with not more than 28 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 70 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: MN 1297 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 70	*30	Interface circuit of bipolar technology, for interfacing data and power between a microcontroller or microcomputer and a read/write terminal, comprising a bridge rectifier, a power supply regulator, a reset circuit, a transmitter and a receiver, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: LB 1167A - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*05	Programmable encoder/decoder circuit of C-MOS technology, comprising an electrically erasable, programmable, read only memory (E²PROM) with a storage capacity of 32 bits, an amplifier and an oscillator, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 20 mm, with not more than 14 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AZ 280 TMC3637 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*06	Analogue modulator of C-MOS technology, having a dynamic range of 123 dB at 375 Hz, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 13 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CS 5323 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*09	Switch unit of bipolar technology, for audio signals, having a distortion not exceeding 0,005 %, comprising 2 control units and 2 alternating switches, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 13 mm, with not more than 10 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TK 15022 Z - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80	*12	Dual-tone multi-frequency (DTMF) generator of C-MOS technology, capable of decoding 4-bit binary data and generating 16 tone pairs, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 10×11 mm, with not more than 14 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TP 5088 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*14	Voltage-to-frequency converter, comprising an amplifier, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 10×24 mm or the diameter of which does not exceed 11 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: VFC32 VFC100 VFC101 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*15	Current-to-voltage converter with an input current not exceeding 100 µA and an output voltage not exceeding -10 V, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8×32 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: ACF 2101 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*16	RMS-converter for computing the root mean square (RMS) value of wave-forms and converting this value to an equivalent direct current or an equivalent direct voltage, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 18×21 mm, or the diameter of which does not exceed 18 mm, with not more than 14 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 536 A AD 636 AD 637 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*17	Temperature transducer, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 6×6 mm or the diameter of which does not exceed 10 mm, with not more than 3 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 590 AD 592 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80	*21	Phase-locked loop (PLL) clock circuit of C-MOS technology, capable of synchronisation or multiplication of frequencies of 20 MHz or more but not exceeding 70 MHz, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 13 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 88915 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*22	Detector for amplitude peaks in read/write signals of disc storage units, consisting of a differential amplifier with automatic gain control and a precision full-wave rectifier, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 12 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: ML 8464 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*23	Voltage comparator of dielectric isolation technology, comprising a master/slave flip/flop, operating within a common voltage range of -12 V or more but not exceeding +12 V and a differential voltage range of -24 V or more but not exceeding +24 V and a response time not exceeding 6 ns, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 11 mm or the diameter of which do not exceed 10 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: EL 2019 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*24	Amplifier/comparator of bipolar technology, for the amplification and comparison of phase/frequency signals from sensor inputs, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 6 × 8 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: CXA 1418 N - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*25	Half-bridgerectifier, consisting of 2 field effect transistors of MOS technology (MOSFETs), capable of driving inductive or capacitive loads with a nominal voltage of 50 V and a nominal current of 2 A, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 6 × 11 mm, with not more than 16 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80 (cont'd)		— an identification marking consisting of or including the following combination of figures and letters: Si9950DY - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*26	Voltage converter and regulator of bipolar technology, with a voltage loss not exceeding 1,6 V at an output current of 100 mA, operating with a supply voltage range of 3,5 V or more but not exceeding 15 V, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: LT 1054 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*32	Speech-transfer circuit of bipolar technology, operating at a supply voltage of 2,3 V or more but not exceeding 22 V, providing continuously regulation of transmit and receive gain and a mute function, comprising 4 amplifiers, an internal voltage reference, 2 DC regulators and a power down function, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 13 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: PBL 3850 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*34	Passive decoder of BiMOS technology, comprising a fixed matrix, a 7-kHz filter, a noise-reducing circuit, a digital delay circuit and a memory-control circuit, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 58 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: LV 1000 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*35	Matrix decoder of BiMOS technology, comprising an adaptive matrix circuit, automatic-balance buffers, a noise generator and a control circuit, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 63 mm, with not more than 48 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SSM 2125 SSM 2126 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80	*37	Serial 13-bit linear encoder/decoder of C-MOS technology, providing digital-to-analogue and analogue-to-digital conversion, comprising 2 sample and hold circuits, a comparator/ amplifier, a data-selector circuit, a successive approximation register, 2 shift registers, a sequence controller and a voltage reference circuit, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 20 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 145402 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*38	Encoder/decoder with pulse-code-modulation filter of C-MOS technology, operating with a +5 V single-power supply, comprising an analogue-to-digital converter and a digital-to-analogue converter, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 13 mm, with not more than 20 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 145480 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*39	Encoder/decoder with pulse-code-modulation filter of C-MOS technology, with a dual-power supply and having a typical dissipation rate of 50 mW, comprising an analogue-to-digital converter and a digital-to-analogue converter, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 145503 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*40	Adaptive differentiated pulse-code-modulation circuit of C-MOS technology, for encoding/decoding data, capable of full duplex data-transmission between a channel with a transfer rate of 64 Kbits per second and a channel with a transfer rate of 16 Kbits, 24 Kbits, 32 Kbits or 64 Kbits per second, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 20 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 145532 - or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80	*41	Voltage reference circuit providing a typical output voltage of 5, 7 or 10 V with a drift slope (output voltage temperature co-efficient) not exceeding 20 ppm/°C, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm or the diameter of which do not exceed 10 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LT 1021 REF 102 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*42	Voltage reference circuit with a reverse breakdown of 1,235 V (± 4 mV) or 2,5 V (± 20 mV), in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 5 × 6 mm or the diameter of which does not exceed 6 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: LT 1004 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*44	Modulator of bipolar technology, operating in the UHF band, for the conversion of audio and video signals, in a frequency range of 470 MHz or more but not exceeding 630 MHz, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 6 × 11 mm, with not more than 16 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ALP 101 CXA 1333 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*45	Full-frame charge-coupled image sensor with single-phase clocking and not more than 1 048 576 photosensitive areas, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 32 × 32 mm, with not more than 160 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TC 223 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*46	Encoder/decoder with pulse-code-modulation filters of C-MOS technology, capable of voice digitisation and reconstruction at a speed of 64 Kbits/s or more but not exceeding 2 048 Kbits/s, with a single power supply of 5 V, a power dissipation not exceeding 37 mW in operating mode and not exceeding 3 mW in power down mode and capable of automatically entering in power down mode with clock stop, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 20 mm, with not more than 28 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: 7508 B 7509 B - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 80	*47	Programmable diode array, consisting of 14 individual diodes and a rectifier, of GaAs technology, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G010 16G011 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 10	*10	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, consisting of a single substrate layer on which are mounted 2 chips, one comprising a central processing unit (CPU) and the other a memory unit, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 33 × 76 mm, with not more than 60 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 57-00000 57-19400 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 30	*30	Four-channel digital-to-analogue converter, each channel having a capacity of 12 bits, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 21 × 41 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 390 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 30	*40	16-bit digital-to-analogue converter, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DAC 705 DAC 706 DAC 707 DAC 708 DAC 709 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*10	Amplifier for the frequency range 20 Hz to 20 000 Hz, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 45 × 80 mm, with not more than 30 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 20 50 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures and letters: STK 4041 STK 4151 STK 4201 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*20	Amplifier for a nominal range of 0 or more but not exceeding 70 kHz, with an isolation voltage of 750 V or more and a leakage of not more than 1 µA, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 52 mm, with not more than 40 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ISO 100 ISO 102 ISO 106ISO 120ISO 121 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*30	Amplifier with a programmable gain factor, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 45 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 3606 G - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*40	Amplifier with an input power of 1 mW and an output power of 3,5 W at a frequency range of 890 MHz or more but not exceeding 915 MHz, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 5 × 15 × 34 mm, with not more than 6 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: XHW 903 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*50	Amplifier, operating with a supply voltage of 28 V, for frequencies of 1 625 MHz or more but not exceeding 1 645 MHz, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 27 × 96 mm, with not more than 6 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: STM 1645-30 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 80	*20	Subscriber-connection units (cross-point switch) with 4 or more but not more than 12 switches and an operating voltage of 150 V or more, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 41 × 92 mm, with not more than 94 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 20 80 (cont'd)		— an identification marking consisting of or including one of the following combinations of figures: 719 904 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 80 00	*01	Dual field-effect transistor of N-MOS (including H-MOS) technology (Dual N/H-MOSFET), having a drain-to-source breakdown-voltage of 20 V or more, operating with a drain-current not exceeding 3,5 A and with a dissipation rate not exceeding 2 W, contained in a housing the exterior dimensions of which do not exceed 6 × 7 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures: 9956 9959 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 80	*91	Electromagnetic display consisting of 7 electromagnetic coils, which by means of the residual magnetism in the stators provide that the last indication remains available (set state), and 7 pivoting light-reflecting segments each of which is attached to a bar magnet. The display is contained in a housing the exterior dimensions of which do not exceed 28 × 36 × 50 mm	0
ex 8543 80 80	*93	Opto-electronic circuit comprising one or more light-emitting diodes (LEDs) and one photodiode with amplifier circuit and an integrated logic gate arrays circuit or one or more light-emitting diodes (LEDs) and of several photodiodes with amplifier circuit, contained in a plastic housing with not more than 8 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HC PL 2 400 HC PL 2730 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 80	*94	Amplifier with an isolation voltage of 1 500 V or more and with a leakage current not exceeding 0,5 µA, consisting of 2 capacitors and 2 monolithic integrated circuits on a printed circuit which is mounted on a plastic carrier, the whole contained in a housing the exterior dimensions of which do not exceed 8 × 21 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: ISO 122 - or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 80	*95	Speech reproducing module, consisting of a printed circuit on which are mounted: — a synthesiser, in the form of a monolithic integrated circuit and contained in a housing, — a piëzo-electric or magnetic loudspeaker, — two or three supply batteries, a switch, three capacitors, and whether or not incorporating: — either a read only memory, non-programmable (ROM) with a storage capacity of 1 Mbit or an UV erasable, programmable, read only memory (EPROM) with a storage capacity of 128 Kbits, in the form of a monolithic integrated circuit and contained in a housing, the whole contained in a housing the exterior dimensions of which do not exceed 4 × 60 × 95 mm, with not more than 24 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8543 80 80 (cont'd)		— an identification marking consisting of or including the following combination of letters: SPMS or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 80	*96	Digital image processor unit with a speed of 1 to 50 images per second, with a spatial resolution of 512×512 pixels or more and a radiometric resolution of 16 bits, comprising supply units and 11 printed circuits on which are mounted integrated circuits and other active and passive elements, the whole mounted in a frame, for use in the manufacture of cardiodiagnostic apparatus (a)	0
ex 8544 19 90	*10	Insulated winding wire containing by weight 99,5 % or more of aluminium, neither lacquered, varnished nor enamelled, with a total thickness of 0,15 mm or more but not exceeding 0,16 mm	0
ex 8545 90 90	*01	Cell and battery carbon, in the form of rods, with a length of 34 mm or more but not exceeding 160 mm and a diameter not exceeding 12 mm	0
ex 8548 00 00	*92	Contact image sensor, comprising a row of 1 728 or more but not more than 2 050 photosensitive dots and a row of light-emitting diodes (LEDs), contained in a housing the exterior dimensions of which do not exceed $23 \times 24 \times 277$ mm	0
ex 9001 10 10 ex 9001 10 90	*10 *10	Image reverser made up from an assembly of optical fibres	0
ex 9002 11 00	*10	Adjustable lens unit, having a focal length of 90 mm or more but not exceeding 180 mm and comprising a combination of between 4 and 8 glass or methacrylic lenses with a diameter of 120 mm or more but not exceeding 180 mm, each lens coated on at least one side with a magnesium fluoride layer, for use in the manufacture of video projectors (a)	0
ex 9002 90 91	*10	Optical element comprising an octagonal Fresnel lens, for use in the manufacture of overhead projectors (a)	0
ex 9013 80 10	*10	Liquid crystal displays (LCDs) consisting of a layer of liquid crystals between 2 glass sheets or plates, with a 7 or more but not exceeding 240 figures or letters, the exterior dimensions do not exceed 45×76 mm, with not more than 192 connections, for use in the manufacture of calculators (a)	0
ex 9021 30 90	*29	Vascular prosthesis, neither woven nor knitted, of which the largest opening has an internal diameter of more than 6 mm but not exceeding 8 mm	0
ex 9021 30 90	*30	Heart valves and parts thereof	0
ex 9608 91 00	*10	Non-fibrous plastic pen-tips with an internal channel	0
ex 9613 90 00	*20	Piezo-electric ignition mechanism	0

(a) Control of the use for this special purpose shall be carried out pursuant to the relevant Community provisions.

COUNCIL REGULATION (EEC) No 1573/93

of 14 June 1993

temporarily suspending the autonomous Common Customs Tariff duty on certain industrial products (in the chemical and allied sectors)

THE COUNCIL OF THE EUROPEAN COMMUNITIES,

Having regard to the Treaty establishing the European Economic Community, and in particular Article 28 thereof,

Having regard to the proposal from the Commission,

Whereas production of the products referred to in this Regulation is at present inadequate or non-existent within the Community and producers are thus unable to meet the needs of user industries in the Community;

Whereas it is in the Community's interest in certain cases to suspend the autonomous Common Customs Tariff duties only partially, particularly because of the existence of Community production, and in other cases to suspend them completely;

Whereas the decision for the suspension of these autonomous duties should be taken by the Community;

Whereas, taking account of the difficulties involved in accurately assessing the development of the economic

situation in the sector concerned in the near future, these suspension measures should be taken only temporarily, by fixing their period of validity by reference to the interests of Community production,

HAS ADOPTED THIS REGULATION:

Article 1

The autonomous Common Customs Tariff duties for the products listed in the tables appearing in the Annexes shall be suspended at the level indicated in respect of each of them.

These suspensions shall apply:

- from 1 July to 31 December 1993 for the products listed in the table appearing in Table I,
- from 1 July 1993 to 30 June 1994 for the products listed in the table appearing in Table II.

Article 2

This Regulation shall enter into force on 1 July 1993.

This Regulation shall be binding in its entirety and directly applicable in all Member States.

Done at Luxembourg, 14 June 1993.

For the Council

The President

J. TRØJBORG

ANNEX

TABLE I

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2819 90 00	*10	Chromium dioxide	0
ex 2825 90 10	*10	Calcium hydroxide, of a purity of 98 % or more calculated on the dry weight, in the form of particles of which: — not more than 1 % by weight have a particle-size exceeding 75 micrometres and — not more than 4 % by weight have a particle-size of less than 1,3 micrometres	0
ex 2827 39 00	*10	Copper monochloride of a purity by weight of 96 % or more but not exceeding 99 %	0
ex 2903 69 00	*20	1,2-Bis(pentabromophenyl)ethane	0
ex 2917 39 90	*20	Benzene-1,2,4-tricarboxylic acid 1,2-anhydride	0
ex 2917 39 90	*50	Benzene-1,2,4-tricarbonyl trichloride	0
ex 2918 29 30	*10	2-Ethylhexyl 4-hydroxybenzoate	0
ex 2920 90 80	*60	Tetraethyl orthosilicate, of a purity by weight of 99,99 % or more and containing: — 1,0 microgram/kg or less of calcium, — 1,0 microgram/kg or less of chromium, — 2,0 microgram/kg or less of iron and — 2,0 microgram/kg or less of sodium, for use in the manufacture of goods of heading No 8542 (a)	0
ex 2921 49 10	*20	Pendimethalin (ISO)	3,5
ex 2928 00 00	*40	N,N-Diethylhydroxylamine	0
ex 2931 00 90	*70	Bis(2-chloroethyl) vinylphosphonate	0
ex 2933 39 80	*06	2,3,5,6-Tetrachloropyridine	0
ex 2933 39 80	*17	3,5-Dichloro-2,4,6-trifluoropyridine	0
ex 2933 69 90	*30	1,3,5-Tris[(3,5-di- <i>tert</i> -butyl-4-hydroxyphenyl)methyl]-1,3,5-triazine-2,4,6-(1 <i>H</i> ,3 <i>H</i> ,5 <i>H</i>)-trione	0
ex 2934 90 99	*37	Mexazolam (INN)	4
ex 2935 00 00	*70	Methyl 2-[4-methoxy-6-methyl-1,3,5-triazin-2-yl(methyl)carbamoylsulphamoyl]benzoate (Tribenuron-methyl)	3,3
ex 2935 00 00	*80	Methyl 3-(4-methoxy-6-methyl-1,3,5-triazin-2-ylcarbamoylsulfamoyl)thiophenecarboxylate (Thifensulfuron-methyl)	3,3
2939 30 00		Caffeine and its salts	4,1
ex 2941 90 00	*20	Josamycin propionate (INN ^M)	2,7
ex 2941 90 00	*45	Ivermectin (INN)	2,7

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2941 90 00	*46	Florfenicol (INN)	0
ex 3208 90 10	*10	Polyurethane of 2,2'-(<i>tert</i> -butylimino)diethanol and 4,4'-methylenedicyclohexyl diisocyanate, in the form of a solution in <i>N,N</i> -dimethylacetamide containing by weight 48 % or more of polymer	0
ex 3909 50 00	*10		
ex 3815 90 00	*70	Catalyst containing titanium trichloride, in the form of a suspension in hexane containing by weight, in the hexane-free material, 14 % or more but not more than 25 % of titanium	0
ex 3823 90 98	*44	Mixture of disodium <i>N</i> -benzyloxycarbonyl-L-aspartate and sodium chloride, in the form of a solution in water	0
ex 3823 90 98	*53	Mixture of tris[2-chloro-1-(chloromethyl)ethyl] phosphate and oligomers of methylphosphonic acid and phosphoric acid with ethane-1,2-diol	0
ex 3823 90 98	*54	Mixture of tris[2-chloro-1-(chloromethyl)ethyl] phosphate and oligomers of 2-chloroethyl phosphate with ethane-1,2-diol	0
ex 3823 90 98	*56	Tetramethylammonium hydroxide, in the form of an solution in water and propan-2-ol containing: — 1 % by weight of tetramethylammonium hydroxide, — 10 microgram/kg or less of potassium, — 10 microgram/kg or less of calcium, — 30 microgram/kg or less of sodium, — 30 microgram/kg or less of iron and — 100 000 particles or less per litre, of a size exceeding 0,5 micrometre, for use in the manufacture of goods of heading No 8542 (a)	0
ex 3901 90 00	*92	Ionomer resin consisting of a salt of a copolymer of ethylene with methacrylic acid	4
ex 3906 90 00	*60	Copolymer of methyl acrylate with ethylene and a monomer containing a non-terminal carboxy group as a substituent, containing by weight 50 % or more of methyl acrylate, whether or not compounded with silica	5
ex 3911 90 10	*10	Poly(oxy-1,4-phenylenesulphonyl-1,4-phenyleneoxy-1,4-phenyleneisopropylidene-1,4-phenylene), in one of the forms mentioned in note 6 (b) to Chapter 39	3,5
ex 3919 90 31	*10	Reflecting laminated sheeting, metallized, consisting of one sheet of polyester and at least another sheet of polyester or other plastic material and coated on one side with an adhesive, whether or not protected by a release sheet	0
ex 3920 62 00	*11		
ex 3921 90 19	*70		
ex 3920 62 00	*04	Polyethylene terephthalate film, of a thickness of less than 10 micrometres, for the manufacture of video cassettes with a playing-time of 300 minutes at a tape-speed of 24 mm per second, or of digital audio cassettes (a)	0
ex 3920 62 00	*09	Polyethylene terephthalate film, of a thickness of 100 micrometres or more but not exceeding 150 micrometres, for the manufacture of photopolymer printing plates (a)	0
ex 3920 62 00	*14	Polyethylene terephthalate film, of a thickness of 20 micrometres or more but not exceeding 30 micrometres, coated on one side with silicone, for use in the manufacture of window safety-film (a)	6,5
ex 3920 62 00	*15	Film of polyethylene terephthalate, coated or covered on one side or on both sides with a layer of modified polyester, of a total thickness of 7 micrometres or more but not exceeding 11 micrometres, for the manufacture of video tapes with a magnetic layer of metallic pigments and a width of 8 mm or of 12,7 mm (a)	0
ex 3921 90 19	*25		

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3920 69 00	*70	Film of a copolymer of ethane-1,2-diol and naphthalene-2,6-dicarboxylic acid, of a thickness of less than 10 micrometres, for the manufacture of video-cassettes with a playing-time of 300 minutes at a tape-speed of 24 mm per second (a)	0
ex 3920 91 00	*91	Polyvinyl butyral film having a graduated coloured band	6
ex 3920 92 00	*21	Poly(<i>epsilon</i> -caprolactam) film: — of a softening point of 200 °C or more and — containing 130 mg/kg or more but not more than 170 mg/kg of copper, for use during the bonding together, under heat and vacuum, of components of composite articles (a)	0
ex 7006 00 90	*20	Colour filter, consisting of a glass plate with red, blue and green pixels, having a total thickness of 1,1 mm ($\pm 0,1$ mm) and exterior dimensions of 320 $\times$ 352 mm or more but not exceeding 320 $\times$ 400 mm, for the manufacture of liquid crystal displays with active matrix (a)	0
ex 7011 20 00	*40	Glass face-plate: — with a diagonal measurement of 366,4 mm ($\pm 1,5$ mm) and of dimensions of 246,4 $\times$ 315,4 mm ($\pm 1,5$ mm), — with a diagonal measurement of 544,5 mm ($\pm 1,5$ mm) and of dimensions of 358 $\times$ 454 mm ($\pm 1,5$ mm) or — with a diagonal measurement of 838,2 mm ($\pm 1,5$ mm) and of dimensions of 549,9 $\times$ 695,6 mm ($\pm 1,5$ mm), and with a raised edge, for the manufacture of colour cathode-ray tubes (a)	0
ex 7011 20 00	*50	Glass face-plate with a diagonal measurement of 704,1 mm ($\pm 1,5$ mm) and of dimensions of 387,1 $\times$ 628,8 mm ($\pm 1,5$ mm)	0
ex 7011 20 00	*60	Glass cone with a diagonal measurement of 698,7 mm ($\pm 1,5$ mm) and of dimensions of 381,7 $\times$ 623,4 mm ($\pm 1,5$ mm)	0
ex 7019 32 00 ex 7019 39 00	*10 *10	Non-woven product of non-textile glass fibre, for the manufacture of air-filters or of air-filtration products (a)	0
ex 7019 90 10	*11	Non-textile glass fibres in which fibres of a diameter of less than 3,5 micrometres predominate	0
ex 8101 99 00	*10	Disk (target) with deposition material, of tungsten or an alloy containing by weight 90 % of tungsten and 10 % titanium, — containing 100 micrograms/kg or less of sodium and — mounted on a copper support, for use in the manufacture of goods of heading No 8542 by sputtering (a)	0
ex 8108 90 90	*92	Disk (target) with deposition material, of titanium, — containing 50 micrograms/kg or less of sodium and — mounted on a copper support, for use in the manufacture of goods of heading No 8542 by sputtering (a)	0

(a) Control of the use for this special purpose shall be carried out pursuant to the relevant Community provisions.

TABLE II

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2707 99 11	*10	Crude light oils containing by weight: — 10 % or more of vinyltoluenes, — 10 % or more of indene and — 1 % or more but not more than 5 % of naphthalene	0
ex 2712 20 00	*10	Synthetic paraffin wax of a molecular weight of 460 or more but not exceeding 1560	0
ex 2712 90 90	*20	Blend of 1-alkenes containing by weight 80 % or more of 1-alkenes of a chain-length of 24 carbon atoms or more but not exceeding 28 carbon atoms	0
ex 2804 29 00	*10	Helium	0
ex 2805 30 10	*10	Alloy of cerium and other rare-earth metals, containing by weight 47 % or more of cerium	0
ex 2810 00 00	*10	Diboron trioxide	0
ex 2811 19 00	*10	Sulphamidic acid	0
ex 2811 29 90	*10	Tellurium dioxide	0
ex 2818 30 00	*10	Aluminium hydroxide oxide in the form of pseudo-boehmite	4
ex 2820 90 00	*10	Manganese oxide containing by weight 77 % or more of manganese	0
ex 2827 60 00	*10	Titanium tetraiodide	0
ex 2836 91 00	*20	Lithium carbonate, in the form of powder and containing one or more of the following impurities at the concentrations indicated: — 2 mg/kg or more of arsenic — 200 mg/kg or more of calcium — 200 mg/kg or more of chlorides — 20 mg/kg or more of iron — 150 mg/kg or more of magnesium — 20 mg/kg or more of heavy metals — 300 mg/kg or more of potassium — 300 mg/kg or more of sodium — 200 mg/kg or more of sulphates, determined according to the methods specified in the European Pharmacopœia	0
2840 11 00		Borates of sodium, anhydrous	0
2840 20 10			0
ex 2840 19 00	*10	Disodium tetraborate pentahydrate	0
ex 2841 80 00	*10	Calcium tungstate (wolframate), for the manufacture of ferro-alloys or decaammonium-41-oxododecawolframate (ammonium paratungstate) (a)	0
ex 2843 90 90	*10	Carboplatin (INN)	0
2845 10 00		Heavy water (deuterium oxide) (<i>Euratom</i>)	0
2845 90 10		Deuterium and compounds thereof; hydrogen and compounds thereof, enriched in deuterium; mixtures and solutions containing these products (<i>Euratom</i>)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2902 90 90	*10	Vinyltoluenes	0
ex 2902 90 90	*40	<i>p</i> -Cymene	0
ex 2902 90 90	*60	1,3-Diisopropylbenzene	0
ex 2902 90 90	*70	1,2,4,5-Tetramethylbenzene (durene)	0
ex 2903 30 10	*10	Carbon tetrafluoride (tetrafluoromethane)	0
ex 2903 30 39	*10	Dibromomethane	0
ex 2903 59 00	*10	1,6,7,8,9,14,15,16,17,17,18,18-Dodecachloropentacyclo[12.2.1.1 ^{6,9} .0 ^{2,13} .0 ^{5,10}]octadeca-7,15-diene, for use in the manufacture of polyamide, polyethylene, synthetic rubber or polystyrene (a)	0
ex 2903 59 00	*20	Hexachlorocyclopentadiene	0
ex 2903 69 00	*10	Di- or tetrachlorotricyclo[8.2.2.2 ^{4,7}]hexadeca-1(12),4,6,10,13,15-hexaene, mixed isomers	0
ex 2904 20 90	*10	Nitromethane	0
ex 2904 20 90	*20	Nitroethane	0
ex 2904 20 90	*30	1-Nitropropane	0
ex 2904 20 90	*40	2-Nitropropane	0
ex 2905 16 90	*10	Octan-2-ol	0
ex 2905 19 10	*10	Potassium <i>tert</i> -butoxide	0
2905 21 00		Allyl alcohol	0
ex 2905 39 90	*10	Butane-1,3-diol	0
ex 2905 39 90	*20	2,4,7,9-Tetramethyldec-5-yne-4,7-diol	0
ex 2905 50 90	*10	2,2-Bis(bromomethyl)propanediol	0
2906 11 00		Menthol	0
ex 2906 19 00	*10	Labd-14-ene-8,13-diol	0
ex 2906 29 90	*10	2,2'-(<i>m</i> -Phenylene)dipropan-2-ol	0
ex 2907 21 00	*10	Resorcinol	0
ex 2909 19 00	*10	1,2-Bis(2-chloroethoxy)ethane	0
ex 2909 30 30	*10	2-Bromo-6-methoxynaphthalene	0
ex 2909 30 30	*20	1,2-Bis(2,4,6-tribromophenoxy)ethane, for the manufacture of acrylonitrile-butadiene-styrene (ABS) (a)	0
ex 2909 49 10	*20	2-(2-Chloroethoxy)ethanol	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2910 90 00	*30	2,3-Epoxypropan-1-ol (glycidol)	0
ex 2910 90 00	*40	Perfluoroepoxypropane	0
ex 2914 19 00	*10	5-Methylhexan-2-one	0
ex 2914 49 00	*10	11 α ,17,21-Trihydroxy-16 β -methylpregna-1,4-diene-3,20-dione	0
ex 2914 50 00	*30	2'-Hydroxyacetophenone	0
ex 2914 50 00	*40	4'-Hydroxyacetophenone	0
ex 2914 69 00	*10	1,4-Naphthoquinone	0
ex 2914 69 00	*30	Ubidecarenone (INN) (coenzyme Q ₁₀)	0
ex 2914 69 00	*40	Idebenone (INN)	0
ex 2915 60 10	*10	1-Isopropyl-2,2-dimethyltrimethylene diisobutyrate	0
ex 2915 90 90	*20	Trimethyl orthoacetate	0
ex 2916 12 00	*10	2- <i>tert</i> -Butyl-6-(3- <i>tert</i> -butyl-2-hydroxy-5-methylbenzyl)-4-methylphenyl acrylate	0
ex 2916 14 00	*10	2,3-Epoxypropyl methacrylate	0
ex 2916 19 90	*20	Butyl perchlorocrotonate	0
ex 2916 19 90	*30	Crotonic acid	0
ex 2916 20 00	*10	Methyl 3-(2,2-dichlorovinyl)-2,2-dimethylcyclopropanecarboxylate	0
ex 2916 39 00	*10	Methyl 3-chlorobenzoate	0
ex 2916 39 00	*20	3,5-Dichlorobenzoyl chloride	3,6
ex 2917 13 00	*10	Azelaic acid and sebacic acid	0
ex 2917 19 90	*20	Sodium 1,2-bis(cyclohexyloxycarbonyl)ethanesulphonate	0
ex 2917 19 90	*30	Docusate sodium (INN)	0
ex 2917 20 00	*20	8,9,10-Trinorborn-5-ene-2,3-dicarboxylic anhydride	0
ex 2917 39 10	*10	Ester or anhydride of tetrabromophthalic acid	0
ex 2917 39 90	*10	Benzene-1,2,4-tricarboxylic acid	0
ex 2917 39 90	*15	Isophthaloyl dichloride, containing by weight 0,8 % or less of terephthaloyl dichloride	0
ex 2917 39 90	*30	Tetrachlorophthalic anhydride	0
ex 2917 39 90	*70	Sodium 3,5-bis(methoxycarbonyl)benzenesulphonate	0
ex 2918 19 10	*10	Malic acid	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2918 19 90	*20	2,2-Bis(hydroxymethyl)propionic acid	0
ex 2918 19 90	*30	Dinoprost (INN), for the manufacture of human medicaments of heading No 3004 (a)	0
ex 2918 19 90	*40	Ammonium (3R,5R)-7-[(1S,2S,6R,8S,8aR)-8-(2,2-dimethylbutyryloxy)-1,2,6,7,8,8a-hexahydro-2,6-dimethyl-1-naphthyl]-3,5-dihydroxyheptanoate	3,5
ex 2918 19 90	*50	Pravastatin sodium (INN)	3,5
ex 2918 90 00	*10	Dinoprostone (INN)	0
ex 2918 90 00	*30	Alprostadil (INN)	0
ex 2918 90 00	*50	2,6-Dimethoxybenzoic acid	0
ex 2918 90 00	*60	Trepibutone (INN)	0
ex 2920 90 10	*10	Diethyl sulphate	0
ex 2920 90 80	*10	O,O'-Dioctadecyl pentaerythritol bis(phosphite)	0
ex 2920 90 80	*30	O,O'-Bis(2,4-di- <i>tert</i> -butylphenyl)pentaerythritol bis(phosphite)	0
ex 2921 19 90	*20	1,1,3,3-Tetramethylbutylamine	0
ex 2921 19 90	*30	Triallylamine	0
ex 2921 29 00	*10	N,N,N',N'-Tetrabutylhexamethylenediamine	0
ex 2921 30 90	*10	Cyclohex-1,3-ylenediamine (1,3-diaminocyclohexane)	0
ex 2921 49 10	*10	α,α',α' -Trifluoro-2,3-xylidine	0
ex 2921 51 10	*10	<i>m</i> -Phenylenediamine, of a purity by weight of 99 % or more and containing: — 1 % or less by weight of water, — 200 mg/kg or less of <i>o</i> -phenylenediamine and — 450 mg/kg or less of <i>p</i> -phenylenediamine	0
ex 2921 59 00	*20	<i>m</i> -Phenylenebis(methylamine)	0
ex 2921 59 00	*30	2,2'-Dichloro-4,4'-methylenedianiline	0
ex 2922 19 00	*20	Dinoprost (INN), trometamol (INN) salt	0
ex 2922 19 00	*30	1-Deoxy-1-(octylamino)-D-glucitol	0
ex 2922 19 00	*50	Tulobuterol hydrochloride (INN)	0
ex 2922 49 80	*10	Tranexamic acid (INN)	0
ex 2922 49 80	*20	β -Alanine	0
ex 2922 50 00	*20	Metipranolol (INN)	0
ex 2922 50 00	*60	Nadolol (INN)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2924 10 00	*20	2-Acrylamido-2-methylpropanesulphonic acid and its sodium or ammonium salts	0
ex 2924 10 00	*30	N-(1,1-Dimethyl-3-oxobutyl)acrylamide	0
ex 2924 29 80	*60	Ioversol (INN)	0
ex 2925 11 00	*20	Saccharin and its sodium salt	0
2925 19 10		3,3',4,4',5,5',6,6'-Octabromo-N,N'-ethylenedipthalimide	0
ex 2925 19 90	*10	N-Phenylmaleimide	0
ex 2926 90 90	*20	2-(3-Phenoxyphenyl)propionitrile	0
ex 2926 90 90	*30	Isophthalonitrile	6
ex 2926 90 90	*60	Etocrilene (INN)	0
ex 2926 90 90	*70	Octocrilene (INN)	0
ex 2926 90 90	*80	2-Cyanoacetamide	0
ex 2926 90 90	*85	Ethyl cyanoacetate	0
ex 2927 00 00	*10	2,2'-Dimethyl-2,2'-azodipropionamide dihydrochloride	0
ex 2928 00 00	*30	Robenidine hydrochloride (INN)	0
ex 2928 00 00	*50	3,3'-Bis(3,5-di- <i>tert</i> -butyl-4-hydroxyphenyl)-N,N'-bipropionamide	0
ex 2928 00 00	*60	2,4,6-Trichlorophenylhydrazine	0
ex 2928 00 00	*70	N,N-Bis(2-methoxyethyl)hydroxylamine	0
ex 2929 10 00	*20	Methylenedicyclohexyl diisocyanate, mixed isomers	0
ex 2929 10 00	*40	<i>trans</i> -(Cyclohex-1,4-ylene diisocyanate), for the manufacture of thermoplastic polyurethane elastomers (a)	0
ex 2929 10 00	*50	3,3'-Dimethylbiphenyl-4,4'-diyl diisocyanate	0
ex 2929 10 00	*60	<i>m</i> -Isopropenyl- α,α -dimethylbenzyl isocyanate	0
ex 2929 10 00	*70	<i>m</i> -Phenylenediisopropylidene diisocyanate	0
ex 2930 90 10	*10	Cilastatin sodium (INN)	0
ex 2930 90 80	*01	2,2'-Thiodiethyl bis[3-(3,5-di- <i>tert</i> -butyl-4-hydroxyphenyl)propionate]	0
ex 2930 90 80	*04	Thiophenol	0
ex 2930 90 80	*05	Probucol (INN)	3,8
ex 2930 90 80	*06	Ethoprophos (ISO)	0
ex 2930 90 80	*07	Netobimin (INN)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2930 90 80	*09	3,3-Dimethyl-1-methylthiobutanone oxime	0
ex 2930 90 80	*11	Thiophanate-methyl (ISO)	0
ex 2930 90 80	*13	Mixture of isomers consisting of 4-methyl-2,6-bis(methylthio)- <i>m</i> -phenylenediamine and 2-methyl-4,6-bis(methylthio)- <i>m</i> -phenylenediamine	0
ex 2930 90 80	*15	4-[4-(1-Methylethoxy)phenylsulphonyl]phenol	0
ex 2930 90 80	*17	3,3'-Thiodi(propionic acid)	0
ex 2931 00 90	*30	2-Diphenylphosphinobenzoic acid	0
ex 2931 00 90	*40	Chlorodiphenylphosphine	0
ex 2931 00 90	*50	Bis(2-chloroethyl) 2-chloroethylphosphonate	0
ex 2931 00 90	*60	Sodium phenylphosphinate	0
ex 2932 11 00	*10	Tetrahydrofuran, containing not more than 40 mg per litre in total of tetrahydro-2-methylfuran and tetrahydro-3-methylfuran, for the manufacture of α -4-hydroxybutyl- ω -hydroxypoly(oxytetramethylene) (a)	0
ex 2932 13 00	*10	Tetrahydrofurfuryl alcohol	0
ex 2932 19 00	*40	Furan of a purity by weight of 99 % or more	0
ex 2932 29 90	*10	Glucurrolactone (INN)	0
ex 2932 29 90	*15	2'-Anilino-6'-[ethyl(isopentyl)amino]-3'-methylspiro[isobenzofuran-1(3H),9'-xanthene]-3-one	0
ex 2932 29 90	*20	1-Hydroxy-4-[1-(4-hydroxy-3-methoxycarbonyl-1-naphthyl)-3-oxo-1H,3H-benz[de]isochromen-1-yl]-6-octadecyloxy-2-naphthoic acid	0
ex 2932 29 90	*30	13,14,15,16-Tetranorlabdano-12,8 α -lactone	0
ex 2932 29 90	*40	3'-Chloro-6'-cyclohexylaminospiro[isobenzofuran-1(3H),9'-xanthene]-3-one	0
ex 2932 29 90	*45	2'-Anilino-6'-diethylamino-3'-methylspiro[isobenzofuran-1(3H),9'-xanthene]-3-one	0
ex 2932 29 90	*55	2'-(2-Chloroanilino)-6'-dibutylaminospiro[isobenzofuran-1(3H),9'-xanthene]-3-one	0
ex 2932 29 90	*60	6'-(N-Ethyl- <i>p</i> -toluidino)-2'-methylspiro[isobenzofuran-1(3H),9'-xanthene]-3-one	0
ex 2932 29 90	*61	2'-Anilino-3'-methyl-6'-methyl(propyl)aminospiro[isobenzofuran-1(3H),9'-xanthene]-3-one	0
ex 2932 29 90	*62	6'-Diethylamino-3'-methyl-2'-(2,4-xylidino)spiro[isobenzofuran-1(3H),9'-xanthene]-3-one	0
ex 2932 29 90	*65	Lovastatin (INN)	4
ex 2932 29 90	*70	2'-Anilino-6'-(N-ethyl- <i>p</i> -toluidino)-3'-methylspiro[isobenzofuran-1(3H),9'-xanthene]-3-one	0
ex 2932 29 90	*80	Etoposide (INN)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2932 29 90	*85	Methyl 6-docosyloxy-1-hydroxy-4-[1-(4-hydroxy-3-methyl-1-phenanthryl)-3-oxo-1H,3H-naphtho[1,8-cd]pyran-1-yl]naphthalene-2-carboxylate	0
ex 2932 90 50	*10	(2S,5R,7S,10R,13S)-4,10-Diacetoxy-2-benzoyloxy-1,7-dihydroxy-9-oxo-5,20-epoxytax-11-en-13-yl (3S)-benzamido-2-hydroxy-3-phenylpropionate (paclitaxel)	0
ex 2932 90 79	*30	Glucosamine hydrochloride (INNM)	0
ex 2932 90 79	*60	(25R)-Spirost-5-en-3 β -ol (diosgenin) and its esters	0
ex 2932 90 79	*70	Bendiocarb (ISO)	0
ex 2933 21 00	*10	Hydantoin	0
ex 2933 29 90	*10	Enoximone (INN)	0
ex 2933 29 90	*20	Reaction product consisting of the methyl esters of (+/-)-6-(4-isopropyl-4-methyl-5-oxo-2-imidazolin-2-yl)- <i>m</i> -toluic acid and (+/-)-2-(4-isopropyl-4-methyl-5-oxo-2-imidazolin-2-yl)- <i>p</i> -toluic acid (Imazamethabenz-methyl)	4
ex 2933 39 80	*01	Carpipramine dihydrochloride (INNM)	0
ex 2933 39 80	*11	3,6-Dichloropyridine-2-carboxylic acid	0
ex 2933 39 80	*12	2-Hydroxyethylammonium 3,6-dichloropyridine-2-carboxylate	0
ex 2933 39 80	*14	(RS)-1-[2-(4-Chlorobenzhydryloxy)ethyl]piperidine and its salts	0
ex 2933 39 80	*15	2-Butoxyethyl (3,5,6-trichloro-2-pyridyloxy)acetate	0
ex 2933 39 80	*18	Pyridine-2,3-dicarboxylic acid	0
ex 2933 39 80	*22	Nicorandil (INN)	0
ex 2933 39 80	*23	5-Methyl-2-pyridylamine	0
ex 2933 39 80	*24	Fluroxypyr (ISO), methyl ester	4
ex 2933 40 90	*10	Quinapril hydrochloride (INNM)	0
ex 2933 59 90	*20	1,4-Diazabicyclo[2.2.2]octane (triethylenediamine)	0
ex 2933 59 90	*30	Amprolium hydrochloride (INNM)	0
ex 2933 59 90	*70	1-Ethyl-6-fluoro-1,4-dihydro-4-oxo-7-piperazin-1-yl-1,8-naphthyridine-3-carboxylic acid and its salts and esters	0
ex 2933 69 90	*10	2,6-Di- <i>tert</i> -butyl-4-[4,6-bis(octylthio)-1,3,5-triazin-2-ylamino]phenol	0
ex 2933 69 90	*20	1,3,5-Tris(4- <i>tert</i> -butyl-3-hydroxy-2,6-dimethylbenzyl)-1,3,5-triazine-2,4,6(1H,3H,5H)-trione	0
ex 2933 79 00	*20	Carteolol hydrochloride (INNM)	0
ex 2933 90 80	*11	Octrizole (INN)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2933 90 80	*13	(2S)-1-(3-Acetylthio-2-methyl-1-oxopropyl)-L-proline	0
ex 2933 90 80	*15	2,4-Di- <i>tert</i> -butyl-6-(5-chlorobenzotriazol-2-yl)phenol	0
ex 2933 90 80	*18	Captopril (INN)	0
ex 2933 90 80	*19	Tryptophan, of a purity by weight not exceeding 98,5 %, for the manufacture of goods of heading No 2309 (a)	0
ex 2933 90 80	*23	2-(2H-Benzotriazol-2-yl)-4,6-di- <i>tert</i> -butylphenol	0
ex 2933 90 80	*24	2-(2H-Benzotriazol-2-yl)-4,6-di- <i>tert</i> -pentylphenol	0
ex 2933 90 80	*25	Bumetizole (INN)	0
ex 2933 90 80	*26	Ethacridine lactate (INN)	0
ex 2933 90 80	*27	2-(2H-Benzotriazol-2-yl)-4,6-bis(1-methyl-1-phenylethyl)phenol	0
ex 2933 90 80	*28	6,6'-Di-2H-benzotriazol-2-yl-4,4'-bis(1,1,3,3-tetramethylbutyl)-2,2'-methylenediphenol	0
ex 2934 90 60	*10	Clotiazepam (INN)	4
ex 2934 90 60	*30	Teniposide (INN)	0
ex 2934 90 60	*40	Etizolam (INN)	4
ex 2934 90 60	*50	Tiquizium bromide (INN)	0
ex 2934 90 70	*10	Ofloxacin (INN)	0
ex 2934 90 70	*30	Pirenoxine sodium (INN)	0
ex 2934 90 99	*11	Zotepine (INN)	4
ex 2934 90 99	*13	1-Ethyl-1,4-dihydro-4-oxo[1,3]dioxolo[4,5-g]cinnoline-3-carbonitrile	0
ex 2934 90 99	*14	2-Chlorodibenz[<i>bf</i>][1,4]oxazepin-11(10H)-one	0
ex 2934 90 99	*16	(6R,7R)-3-Acetoxymethyl-7-[(R)-2-formyloxy-2-phenylacetamido]-8-oxo-5-thia-1-azabicyclo[4.2.0]oct-2-ene-2-carboxylic acid and its salts and esters	0
ex 2934 90 99	*19	Adenosine phosphate (INN)	0
ex 2934 90 99	*31	<i>p</i> -Methoxybenzyl 3-chloromethyl-7-(2-phenylacetamido)-3-cephem-4-carboxylate	0
ex 2934 90 99 ex 3004 90 99	*34 *15	2',3'-Dideoxyinosine	0
ex 2934 90 99	*35	7-Chloro-5-methyl-2H-1,4-benzothiazin-3-(4H)-one	0
ex 2934 90 99	*36	1-[2-(1,3-Dioxan-2-yl)ethyl]-2-methylpyridinium bromide	0
ex 2935 00 00	*30	Sulfathiazole (INN) and its salts	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2935 00 00	*35	3-[1-[7-(Hexadecylsulphonylamino)-1 <i>H</i> -indole-3-yl]-3-oxo-1 <i>H</i> ,3 <i>H</i> -naphtho[1,8- <i>cd</i>]pyran-1-yl]- <i>N,N</i> -dimethyl-1 <i>H</i> -indole-7-sulphonamide	0
ex 2935 00 00	*50	3-[1-(7-Hexadecane-1-sulphonamidoindol-3-yl)-3-oxo-1 <i>H</i> ,3 <i>H</i> -benz[<i>de</i>]isochromen-1-yl]indole-7-carboxylic acid	0
ex 2935 00 00	*60	Sulfadimethoxine (INN) and its sodium salt	3,3
ex 2935 00 00	*75	Methyl 2-(4-methoxy-6-methyl-1,3,5-triazin-2-ylcarbamoysulphamoyl)benzoate (Metsulfuron-methyl)	3,3
ex 2935 00 00	*85	Chlorsulfuron (ISO)	3,3
ex 2936 24 00	*10	Dexpantenol (INN)	0
ex 2936 29 10	*10	Folic acid (INN)	0
ex 2937 10 90	*10	Protirelin tartrate (INNM)	0
ex 2937 29 90	*10	Methylprednisolone (INN) and its salts and esters	0
ex 2937 29 90	*20	Mometasone furoate (INNM)	0
ex 2937 29 90	*30	Alclometasone 17,21-dipropionate (INNM)	0
ex 2937 99 00	*10	Calcitonin (INN) (porcine)	0
ex 2937 99 00	*20	Calcitonin (INN) (salmon), and its salts	0
ex 2937 99 00	*30	Des-1-alanine-[125-serine] interleukin-2 (human)	0
ex 2939 90 90	*10	Irinotecan (INN)	0
ex 3004 40 90	*10		0
ex 2940 00 90	*30	Ribose	0
ex 2941 10 00	*20	Piperacillin sodium (INNM)	
ex 3004 10 10	*10		3,6
ex 2941 10 00	*30	Piperacillin (INN)	4,1
ex 2941 50 00	*10	Clarithromycin (INN)	2,7
ex 2941 90 00	*01	Cefaclor (INN) and its hydrates, salts and esters	0
ex 2941 90 00	*02	Amikacin (INN) and its salts	
ex 3004 20 90	*10		0
ex 2941 90 00	*03	Amphotericin B (INN)	0
ex 2941 90 00	*04	Clindamycin (INN) and its salts and esters	0
ex 2941 90 00	*05	Lincomycin (INN) and its salts and esters, for the manufacture of medicaments of heading No 3003 or 3004 (a)	0
ex 2941 90 00	*06	Tobramycin (INN) and its salts	0
ex 2941 90 00	*08	Monensin (INN) and its salts	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 2941 90 00	*09	Spectinomycin dihydrochloride (INN), pentahydrate	0
ex 2941 90 00	*13	Midecamycin (INN) and its acetates	2,7
ex 2941 90 00	*14	Cefoxitin sodium (INN)	0
ex 2941 90 00	*15	Cefradine (INN)	0
ex 2941 90 00	*16	Kitasamycin (INN) and its tartrate	0
ex 2941 90 00	*18	Latamoxef (INN) and its salts and esters	0
ex 2941 90 00	*19	Josamycin (INN)	2,7
ex 2941 90 00	*21	Mitomycin (INN)	0
ex 3004 20 90	*30		0
ex 2941 90 00	*23	Bleomycin sulphate (INN)	0
ex 3004 20 90	*20		0
ex 2941 90 00	*24	Dibekacin sulphate (INN)	0
ex 2941 90 00	*25	Sisomycin sulphate (INN)	0
ex 2941 90 00	*26	Spectinomycin sulphate (INN)	0
ex 2941 90 00	*27	Netilmicin sulphate (INN)	0
ex 2941 90 00	*28	Ribostamycin sulphate (INN)	0
ex 2941 90 00	*29	Neomycin sulphate (INN)	0
ex 2941 90 00	*31	Aztreonam (INN)	0
ex 2941 90 00	*32	Imipenem (INN)	0
ex 2941 90 00	*33	Cefotetan (INN)	0
ex 2941 90 00	*34	Ceftizoxime sodium (INN)	0
ex 2941 90 00	*35	Kanamycin sulphate (INN)	0
ex 2941 90 00	*36	Cefotiam dihydrochloride (INN)	0
ex 2941 90 00	*37	Cefmenoxime hemihydrochloride (INN)	0
ex 2941 90 00	*38	Vancomycin (INN) and its salts and esters	0
ex 3004 20 90	*50		0
ex 2941 90 00	*39	Cefixime (INN)	0
ex 2941 90 00	*41	Pirarubicin (INN)	0
ex 2941 90 00	*42	Rokitamycin (INN)	2,7
ex 2941 90 00	*43	Ceftibuten (INN)	0
ex 2941 90 00	*44	1-(Isopropoxycarbonyloxy)ethyl ester of cefpodoxime (INN)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3002 10 10	*10	Tetanus-immunoglobulin	0
ex 3002 10 10	*20	Anti-tetanus immunoplasma	0
ex 3002 10 10	*60	Rabies immunoplasma	0
ex 3002 10 91	*10	Anti-haemophilic globulin and anti-Rh ₀ (D)-globulin derived from human blood	0
ex 3002 10 91	*20	Gamma-globulin, in solution, derived from human blood	0
ex 3002 10 91	*30	Lyophilized gamma-globulin derived from human blood	0
ex 3002 10 99	*10	Unsterile blood-serum obtained from the blood of a bovine foetus or non-immunized newly-born calf	0
ex 3002 10 99	*30	Plasma, frozen, derived from the blood of horses, for the manufacture of serum gonadotrophin (INN) (a)	0
ex 3002 90 90	*10	Purified lysate of the Human Immunodeficiency Virus, obtained from human T-cells infected with the Gallo HTLV-IIIb strain	0
ex 3003 10 00	*10	Mixture of piperacillin (INN) and tazobactam (INN)	3,2
ex 3003 39 00	*10	Mixture of oestrogens of equine origin, in the form of powder	0
ex 3003 40 00	*10	Granules containing by weight 40 % or more but not more than 50 % of theophylline	0
ex 3003 90 90	*10	Granules containing by weight 0,7 % or more but not more than 1,2 % of glycerol trinitrate	0
ex 3004 39 90	*10	Preparation containing des-1-alanine-(125-serine) interleukin-2 (human), obtained from genetically-manipulated <i>Escherichia coli</i>	0
ex 3004 90 19	*10	Purified collagen dispersed in a phosphate physiological saline solution, whether or not containing lidocaine (INN)	3
ex 3004 90 99	*10	Carboplatin (INN) mixed with mannitol	0
ex 3005 10 00	*10	Self-adhesive plastic film, coated or covered on one side with glycerol trinitrate mixed with an acrylic adhesive, containing 4 mg/cm ² or more of glycerol trinitrate, protected by a release sheet, not put up in forms or packings for retail sale	0
3201 20 00		Tanning extracts of wattle (mimosa)	0
ex 3201 90 90	*10	Tanning extracts of eucalyptus	3,2
ex 3201 90 90	*20	Tanning extracts derived from gambier and myrobalan fruits	0
ex 3206 49 90	*10	Black preparation of iron-oxide pigments, in liquid form, with a maximum particle-size not exceeding 20 nanometres and containing by weight 25 % or more of iron evaluated as Fe ₂ O ₃ , for the manufacture of goods of heading No 3304 or 9608 (a)	0
ex 3207 40 90	*10	Glass, in the form of flakes of a length of 0.1 mm or more but not exceeding 3,5 mm and of a thickness of 2 micrometres or more but not exceeding 5 micrometres	0
ex 3207 40 90	*20	Glass, in the form of powder or granules, containing by weight 99 % or more of silicon dioxide	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3208 90 10 ex 3911 90 90	*20 *86	Copolymer of <i>p</i> -cresol and divinylbenzene, in the form of a solution in <i>N,N</i> -dimethylacetamide containing by weight 48 % or more of polymer	0
ex 3402 11 00	*10	Aqueous solution containing by weight 30 % or more but not more than 50 % of disodium alkyl [oxydi(benzenesulphonate)]	0
ex 3402 90 10	*20	Mixture of docusate sodium (INN) and sodium benzoate	0
ex 3402 90 90	*10	Crystalline powder obtained by the reaction of trisodium phosphate with a mixture of sodium hypochlorite and sodium chloride ('chlorinated trisodium phosphate'), containing by weight: — 3,5 % or more of available chlorine, measured iodometrically and — 17,0 % or more of phosphorus evaluated as P ₂ O ₅	0
ex 3504 00 00	*10	Purified antigens obtained from genetically-manipulated yeast-cells, for the manufacture of detection-tests for hepatitis-C (a)	0
ex 3505 10 50	*20	O-(2-Hydroxyethyl)-derivative of hydrolysed waxy maize-starch	0
ex 3507 90 00	*10	Bromelains (INN)	0
ex 3507 90 00	*40	Lipoprotein lipase	0
ex 3507 90 00	*50	Serrapeptase (INN)	0
ex 3507 90 00	*60	Aspergillus alkaline protease	0
ex 3507 90 00	*70	Enzymatic preparation based on thermolysine	0
ex 3507 90 00	*80	L-Lactate: oxygen-2-oxidoreductase, non-decarboxylating	0
ex 3701 99 00	*10	Plate of quartz or of glass, covered with a film of chromium and coated with a photo-sensitive or electron-sensitive resin, for the manufacture of masks for integrated circuits (a)	0
ex 3702 31 90	*10	Colour negative film: — of a width of 75 mm or more but not exceeding 105 mm and — of a length of 100 m or more, for the manufacture of instant-picture film-packs (a)	0
3805 10 90		Sulphate turpentine	0
3805 20 00		Pine oil	1,7
ex 3806 90 00	*20	Hydroabietyl alcohol	0
ex 3809 91 00	*10	Mixture of (5-ethyl-2-methyl-2-oxo-1,3,2λ ⁵ -dioxaphosporan-5-ylmethyl) methyl methylphosphonate and bis(5-ethyl-2-methyl-2-oxo-1,3,2λ ⁵ -dioxaphosporan-5-ylmethyl) methylphosphonate	0
ex 3811 21 00	*10	Salts of dinonylnaphthalenesulphonic acid, in the form of a solution in mineral oils	0
ex 3812 20 00	*10	Reaction mixture containing benzyl 3-isobutyryloxy-1-isopropyl-2,2-dimethylpropyl phthalate and benzyl 3-isobutyryloxy-2,2,4-trimethylpentyl phthalate	0
ex 3812 30 80	*10	Tetraaluminium nonamagnesium dicarbonate hexacosahydroxide heptahydrate, coated with a surface-active agent	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3815 12 00	*10	Catalyst, in the form of granules or rings of a diameter of 3 mm or more but not exceeding 10 mm, consisting of silver on an aluminium-oxide support and containing by weight 8 % or more but not more than 20 % of silver	0
ex 3815 19 00	*01	Catalyst, in the form of grains of which 90 % or more by weight have a particle-size not exceeding 10 micrometres, consisting of a mixture of oxides on a magnesium-silicate support, containing by weight: — 20 % or more but not more than 35 % of copper and — 2 % or more but not more than 3 % of bismuth, and of an apparent specific gravity of 0,2 or more but not exceeding 1,0	0
ex 3815 19 00	*03	Catalyst, consisting of chromium trioxide or dichromium trioxide fixed on a silicon-dioxide support, of a pore-volume, as determined by the nitrogen-absorption method, of 2 cm ³ /g or more	0
ex 3815 19 00	*11	Catalyst consisting of chromium oxides and titanium dioxide fixed on a support of silicon dioxide, aluminium oxide or aluminium phosphate	0
ex 3815 19 00	*13	Catalyst consisting of titanium tetrachloride supported on magnesium dichloride, in the form of a suspension in mineral oil, for use in the manufacture of polypropylene (a)	0
ex 3815 19 00	*14	Catalyst, in the form of spheres of a diameter of 4,2 mm or more but not exceeding 9 mm, consisting of a mixture of oxides of molybdenum, tungsten, vanadium, copper and strontium, on a support of silicon dioxide and/or aluminium oxide, for use in the manufacture of acrylic acid (a)	0
ex 3815 19 00	*15	Catalyst consisting of organo-metallic compounds of titanium, magnesium and aluminium on a support of silicon dioxide, in the form of a suspension in tetrahydrofuran	0
ex 3815 90 00	*15	Catalyst, in the form of rodlets of a diameter of 4 mm or more but not exceeding 6 mm, consisting of a mixture of oxides containing by weight more than 96 % of oxides of molybdenum, vanadium, nickel and antimony, for use in the manufacture of acrylic acid (a)	0
ex 3815 90 00	*20	Catalyst, in powder form, consisting of a mixture of titanium trichloride and aluminium chloride, containing by weight: — 20 % or more but not more than 30 % of titanium and — 55 % or more but not more than 72 % of chlorine	0
ex 3815 90 00	*25	Catalyst, in the form of rodlets of a diameter of 4 mm or more but not exceeding 6 mm, consisting of a mixture of oxides containing by weight more than 96 % of oxides of molybdenum, bismuth, nickel, iron and silicon, for use in the manufacture of acrylaldehyde (a)	0
ex 3815 90 00	*35	Catalyst, in the form of a suspension in oil, consisting of titanium trichloride and aluminium trichloride, containing by weight (on an oil-free basis): — 15 % or more but not more than 30 % of titanium and — 40 % or more but not more than 72 % of chlorine	0
ex 3815 90 00	*40	Catalyst consisting of ethyltriphenylphosphonium acetate in the form of a solution in methanol	0
ex 3815 90 00	*55	Catalyst, in the form of rodlets of a length of 5 mm or more but not exceeding 8 mm, consisting of a mixture of oxides of iron, molybdenum and bismuth, for use in the manufacture of acrylic acid (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3815 90 00	*60	Catalyst, in the form of pellets, consisting of mixed vanadium and phosphorus oxides, containing by weight 0,5 % or less of one of the following elements: lithium, potassium, sodium, cadmium or zinc, for use in the manufacture of maleic anhydride from butane (a)	0
ex 3815 90 00	*80	Catalyst, in the form of rodlets, consisting of an acid aluminosilicate (zeolite): — with a mole-ratio of silicon dioxide : dialuminium trioxide of not less than 500 : 1 and — containing by weight 0,2 % or more but not more than 0,8 % of platinum	0
ex 3815 90 00	*86	Catalyst based on a mordenite zeolite, in the form of granules, for use in the manufacture of mixtures of methylamines containing by weight 50 % or more of dimethylamine (a)	0
ex 3822 00 00	*10	Lyophilized extract of the blood cells of <i>Limulus polyphemus</i> (Limulus amoebocyte lysate)	0
ex 3823 90 20	*10	Acid aluminosilicate (artificial zeolite of the Y type) in the sodium form, containing by weight not more than 11 % of sodium evaluated as sodium oxide, in the form of rodlets	0
ex 3823 90 91	*01	Intermediate products of the antibiotics manufacturing process obtained from the fermentation of <i>Streptomyces tenebrarius</i> , whether or not dried, for use in the manufacture of human medicaments of No 3004 (a)	0
ex 3823 90 91	*02	Intermediate products of the antibiotics manufacturing process obtained from the fermentation of <i>Micromonospora purpurea</i> , whether or not dried	0
ex 3823 90 91	*03	Cholic acid and 3 α ,12 α -dihydroxy-5 β -cholan-24-oic acid (deoxycholic acid), crude	0
ex 3823 90 91	*04	Products obtained by the N-ethylation of sisomycin (INN)	0
ex 3823 90 91	*05	Intermediate products from the manufacture of monensin salts	0
ex 3823 90 91	*06	Crude bile acids	0
ex 3823 90 91	*09	Intermediate products of the antibiotics manufacturing process obtained from the fermentation of <i>Micromonospora inyoensis</i> , whether or not dried	0
ex 3823 90 91	*15	Residues of manufacture containing by weight 40 % or more of 11 β ,17,20,21-tetrahydroxy-6-methylpregna-1,4-dien-3-one-21-acetate	0
ex 3823 90 91	*16	4-(2-Aminoethylthiomethyl)-1,3-thiazol-2-ylmethyldimethylamine, in the form of a solution in toluene	0
ex 3823 90 98	*08	Colloidal diantimony pentaoxide	0
ex 3823 90 98	*13	Mixture of nitromethane and 1,2-epoxybutane	0
ex 3823 90 98	*17	Tetrakis(2-chloroethyl)ethylene bis(phosphate), not chemically defined	5
ex 3823 90 98	*18	Grains, consisting of a mixture of dialuminium trioxide and zirconium dioxide, containing by weight: — 70 % or more but not more than 78 % of dialuminium trioxide and — 19 % or more but not more than 26 % of zirconium dioxide	5,2

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3823 90 98	*19	Grains, consisting of a mixture of dialuminium trioxide and zirconium dioxide, containing by weight: — 54 % or more but not more than 62 % of dialuminium trioxide and — 36 % or more but not more than 44 % of zirconium dioxide	5,2
ex 3823 90 98	*21	Crude lithium hypochlorite	0
ex 3823 90 98	*23	Mixture of 2-(perfluoroalkyl)ethanethiols of a perfluoroalkyl chain-length of 6 or more carbon atoms but not exceeding 20 carbon atoms	0
ex 3823 90 98	*25	Mixed oxides of barium, titanium and other metals, in the form of powder, containing by weight: — 5 % or more of barium and — 15 % or more of titanium, for use as dielectric materials in the manufacture of multilayer ceramic capacitors (a)	0
ex 3823 90 98	*26	Lithium niobate wafers, undoped	0
ex 3823 90 98	*27	Preparation, in the form of powder, containing by weight 75 % or more of zinc bis[3,5-bis(1-phenylethyl)salicylate]	0
ex 3823 90 98	*31	Film consisting of the oxides of barium, calcium and either titanium or zirconium, mixed with binding materials	0
ex 3823 90 98	*32	Mixture of amines derived from dimerized fatty acids, of an average molecular weight of 520 or more but not exceeding 550	0
ex 3823 90 98	*34	Preparation consisting essentially of alkaline asphalt sulphonate, of: — a specific gravity of 0,9 or more but not exceeding 1,5 and — a solubility in water of 70 % by weight or more	0
ex 3823 90 98	*35	Anti-corrosion preparations consisting of salts of dinonylnaphthalenesulphonic acid, either: — on a support of mineral wax, whether or not modified chemically, or — in the form of a solution in an organic solvent	0
ex 3823 90 98	*36	Mixture of lithium diisopropylamide (lithium salt of diisopropylamine) and magnesium bis(diisopropylamide) (magnesium salt of diisopropylamine), in the form of a solution in organic solvents	0
ex 3823 90 98	*37	Calcined bauxite (refractory grade)	0
ex 3823 90 98	*38	Magnetisable iron oxide, in the form of powder, containing by weight: — 30 % or more but not more than 38 % of bivalent iron in relation to the total iron and — 1 % or more but not more than 4 % of cobalt	0
ex 3823 90 98	*39	Spent catalyst, in the form of rodlets of diameter of 1 mm or more but not exceeding 3 mm, containing a mixture of sulphides of tungsten and of nickel on a support of zeolite, containing by weight not more than 10 % of tungsten and not more than 10 % of nickel, for regeneration as a catalyst for hydrocarbon-cracking (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3823 90 98	*40	Mixture containing by weight: — 7 % or more but not more than 9 % of 2-methyl-1,3-phenylene diisocyanate, — 31 % or more but not more than 34 % of 4-methyl-1,3-phenylene diisocyanate, — 10 % or more but not more than 13 % of 2,4'-methylenediphenyl diisocyanate, — 46 % or more but not more than 49 % of 4,4'-methylenediphenyl diisocyanate	0
ex 3823 90 98	*42	3-(1-Ethyl-1-methylpropyl)isoxazol-5-ylamine, in the form of a solution in toluene	0
ex 3823 90 98	*43	Mixture of magnesium bromide 2-oxoperhydroazepin-1-ide and <i>epsilon</i> -caprolactam	0
ex 3823 90 98	*45	Disodium 9,10-dihydro-9,10-dioxoanthracene-2,7-disulphonate, containing by weight 10 % or more but not more than 20 % of sodium sulphate	0
ex 3823 90 98	*48	Eutectic alloy wholly of potassium and sodium, containing by weight 77 % or more but not more than 79 % of potassium	0
ex 3823 90 98	*49	Blend of terephthaloyl dichloride and isophthaloyl dichloride	0
ex 3823 90 98	*51	Acid-hydrolysed casein, containing by weight: — 8 % or more but not more than 11 % of nitrogen and — 10 % or more but not more than 20 % of sodium chloride, for the manufacture of prepared culture media for development of micro-organisms (a)	0
ex 3823 90 98	*52	Preparation consisting by weight of 90 % or more of 3a,4,7,7a-tetrahydro-4,7-methanoindene (dicyclopentadiene), a synthetic rubber and — either an aluminium-alkyl compound — or an organic complex of tungsten	0
ex 3823 90 98	*55	Mixture of sucrose esters, derived from the esterification of sucrose with industrial stearic acid	0
ex 3901 20 00	*10	Polyethylene, in one of the forms mentioned in note 6 (b) to Chapter 39, of a specific gravity of 0,945 or more but not exceeding 0,985, for the manufacture of films for typewriter ribbon or similar ribbon (a)	0
ex 3901 20 00	*20	Polyethylene, containing by weight 35 % or more but not more than 45 % of mica	0
ex 3901 20 00	*40	Polyethylene, in one of the forms mentioned in note 6 (b) to Chapter 39, of a specific gravity of 0,958 or more at 23 °C, containing: — 50 mg/kg or less of aluminium, — 2 mg/kg or less of calcium, — 2 mg/kg or less of chromium, — 2 mg/kg or less of iron, — 2 mg/kg or less of nickel, — 2 mg/kg or less of titanium and — 8 mg/kg or less of vanadium, for the manufacture of chlorosulphonated polyethylene (a)	0
ex 3901 90 00	*93	Ionomer resin consisting of a salt of a terpolymer of ethylene with isobutyl acrylate and methacrylic acid	0
ex 3902 90 00	*96	Polybut-1-ene, a copolymer of but-1-ene with ethylene containing by weight 10 % or less of ethylene, or a blend of polybut-1-ene with polyethylene and/or polypropylene containing by weight 10 % or less of polyethylene and/or 25 % or less of polypropylene, in one of the forms mentioned in note 6 (b) to Chapter 39	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3902 90 00 ex 3903 90 00	*97 *60	A-B Block copolymer of polystyrene and an ethylene-propylene copolymer, containing by weight 40 % or less of styrene, in one of the forms mentioned in note 6 (b) to Chapter 39	0
ex 3903 90 00	*10	Copolymer, solely of styrene with allyl alcohol, of an acetyl value of 175 or more	0
ex 3903 90 00	*20	Copolymer, entirely of styrene with maleic anhydride, or entirely of styrene with maleic anhydride and an acrylic monomer, whether or not containing a styrene-butadiene block copolymer, in one of the forms mentioned in note 6 (b) to Chapter 39, for the manufacture of sheetings for head-liners for cars (a)	0
ex 3903 90 00	*30	Brominated polystyrene, containing by weight 58 % or more but not more than 71 % of bromine, in one of the forms mentioned in note 6 (b) to Chapter 39	0
ex 3903 90 00	*40	Copolymer of styrene with 2-ethylhexyl acrylate or with <i>n</i> -butyl acrylate, containing: — 10 mole % or more but not more than 16 mole % of acrylate, — 0,2 mg/kg or less of sodium and — 0,1 mg/kg or less of calcium	0
ex 3903 90 00 ex 3905 19 00	*50 *20	Block copolymer of styrene and vinyl acetate, containing by weight 5 % or more but not more than 55 % of vinyl acetate	0
ex 3904 40 00	*91	Copolymer of vinyl chloride with vinyl acetate and vinyl alcohol, containing by weight: — 87 % or more but not more than 92 % of vinyl chloride, — 2 % or more but not more than 9 % of vinyl acetate and — 1 % or more but not more than 8 % of vinyl alcohol, in one of the forms mentioned in note 6 (a) or (b) to Chapter 39	0
ex 3904 50 00	*91	Copolymer of vinylidene chloride with acrylonitrile, in the form of expansible beads of a diameter of 4 micrometres or more but not more than 20 micrometres	0
ex 3904 50 00	*92	Copolymer of vinylidene chloride with vinyl chloride, containing by weight 79,5 % or more of vinylidene chloride, in one of the forms mentioned in note 6 (a) or (b) to Chapter 39, for the manufacture of fibres, monofilament or strip (a)	0
ex 3904 69 00	*91	Copolymer of ethylene with chlorotrifluoroethylene and hexafluoro(2-methylpropene), in one of the forms mentioned in note 6 (b) to Chapter 39	0
ex 3904 69 00	*93	Polyvinyl fluoride, in one of the forms mentioned in note 6 (b) to Chapter 39	0
ex 3904 69 00	*95	Copolymer of ethylene with chlorotrifluoroethylene, in one of the forms mentioned in note 6 (b) to Chapter 39	0
ex 3905 90 00	*93	Poly(vinyl formal), in one of the forms mentioned in note 6 (b) to Chapter 39, of a molecular weight of 10 000 or more but not exceeding 40 000 and containing by weight: — 9,5 % or more but not more than 13 % of acetyl groups evaluated as vinyl acetate and — 5 % or more but not more than 6,5 % of hydroxy groups evaluated as vinyl alcohol	0
ex 3906 10 00	*10	Polymethyl methacrylate, in the form of expansible beads containing 2-methylpentane as blowing agent	0
ex 3906 90 00	*10	Poly[N-(3-hydroxyimino-1,1-dimethylbutyl)acrylamide]	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3906 90 00	*20	Copolymer of 2-diisopropylaminoethyl methacrylate with decyl methacrylate, in the form of a solution in N,N-dimethylacetamide containing by weight 55 % or more of copolymer	0
ex 3906 90 00	*30	Copolymer of acrylic acid with 2-ethylhexyl acrylate, containing by weight 10 % or more but not more than 11 % of 2-ethylhexyl acrylate	0
ex 3906 90 00	*40	Copolymer of acrylonitrile with methyl acrylate, modified with polybutadiene-acrylonitrile (NBR)	0
ex 3906 90 00	*50	Polymerization product of acrylic acid with alkyl methacrylate and small quantities of other monomers, for use as a thickener in the manufacture of textile printing pastes (a)	0
ex 3906 90 00	*70	Polymerization product of acrylic acid with small quantities of a polyunsaturated monomer, for use as a thickener in textile pigment printing pastes or in the manufacture of medicaments of heading No 3003 or 3004 (a)	0
ex 3906 90 00	*80	Polymerization product of acrylic acid with small quantities of a polyunsaturated monomer, for use as a stabilizer in emulsions or dispersions with a pH of more than 13 (a)	6
ex 3907 20 11	*10	Poly(ethylene oxide)	0
ex 3907 20 90	*15	Bis[2-(ω-hydroxy-poly(ethyleneoxy))ethyl] hydroxymethylphosphonate	0
ex 3907 20 90	*20	Poly(oxypropylene) having alkoxysilyl end-groups	0
ex 3907 20 90	*40	Poly[oxy-1,4-phenyleneisopropylidene-1,4-phenyleneoxy-(2-hydroxytrimethylene)], of an average molecular weight of more than 26 000, in one of the forms mentioned in note 6 (b) to Chapter 39	0
ex 3907 20 90	*60	α-4-Hydroxybutyl-ω-hydroxypoly(oxytetramethylene), containing less than 1 mg/kg of halogen and less than 1 mg/kg of metal, and of a colour not exceeding 20 units on the Hazen scale	0
ex 3907 20 90	*70	Homopolymer of 1-chloro-2,3-epoxypropane (epichlorohydrin)	0
ex 3907 20 90	*80	Copolymer of 1-chloro-2,3-epoxypropane with ethylene oxide	0
ex 3907 91 90	*10	Diallyl phthalate prepolymer	0
ex 3907 99 00	*30	Poly(oxy-1,4-phenylenecarbonyl), in the form of powder	0
ex 3907 99 00	*40	Poly(ethylene naphthalene-2,6-dicarboxylate)	0
ex 3908 90 00	*10	Poly(iminomethylene-1,3-phenylenemethyleneiminoadipoyl), in one of the forms mentioned in note 6 (b) to Chapter 39	0
ex 3909 40 00	*10	Polycondensation product of phenol with formaldehyde, in the form of hollow spheres of a diameter of less than 150 micrometres	0
ex 3911 90 10	*20	Polyfurfuryl alcohol	0
ex 3912 11 00	*10	Non-plasticized cellulose triacetate, in the form of flakes, for the manufacture of cellulose triacetate yarn (a)	0
ex 3912 39 10	*10	Ethylcellulose, not plasticized	0
ex 3912 39 90	*10	Cellulose, both hydroxyethylated and ethylated, insoluble in water	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3912 39 90	*20	Hydroxypropylcellulose	0
ex 3912 39 90	*40	Cellulose, both hydroxyethylated and alkylated with alkyl chain-lengths of 3 or more carbon atoms	0
ex 3912 90 10	*20	Cellulurate (INN), non-plasticized, in the form of powder: — with a butyryl content by weight of 25 % or more, as determined by the ASTM D 817-72 method and — of a viscosity not exceeding 57 poise, as determined by the ASTM D 817-72 method, for use as a binding agent in the manufacture of paints or lacquers for cars and aircraft, or for the manufacture of heat-transfer printing inks (a)	0
ex 3913 90 80	*30	Chondroitinsulphuric acid, sodium salt	0
ex 3914 00 00	*20	Colestipol hydrochloride (INN)	0
ex 3915 90 93	*10	Waste and scrap of photographic (including cinematographic) and X-ray film	0
ex 3915 90 99	*10		0
ex 3919 90 31	*30	Polyethylene terephthalate film, dyed in the mass, of a thickness not exceeding 25 micrometres and metallized on one side	6
ex 3920 62 00	*05		
ex 3921 90 19	*60		
ex 3919 90 31	*40	Reflecting polyester sheeting embossed in a regular pyramidal pattern, for the manufacture of safety stickers and badges, safety clothing and accessories thereof, or of school satchels, bags or similar containers (a)	0
ex 3920 62 00	*13		
ex 3920 63 00	*30		
ex 3920 69 00	*30		
ex 3919 90 50	*92	Polyvinyl chloride sheeting, of a thickness of less than 1 mm, coated with an adhesive in which are embedded hollow glass balls of a diameter not exceeding 100 micrometres	0
ex 3920 10 21	*92	Polyethylene film, of a thickness of 20 micrometres or more but not exceeding 40 micrometres, for the production of photo-resist film used in the manufacture of semiconductors or printed circuits (a)	0
ex 3920 10 21	*94	Laminated sheet or strip consisting of a film composed of a blend of a copolymer of ethylene with vinyl acetate and a modified ethylene-propylene-elastomer (EPM) or a modified ethylene-propylene-diene elastomer (EPDM), coated or covered on both sides with a film of a copolymer of ethylene with vinyl acetate	0
ex 3920 10 29	*93		
ex 3921 90 60	*26		
ex 3920 10 21	*95	Film of polyethylene, of a thickness of 20 micrometres or more but not exceeding 45 micrometres, containing calcium carbonate in the mass, for the manufacture of napkins for babies or of sanitary towels or of tampons or of disposable surgical gowns (a)	0
ex 3920 10 90	*93	Synthetic paper pulp, in the form of moist sheets, made from unconnected finely-branched polyethylene fibrils, whether or not blended with cellulose fibres in a quantity not exceeding 15 %, containing polyvinyl alcohol dissolved in water as the moistening agent	0
ex 3920 10 90	*95	Film of a thickness not exceeding 0,20 mm, of a blend of polyethylene and a copolymer of ethylene with oct-1-ene, embossed in a regular rhomboidal pattern, for coating both sides of a layer of unvulcanized rubber (a)	0
ex 3920 20 90	*91	Synthetic paper pulp, in the form of moist sheets, made from unconnected finely-branched polypropylene fibrils, whether or not blended with cellulose fibres in a quantity not exceeding 15 %, containing polyvinyl alcohol dissolved in water as the moistening agent	0
ex 3920 30 00	*20	Laminated sheet or strip, consisting of a film of a thickness of 100 micrometres or more but not exceeding 200 micrometres, composed of a blend of a thermoplastic elastomer (TPE) of styrene-butadiene-styrene (SBS) with polyethylene or polypropylene, coated or covered on both sides with a film of polypropylene of a thickness not exceeding 20 micrometres	0
ex 3921 90 60	*27		

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3920 42 11 ex 3920 42 91	*92 *92	Reflecting sheeting, consisting solely of a single layer of polyvinyl chloride, wholly embossed on one side in a regular pyramidal pattern	0
ex 3920 42 91	*93	Sheeting of polyvinyl chloride, stabilized against ultraviolet rays, without any holes, even microscopic, of a thickness of 60 micrometres or more but not exceeding 80 micrometres, containing 30 or more but not more than 40 parts of plasticizer to 100 parts of polyvinyl chloride	0
ex 3920 59 00	*10	Copolymer of acrylic and methacrylic esters, in the form of film of a thickness not exceeding 150 micrometres	0
ex 3920 62 00	*03	Polyethylene terephthalate film, of a thickness of 72 micrometres or more but not exceeding 79 micrometres, for the manufacture of flexible magnetic disks (floppy disks) (a)	0
ex 3920 62 00 ex 3920 63 00 ex 3921 90 19	*07 *50 *20	Polyester film of a thickness not exceeding 70 micrometres, coated on one side with a substance which becomes coloured in the presence of salicylic acid at a temperature not exceeding 100 °C	0
ex 3920 69 00 ex 3921 90 19	*40 *15	Iridescent film of polyester and polymethyl methacrylate	0
ex 3920 69 00	*50	Polycondensation product of terephthalic acid with a mixture of cyclohex-1,4-ylenedimethanol and ethane-1,2-diol, in the form of a film	0
ex 3920 91 00	*92	Plasticized film of polyvinyl butyral, containing by weight: — either 14,5 % or more but not more than 17,5 % of dihexyl adipate — or 14,5 % or more but not more than 28,5 % of dibutyl sebacate	0
ex 3920 92 00	*10	Film consisting of poly(<i>epsilon</i> -caprolactam) blended with: — either 10 % or less by weight of polyethylene — or 10 % or less by weight of ethylene-vinyl acetate copolymer	0
ex 3920 99 19 ex 3921 90 50	*10 *20	Polyimide sheet and strip, uncoated, or coated or covered solely with plastic	0
ex 3920 99 50	*21	Polyvinyl fluoride sheet	0
ex 3920 99 50 ex 3921 90 60	*22 *25	Ion-exchange membranes of fluorinated plastic material, for use in chlor-alkali electrolytic cells (a)	0
ex 3920 99 50	*23	Biaxially-orientated polyvinyl alcohol film, containing by weight 97 % or more of polyvinyl alcohol, uncoated, of a thickness not exceeding 1 mm	0
ex 3920 99 50	*24	Film entirely of polyvinyl alcohol, of a thickness not exceeding 1 mm and containing by weight: — 2 % or less of unhydrolysed acetate groups evaluated as vinyl acetate and — 5 % or more but not more than 25 % of glycerol as plasticizer, for the manufacture of roof-windows (a)	0
ex 3920 99 50	*26	Poly(1-chlorotrifluoroethylene) film	0
ex 3920 99 50	*27	Film of a mixture of polyvinylidene fluoride with an acrylic polymer, of a thickness of 40 micrometres or more but not exceeding 60 micrometres	0
ex 3920 99 50	*28	Film and sheet of a copolymer of ethylene with chlorotrifluoroethylene, of a thickness of 12 micrometres or more but not exceeding 400 micrometres	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 3920 99 50 ex 3921 90 60	*32 *22	Sheet of a thickness of 1 mm or more but not exceeding 3 mm, consisting of a copolymer of ethylene with chlorotrifluoroethylene, whether or not backed with a woven glass-fibre fabric	0
ex 3920 99 50	*36	Film entirely of polyvinyl alcohol, of a thickness not exceeding 1 mm and of a width of 2,20 m or more, with an extension at break, in the transverse direction, of 350 % or more	0
ex 3920 99 50	*37	Biaxially-oriented film of polyvinyl alcohol, coated on both sides, of a total thickness of less than 1 mm	0
ex 3920 99 50 ex 3921 90 60	*38 *29	Iridescent film of polyester, polyethylene and an ethylene-vinyl acetate copolymer	0
ex 3921 90 19	*35	Composite plate of polycarbonate and polybutylene terephthalate, reinforced with glass fibres	0
ex 3921 90 19	*45	Composite plate of polyethylene terephthalate or of polybutylene terephthalate, reinforced with glass fibres	0
ex 3921 90 60	*28	Translucent laminated sheet consisting of a polyester film coated on one surface with metal and metal oxides, and covered on both sides with a film of polyvinyl butyral	0
ex 3926 90 91	*20	Reflecting sheeting or tape, consisting of a facing-strip of polyvinyl chloride embossed in a regular pyramidal pattern, heat-sealed in parallel lines or in a grid-pattern to a backing-strip of plastic material, or of knitted or woven fabric covered on one side with plastic material	0
ex 4104 10 91 ex 4104 21 00	*10 *10	Leather of East India-kip, whole, whether or not the heads and legs have been removed, each of a net weight exceeding 4,5 kg but not exceeding 8 kg, not further prepared than vegetable-tanned, whether or not having undergone certain treatments, but obviously unsuitable for immediate use for the manufacture of leather articles	0
4105 11 91 4105 11 99 4105 12 10 4105 12 90 4105 19 10 4105 19 90		Sheep or lamb skin leather, without wool on, tanned or retanned but not further prepared, whether or not split, other than leather of heading No 4108 or 4109	0
4106 11 90 4106 12 00 4106 19 00		Goat or kid skin leather, without hair on, tanned or retanned but not further prepared, whether or not split, other than leather of heading No 4108 or 4109	0
4107 10 10 4107 29 10 4107 90 10		Leather of other animals, without hair on, not further prepared than tanned, other than leather of heading No 4108 or 4109	0
ex 4416 00 90	*10	Used casks and barrels of oak, whether assembled or not; their staves and heads	0
4501 10 00		Natural cork, raw or simply prepared	0
ex 4811 21 00	*10	Impregnated paper coated or covered with a pressure-sensitive self-adhesive layer, the whole: — of a tensile of 2 700 N/m or more but not exceeding 3 700 N/m in the machine direction (as determined by the DIN 53112 method), — of a stretch factor of 1,5 % or more but not exceeding 3,0 % in the machine direction (as determined by the DIN 53112 method) - and — of adhesivity on stainless steel (as determined by the DIN 30646 method) of 50 N/m or more but not exceeding 225 N/m, at a temperature of 23 °C (± 3 °C) and a relative humidity of 50 % (± 5 %)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 4823 90 90	*12	Strips of paper glued to one another to form a honeycomb of a height not exceeding 13 cm, for agricultural purposes (a)	0
5002 00 00		Raw silk (not thrown)	0
ex 5004 00 10	*10	Yarn spun entirely from silk, not put up for retail sale	2,5
ex 5004 00 90	*10		
ex 5005 00 10	*10	Yarn spun entirely from silk waste (noil), not put up for retail sale	0
ex 5005 00 90	*10		
ex 5306 10 11	*10	Unbleached flax yarn (other than tow yarn) measuring 333,3 decitex or more (not exceeding 30 metric number), for the manufacture of multiple or cabled yarn of the footwear industry or for whipping cables (a)	1,8
ex 5306 10 31	*10		
ex 5402 39 10	*10	Texturized yarn of polypropylene, impregnated with silicone-based water-repellant	0
ex 5402 41 10	*10	Polyamide yarn, not textured, untwisted or with a twist not exceeding 22 turns per metre, of crimpable bicomponent filaments consisting of poly(hexamethylene adipamide) with a copolyamide, for the manufacture of: — knee-length stockings of subheadings 6115 20 11 and 6115 93 30, — women's stockings of subheadings 6115 20 19 and 6115 93 91, or — panty hose (tights) of subheading 6115 11 00 (a)	0
ex 5402 41 30	*10	Yarn of synthetic textile fibres solely of aromatic polyamides obtained by the polycondensation of <i>m</i> -phenylenediamine and isophthalic acid	0
ex 5402 41 90	*10		
ex 5402 49 99	*10	Yarn of polytetrafluoroethylene	0
ex 5402 69 90	*20		
ex 5402 49 99	*30	Yarn of a copolymer of glycollic acid with lactic acid, for the manufacture of surgical sutures (a)	0
ex 5402 49 99	*50	Non-textured filament yarn of polyvinyl alcohol, for the manufacture of sleeves for printing rollers (a)	0
ex 5402 59 90	*20		
ex 5402 69 90	*40		
ex 5402 49 99	*60	Yarn wholly of polyglycollic acid	0
ex 5402 69 90	*10		
ex 5402 49 99	*70	Synthetic filament yarn, single, containing by weight 85 % or more of acrylonitrile, in the form of a wick containing 1 000 continuous filaments or more but not more than 25 000 continuous filaments, of a weight per metre of 0,12 g or more but not exceeding 3,75 g and of a length of 100 m or more, for the manufacture of carbon-fibre yarn (a)	0
ex 5404 10 90	*10	Monofilament of polytetrafluoroethylene	0
ex 5404 10 90	*20	Monofilament of poly(1,4-dioxanone)	0
ex 5404 10 90	*30	Monofilament of a copolymer of 1,3-dioxan-2-one with 1,4-dioxan-2,5-dione, for the manufacture of surgical sutures (a)	0
ex 5404 90 90	*10	Strip of polytetrafluoroethylene, with an extension at break not exceeding 25 %	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 5407 71 00	*10	Woven fabrics of polyvinyl alcohol fibres, for machine embroidery	0
ex 5407 71 00	*20	Woven polytetrafluoroethylene-fibre fabric, coated or covered on one side with a copolymer of tetrafluoroethylene and trifluoroethylene having perfluorinated alkoxy side-chains ending in carboxylic-acid or sulphonic-acid groups in the potassium- or sodium-salt form, whether or not coated on the same side with a metallic inorganic compound	0
ex 5903 90 99	*10		
ex 5503 10 19	*10	Textile fibres exclusively of aromatic polyamides obtained by the polycondensation of <i>m</i> -phenylenediamine and isophthalic acid, for uses other than the manufacture of goods of Chapters 60 to 65 or of materials used in the manufacture of such goods (a)	0
ex 5503 10 19	*30	Textile fibres containing by weight: — 1 % or more but not more than 15 % of fibres of poly(<i>p</i> -phenylene terephthalamide) and — 85 % or more of fibres of aromatic polyamides obtained by the polycondensation of <i>m</i> -phenylenediamine and isophthalic acid	5
ex 5503 90 10	*10	Acetalized, multicomponent spun fibres with a matrix fibril structure, consisting of emulsion-polymerized polyvinyl alcohol and polyvinyl chloride	0
ex 5503 90 90	*30		
ex 5503 90 90	*10	Textile fibres of polytetrafluoroethylene	0
ex 5503 90 90	*20	Polyvinyl alcohol fibres, whether or not acetalized	0
ex 5601 30 00	*10		
ex 5503 90 90	*40	Fibres wholly of poly(thio-1,4-phenylene)	0
ex 5504 10 00	*10	Fibres entirely of modal (ISO)	0
ex 5603 00 10	*10	Polyvinyl alcohol nonwovens, in the piece or cut into rectangles:	
ex 5603 00 91	*10	— of a thickness of 200 micrometres or more but not exceeding 280 micrometres	
ex 5603 00 93	*10	and — of a weight of 20 g/m ² or more but not exceeding 50 g/m ²	0
ex 5603 00 10	*20	Nonwovens of aromatic polyamide fibres obtained by polycondensation of <i>m</i> -phenylenediamine and isophthalic acid, in the piece or cut into rectangles	
ex 5603 00 93	*30		
ex 5603 00 95	*30		
ex 5603 00 99	*10		0
ex 5603 00 93	*40	Nonwovens of polypropylene consisting of a melt-blown central layer, laminated on each side with spun-bonded fibres, of a thickness not exceeding 550 micrometres and of a weight not exceeding 80 g/m ² , in the piece or simply cut into rectangular shape, not impregnated	0
ex 5603 00 95	*10		
ex 5903 10 90	*10	Knitted or woven fabrics, coated or covered on one side with artificial plastic material in which are embedded microspheres	0
ex 5903 20 90	*10		
ex 5903 90 99	*20		0
ex 5907 00 00	*10	Textile fabrics, coated with adhesive in which are embedded spheres of a diameter not exceeding 75 micrometres, of a weight not exceeding 550 g/m ²	0
ex 5911 10 00	*10	Needle-punched synthetic-fibre felts on a woven synthetic-fibre base not containing polyester, coated or covered on one side with polytetrafluoroethylene film, for the manufacture of filtration products (a)	0
ex 5911 90 90	*10	Yarn and strip of impregnated polytetrafluoroethylene, whether or not oiled or graphited	0
6305 10 10		Sacks and bags, of a kind used for the packing of goods, used, of jute or of other textile bast fibres of heading No 5303	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 6305 90 00	*10 *91 *93	Sacks and bags, of a kind used for the packing of goods, used, of flax or of sisal	0
ex 6815 99 90	*10	Microspheres: — of a diameter of less than 100 micrometres, — of a refractive index of 2,1 or more but not exceeding 2,4 and — containing by weight more than 90 % of barium and titanium evaluated as barium oxide and titanium dioxide	0
ex 6903 20 90	*10	Yarn of continuous ceramic filaments, each filament containing by weight: — 12 % or more of diboron trioxide, — 26 % or less of silicon dioxide and — 60 % or more of dialuminium trioxide	0
ex 6909 19 00	*20	Plate, of dialuminium trioxide and titanium carbide, of dimensions not exceeding 48×48 mm, or of a diameter not exceeding 125 mm, for the manufacture of magnetic heads (a)	0
ex 6909 19 00	*30	Supports for catalysts, consisting of porous cordierite or mullite ceramic pieces, of an overall volume not exceeding 65 000 ml, having, per 100 mm ² of the cross-section, not less than one continuous channel which may be open at both ends or stopped at one end	0
ex 7006 00 90	*10	Glass plate, coated on one side with chromium and/or with a mixture of diindium trioxide and tin dioxide, of dimensions of 320×352 mm or more but not exceeding 320×400 mm, and of a thickness of 1,1 mm (±0,1 mm), with a flatness deviation not exceeding 25 micrometres, for the manufacture of liquid crystal displays with active matrix (a)	0
ex 7006 00 90	*30	Glass plate, uncoated, of dimensions of 320×352 mm or more but not exceeding 320×400 mm, and of a thickness of 1,1 mm (±0,1 mm), with a flatness deviation not exceeding 25 micrometres, for the manufacture of liquid crystal displays with active matrix (a)	0
ex 7011 20 00	*10	Glass envelopes for monochrome cathode-ray tubes: — of a diagonal screen-measurement of 3,8 cm or more but not exceeding 51 cm and — of a nominal neck-diameter of 13 mm, 20 mm, 29 mm or 37 mm	0
ex 7019 10 59	*10	Yarn of 33 tex or a multiple thereof, ±7,5 %, obtained from continuous spun-glass filaments of a nominal diameter of 3,5 micrometres or of 4,5 micrometres, in which filaments of a diameter of 3 micrometres or more but not exceeding 5,2 micrometres predominate, other than those treated so as to improve their adhesion to elastomers	0
ex 7019 10 59	*30	Yarn of 22 tex ±7,5 %, obtained from continuous spun-glass filaments of a nominal diameter of 5 micrometres, in which filaments of a diameter of 4,2 micrometres or more but not exceeding 5,8 micrometres predominate	0
ex 7019 10 59	*40	Yarn of 33, 34 or 51 tex or a multiple thereof, ±7,5 %, obtained from continuous spun-glass filaments of a nominal diameter of 6 micrometres, in which filaments of a diameter of 5,1 micrometres or more but not exceeding 6,9 micrometres predominate	0
ex 7205 10 00	*10	Magnetisable iron alloy, in the form of granules, containing by weight: — 88 % or more but not more than 91 % of iron and — 4 % or less of cobalt	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 7410 21 00	*10	Sheet or plate of polytetrafluoroethylene, with aluminium oxide or titanium dioxide as a filler or reinforced with glass-fibre fabric, laminated on both sides with copper foil, or sheet of polyimide, laminated on one side or on both sides with copper foil	0
7602 00 19		Waste of aluminium, other (including factory rejects)	0
ex 7905 00 90	*10	Plate of an alloy of zinc, ground and polished on one surface and coated with an epoxide resin on the other surface, of rectangular or square shape, of a length of 300 mm or more but not exceeding 2 000 mm and of a width of 300 mm or more but not exceeding 1 000 mm, and containing: — 10 mg/kg or less of iron, — 10 mg/kg or less of lead, — 700 mg/kg or more but not more than 900 mg/kg of aluminium and — 500 mg/kg or more but not more than 900 mg/kg of magnesium, for the manufacture of sensitised printing plates (a)	0
ex 8103 90 10	*10	Wire of unalloyed tantalum, of a diameter of 0,2 mm or more but not exceeding 0,5 mm, for the manufacture of capacitors (a)	0
ex 8103 90 90	*10	Tube solely of tantalum, or solely of an alloy of tantalum with tungsten containing by weight 2,5 % or less of tungsten	0
ex 8104 90 10	*10	Ground and polished magnesium sheets, of dimensions not exceeding 1 500 × 2 000 mm, coated on one side with an epoxy resin insensitive to light	0
ex 8108 10 10	*10	Titanium sponge	0
8108 10 90		Waste and scrap titanium	0
ex 8111 00 11	*10	Electrolytic manganese of a purity by weight of 99,7 % or more, for the chemical industry (a)	0
ex 8112 99 30	*10	Alloy of niobium (columbium) and titanium, in the form of bars and rods	0
ex 8419 89 80	*10	Immersion-tube (coils) bundles, consisting of an assembly of plastic tubes terminating at each end in a honeycomb-structure (end-fitting) surrounded by a pipe-connector	0
ex 8421 99 00	*91	Parts of equipment, for the purification of water by reverse osmosis, consisting of a bundle of hollow fibres of artificial plastic material with permeable walls, embedded in a block of artificial plastic material at one end and passing through a block of artificial plastic material at the other end, whether or not housed in a cylinder	0
ex 8421 99 00	*92	Parts of equipment for the purification of water by reverse osmosis, consisting essentially of plastic-based membranes, supported internally by woven or non-woven textile materials which are wound round a perforated tube, and enclosed in a cylindrical plastic casing of a wall-thickness not exceeding 4 mm, whether or not housed in a cylinder of a wall-thickness of 5 mm or more	0
ex 5911 90 90	*30		
ex 8421 99 00	*93	Components of separators for the separation or purification of gases from gas mixtures, consisting of a bundle of permeable hollow fibres enclosed within a container, whether or not perforated, of an overall length of 300 mm or more but not exceeding 3 700 mm and a diameter not exceeding 500 mm	0
ex 8439 99 10	*10	Suction-roll shells, not drilled, in the form of alloy-steel tubes, of a length of 5 207 mm or more and an external diameter of 754 mm or more, for use in machinery for making paper or paperboard (a)	0
ex 8439 99 90	*10		

(a) Control of the use for this special purpose shall be carried out pursuant to the relevant Community provisions.